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NDS0605 P-Channel Enhancement Mode Field Effect Transistor

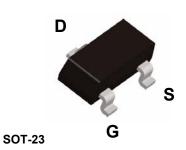
General Description

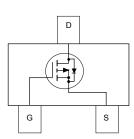
These P-Channel enhancement mode field effect transistors are produced using ON Semiconductor's proprietary, high cell density, DMOS technology. This very high density process has been designed to minimize on-state resistance, provide rugged and reliable performance and fast switching. They can be used, with a minimum of effort, in most applications requiring up to 180mA DC and can deliver current up to 1A.

This product is particularly suited to low voltage applications requiring a low current high side switch.

Features

- -0.18A, -60V. $R_{DS(ON)}$ = 5 Ω @ V_{GS} = -10 V
- Voltage controlled p-channel small signal switch
- High density cell design for low $R_{\text{DS}(\text{ON})}$
- High saturation current





Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter			Ratings	Units
V _{DSS}	Drain-Source	ce Voltage		-60	V
V _{GSS}	Gate-Sourc	irce Voltage		±20	V
ID	Drain Current – Continuous (Not		(Note 1)	-0.18	A
	– Pulsed			-1	
P _D	Maximum Power Dissipation (Note		(Note 1)	0.36	W
	Derate Above 25°C			2.9	mW/°0
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering Purposes, 1/16" from Case for 10 Seconds			300	°C
Therma	l Charac	teristics			
R _{0JA}	Thermal Resistance, Junction-to-Ambient (Note 1)		Ambient (Note 1)	350	°C/W
		g and Orderin	g Information	Topo width	Quantity
Device Marking				Tape width	Quantity
65D		NDS0605	7"	8mm	3000 units

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September-2017, Rev. 2

NDS0605

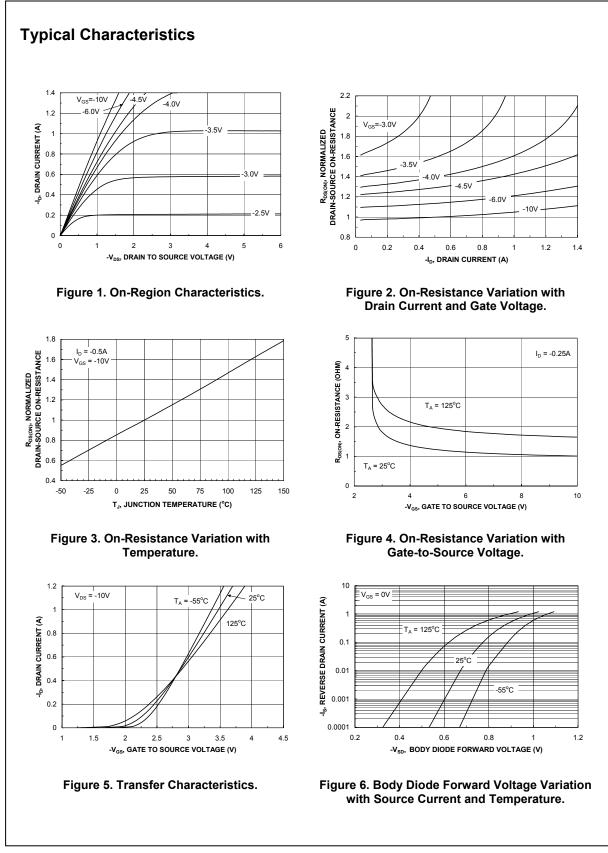
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics				11	
BV _{DSS}	Drain–Source Breakdown Voltage	$V_{GS} = 0 V$, $I_D = -10 \mu A$	-60			V
ΔBV_{DSS} ΔT_J	Breakdown Voltage Temperature Coefficient	$I_D = -10 \ \mu$ A,Referenced to 25°C		-53		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -48 V$, $V_{GS} = 0 V$			-1	μA
		V _{DS} = -48 V,V _{GS} = 0 V T _J = 125°C			-500	μA
I _{GSS}	Gate-Body Leakage.	V_{GS} = ±20 V, V_{DS} = 0 V			±100	nA
On Chara	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	-1	-1.7	-3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I_D = -250 µA,Referenced to 25°C		3		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	$V_{GS} = -10 V$, $I_D = -0.5 A$ $V_{GS} = -4.5 V$, $I_D = -0.25 A$ $V_{GS} = -10 V$, $I_D = -0.5 A$, $T_J=125^{\circ}C$		1.0 1.3 1.7	5.0 7.5 10	Ω
I _{D(on)}	On-State Drain Current	V _{GS} = -10 V, V _{DS} = -10 V	-0.6			А
g _{FS}	Forward Transconductance	$V_{DS} = -10V$, $I_{D} = -0.2 A$	0.07	0.43		S
Dynamic	Characteristics	<u>.</u>				
C _{iss}	Input Capacitance	$V_{DS} = -25 V$, $V_{GS} = 0 V$,		79		pF
C _{oss}	Output Capacitance	f = 1.0 MHz		10		pF
C _{rss}	Reverse Transfer Capacitance	-		4		pF
R _G	Gate Resistance	V _{GS} = -15 mV, f = 1.0 MHz		10		Ω
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn–On Delay Time	$V_{DD} = -25 V$, $I_D = -0.2 A$,	1	2.5	5	ns
tr	Turn–On Rise Time	$V_{GS} = -10 \text{ V}, R_{GEN} = 6 \Omega$		6.3	12.6	ns
t _{d(off)}	Turn–Off Delay Time	-		10	20	ns
t _f	Turn–Off Fall Time			7.5	15	ns
Qg	Total Gate Charge	$V_{DS} = -48 V$, $I_D = -0.5 A$,		1.8	2.5	nC
Q _{gs}	Gate-Source Charge	V _{GS} = -10 V		0.3		nC
Q _{gd}	Gate-Drain Charge			0.4		nC
Drain-So	ource Diode Characteristics	and Maximum Ratings				
Is	Maximum Continuous Drain–Source Diode Forward Current				_ 0.18	А
V _{SD}	Drain–Source Diode Forward Voltage	$V_{GS} = 0 V$, $I_{S} = -0.5 A(Note 2)$		-0.8	-1.5	V
t _{rr}	Diode Reverse Recovery Time	I _F = -0.5A		17		nS
Q _{rr}	Diode Reverse Recovery Charge	$d_{iF}/d_t = 100 \text{ A}/\mu \text{s}$ (Note 2)		15		nC

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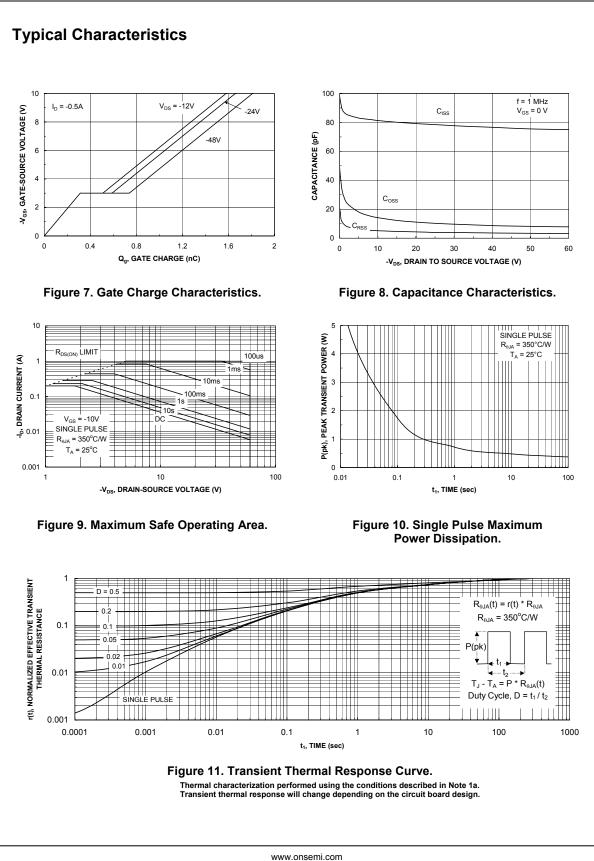
a) 350°C/W when mounted on a minimum pad..

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width $\leq 300~\mu\text{s},$ Duty Cycle $\leq 2.0\%$



NDS0605



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