IGBT with Monolithic Free **Wheeling Diode**

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications.

Features

- Extremely Efficient Trench with Fieldstop Technology
- 1350 V Breakdown Voltage
- Optimized for Low Case Temperature in IH Cooker Application
- Reliable and Cost Effective Single Die Solution
- These are Pb-Free Devices

Typical Applications

- Inductive Heating
- Consumer Appliances
- Soft Switching

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V_{CES}	1350	V
Collector current @ Tc = 25°C @ Tc = 100°C	I _c	30 15	A
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	60	Α
Diode forward current @ Tc = 25°C @ Tc = 100°C	l _F	30 15	Α
Diode pulsed current, T _{pulse} limited by T _{Jmax}	I _{FM}	60	Α
Gate-emitter voltage Transient Gate-emitter Voltage $(T_{pulse} = 5 \mu s, D < 0.10)$	V_{GE}	±20 ±25	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	357 178	W
Operating junction temperature range	TJ	-40 to +175	°C
Storage temperature range	T _{stg}	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

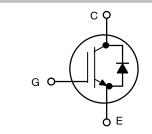
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

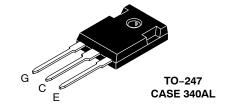


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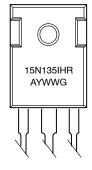
http://onsemi.com

15 A, 1350 V **V_{CEsat}** = 2.15 **V** $E_{off} = 0.42 \text{ mJ}$





MARKING DIAGRAM



= Assembly Location

= Year WW = Work Week = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTB15N135IHRWG	TO-247 (Pb-Free)	30 Units / Rail

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case	$R_{ heta JC}$	0.42	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC		•		•		
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V, I}_{C} = 500 \mu\text{A}$	V _{(BR)CES}	1350	_	_	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 15 A V _{GE} = 15 V, I _C = 15 A, T _J = 175°C	V _{CEsat}	- -	2.15 2.25	2.65 -	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 250 \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V _{GE} = 0 V, V _{CE} = 1350 V	I _{CES}	-	_	0.1	mA
Gate leakage current, collector-emitter short-circuited	V _{GE} = 20 V, V _{CE} = 0 V	I _{GES}	-	_	100	nA
DYNAMIC CHARACTERISTIC		•		•		
Input capacitance		C _{ies}	-	3560	_	pF
Output capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 10 kHz	C _{oes}	-	87	_	
Reverse transfer capacitance	1	C _{res}	-	68	_	
Gate charge total		Q_g	-	156	_	nC
Gate to emitter charge	V _{CE} = 600 V, I _C = 15 A, V _{GE} = 15 V	Q _{ge}	-	27	_	
Gate to collector charge		Q _{gc}	-	70	_	
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-off delay time	T _J = 25°C	t _{d(off)}	-	170	_	ns
Fall time	$V_{CC} = 600 \text{ V, } I_{C} = 15 \text{ A}$ $R_{a} = 10 \Omega$	t _f	-	200	_	
Turn-off switching loss	V _{GE} = 0 V/ 15V	E _{off}	-	0.42	_	mJ
Turn-off delay time	T _J = 150°C	t _{d(off)}	-	190	_	ns
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 15 \text{ A}$ $R_{c} = 10 \Omega$	t _f	-	290	-	
Turn-off switching loss	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V} / 15 \text{V}$	E _{off}	-	0.95	_	mJ
DIODE CHARACTERISTIC						
Forward voltage	$V_{GE} = 0 \text{ V}, I_F = 15 \text{ A}, T_J = 25^{\circ}\text{C}$ $V_{GE} = 0 \text{ V}, I_F = 15 \text{ A}, T_J = 175^{\circ}\text{C}$	V _F	- -	1.85 2.75	2.10 -	V

TYPICAL CHARACTERISTICS

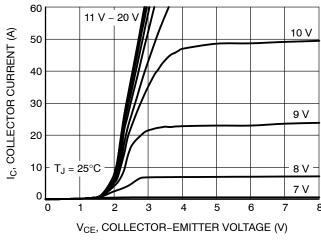


Figure 1. Output Characteristics

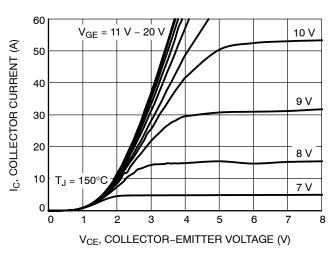


Figure 2. Output Characteristics

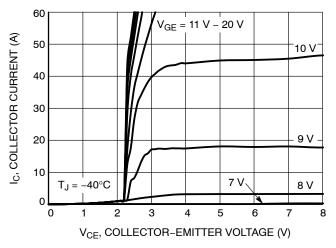


Figure 3. Output Characteristics

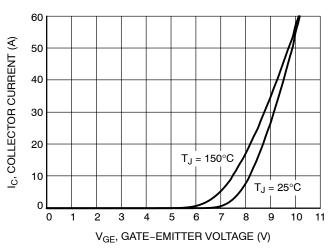


Figure 4. Typical Transfer Characteristics

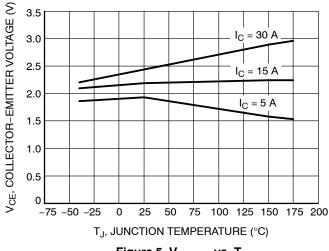


Figure 5. V_{CE(sat)} vs. T_J

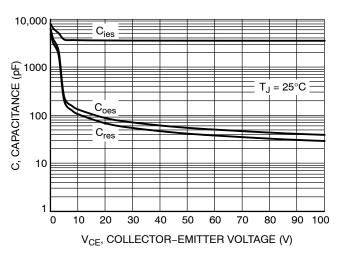
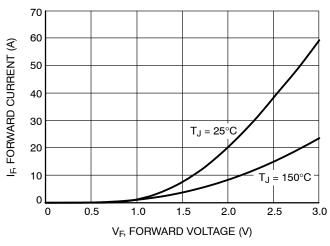


Figure 6. Typical Capacitance

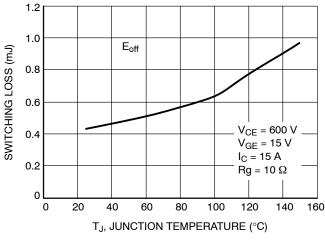
TYPICAL CHARACTERISTICS



16 V_{GE}, GATE-EMITTER VOLTAGE (V) 14 12 10 8 6 V_{CE} = 600 V $V_{GE} = 15 V$ $I_{\rm C} = 15 \, {\rm A}$ 2 20 40 60 80 100 120 140 160 180 200 0 Q_G, GATE CHARGE (nC)

Figure 7. Diode Forward Characteristics

Figure 8. Typical Gate Charge



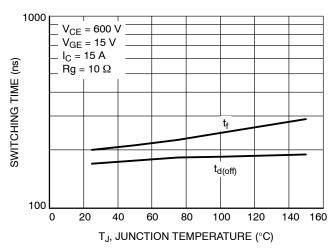
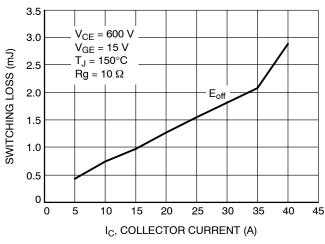


Figure 9. Switching Loss vs. Temperature

Figure 10. Switching Time vs. Temperature



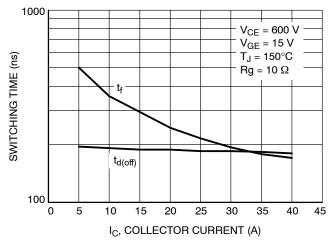
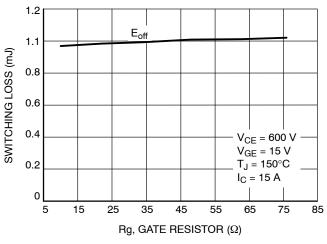


Figure 11. Switching Loss vs. I_C

Figure 12. Switching Time vs. I_C

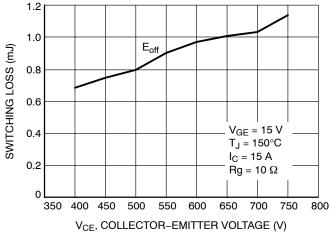
TYPICAL CHARACTERISTICS



1000 t_{d(off} SWITCHING TIME (ns) $t_{\rm f}$ V_{CE} = 600 V $V_{GE} = 15 V$ $T_{J} = 150^{\circ}C$ I_C = 15 A 100 15 25 35 45 55 65 75 5 85 Rg, GATE RESISTOR (Ω)

Figure 13. Switching Loss vs. Rg

Figure 14. Switching Time vs. Rg



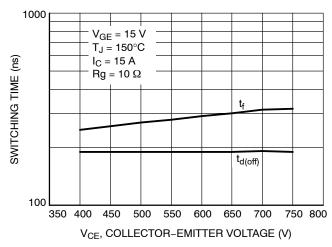
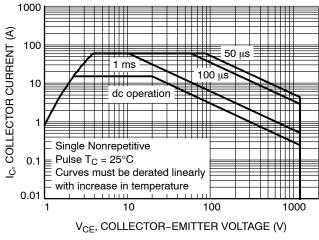


Figure 15. Switching Loss vs. V_{CE}

Figure 16. Switching Time vs. V_{CE}



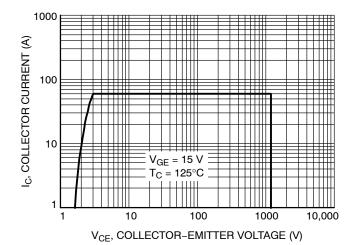


Figure 17. Safe Operating Area

Figure 18. I_C vs. V_{CE}

TYPICAL CHARACTERISTICS

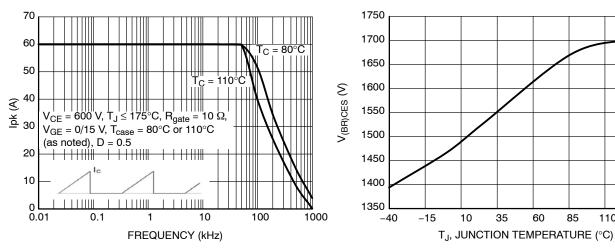


Figure 19. Collector Current vs. Switching Frequency

Figure 20. Typical V_{(BR)CES} vs. Temperature

85

110

135

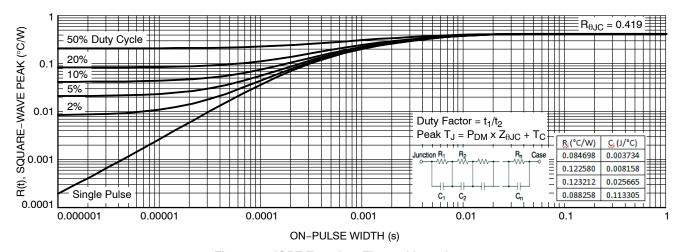


Figure 21. IGBT Transient Thermal Impedance

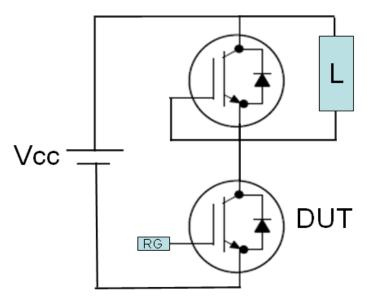


Figure 22. Test Circuit for Switching Characteristics

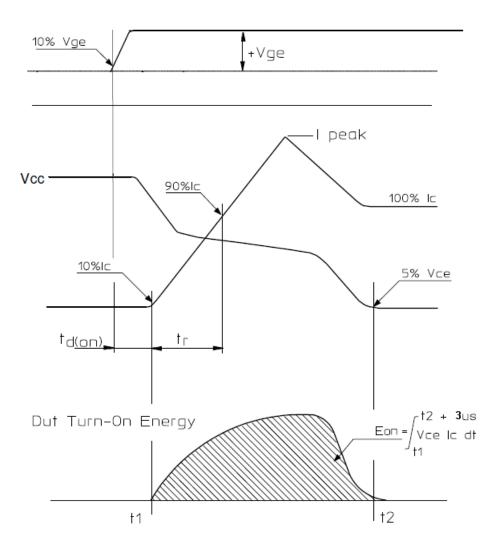


Figure 23. Definition of Turn On Waveform

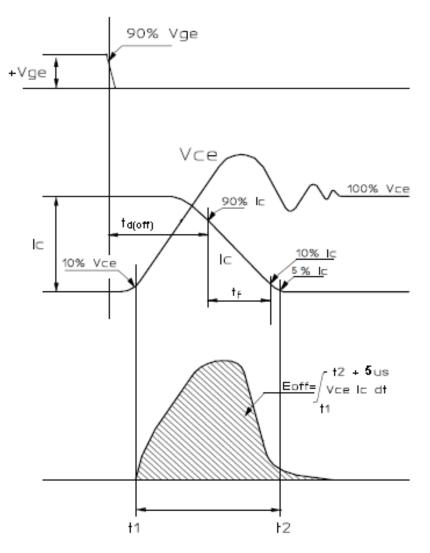
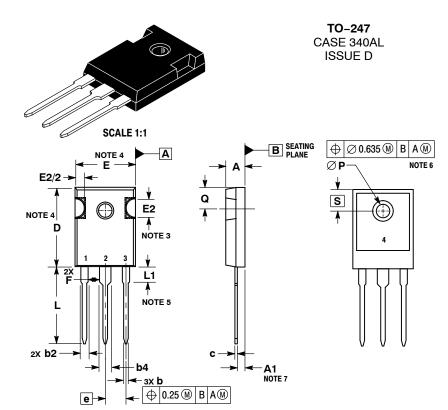


Figure 24. Definition of Turn Off Waveform



DATE 17 MAR 2017

- NOTES:

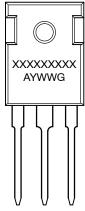
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. SLOT REQUIRED, NOTCH MAY BE ROUNDED.

 - DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH.
 MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY
- LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY
- ©P SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91.

 DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.70	5.30	
A1	2.20	2.60	
b	1.07	1.33	
b2	1.65	2.35	
b4	2.60	3.40	
С	0.45	0.68	
D	20.80	21.34	
E	15.50	16.25	
E2	4.32	5.49	
е	5.45 BSC		
F	2.655		
L	19.80	20.80	
L1	3.81	4.32	
P	3.55	3.65	
Q	5.40 6.2		
S	6.15 BSC		

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code Α = Assembly Location

Υ = Year WW = Work Week = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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