

# IGBT - Field Stop II NGTB40N120FL2WG

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop II Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. The IGBT is well suited for UPS and solar applications. Incorporated into the device is a soft and fast co–packaged free wheeling diode with a low forward voltage.

#### **Features**

- Extremely Efficient Trench with Field Stop Technology
- $T_{Jmax} = 175^{\circ}C$
- Soft Fast Reverse Recovery Diode
- Optimized for High Speed Switching
- 10 us Short Circuit Capability
- These are Pb-Free Devices

# **Typical Applications**

- Solar Inverter
- Uninterruptible Power Inverter Supplies (UPS)
- Welding

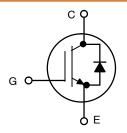
# **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter Voltage	V <sub>CES</sub>	1200	V
Collector Current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	I <sub>C</sub>	80 40	A
Pulsed Collector Current, T <sub>pulse</sub> Limited by T <sub>Jmax</sub>	I <sub>CM</sub>	200	Α
Diode Forward Current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	I <sub>F</sub>	80 40	А
Diode Pulsed Current, T <sub>pulse</sub> Limited by T <sub>Jmax</sub>	I <sub>FM</sub>	200	Α
Gate-emitter Voltage Transient Gate-emitter Voltage $(T_{pulse} = 5 \mu s, D < 0.10)$	V <sub>GE</sub>	±20 ±30	V
Power Dissipation @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	P <sub>D</sub>	535 267	W
Short Circuit Withstand Time $V_{GE} = 15 \text{ V}, V_{CE} = 500 \text{ V}, T_J \le 150^{\circ}\text{C}$	T <sub>SC</sub>	10	μs
Operating Junction Temperature Range	TJ	–55 to +175	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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35 A, 650 V V<sub>CEsat</sub> = 1.70 V E<sub>off</sub> = 0.28 mJ





#### MARKING DIAGRAM



40N120FL2 = Specific Device Code

A = Assembly Location Y = Year

WW = Work Week
G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
NGTB40N120FL2WG	TO-247 (Pb-Free)	30 Units / Rail

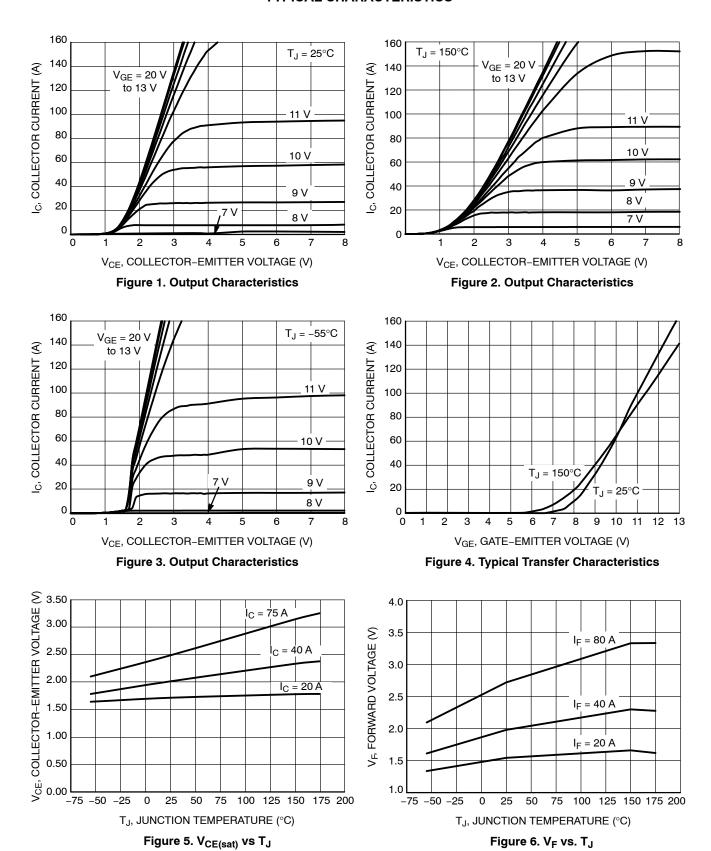
# THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{\theta JC}$	0.28	°C/W
Thermal resistance junction-to-case, for Diode	$R_{\theta JC}$	0.5	°C/W
Thermal resistance junction-to-ambient	$R_{\theta JA}$	40	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V, } I_{C} = 500  \mu\text{A}$	V <sub>(BR)CES</sub>	1200	_	-	V
Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A, T <sub>J</sub> = 175°C	V <sub>CEsat</sub>	- -	2.00 2.40	2.40	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}$ , $I_C = 400 \mu A$	V <sub>GE(th)</sub>	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J =</sub> 175°C	I <sub>CES</sub>	- -	- -	0.1 2	mA
Gate leakage current, collector-emitter short-circuited	V <sub>GE</sub> = 20 V , V <sub>CE</sub> = 0 V	I <sub>GES</sub>	-	-	200	nA
DYNAMIC CHARACTERISTIC	•					
Input capacitance		C <sub>ies</sub>	_	7385	-	pF
Output capacitance	V <sub>CE</sub> = 20 V, V <sub>GE</sub> = 0 V, f = 1 MHz	C <sub>oes</sub>	_	230	-	
Reverse transfer capacitance	1 02 1 02	C <sub>res</sub>	-	140	-	
Gate charge total		Q <sub>g</sub>	-	313	-	nC
Gate to emitter charge	$V_{CE} = 600 \text{ V}, I_{C} = 40 \text{ A}, V_{GE} = 15 \text{ V}$	Q <sub>ge</sub>	-	61	-	
Gate to collector charge	1	Q <sub>gc</sub>	-	151	-	
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-on delay time		t <sub>d(on)</sub>	_	116	-	ns
Rise time	1	t <sub>r</sub>	_	42	-	
Turn-off delay time	T <sub>J</sub> = 25°C	t <sub>d(off)</sub>	_	286	-	
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 40 \text{ A}$ $R_{q} = 10 \Omega$	t <sub>f</sub>	_	121	-	
Turn-on switching loss	$V_{GE} = 0 \text{ V/ } 15 \text{V}$	E <sub>on</sub>	_	3.4	-	mJ
Turn-off switching loss	1	E <sub>off</sub>	_	1.1	-	
Total switching loss	1	E <sub>ts</sub>	_	4.5	-	
Turn-on delay time		t <sub>d(on)</sub>	_	111	-	ns
Rise time	1	t <sub>r</sub>	_	43	-	
Turn-off delay time	T <sub>J</sub> = 175°C	t <sub>d(off)</sub>	_	304	-	
Fall time	$V_{CC} = \tilde{6}00 \text{ V, } I_{C} = 40 \text{ A}$ $R_{g} = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t <sub>f</sub>	_	260	-	
Turn-on switching loss		E <sub>on</sub>	-	4.4	-	mJ
Turn-off switching loss		E <sub>off</sub>	-	2.5	-	
Total switching loss		E <sub>ts</sub>	-	6.9	-	
DIODE CHARACTERISTIC						•
Forward voltage	$V_{GE} = 0 \text{ V, I}_{F} = 40 \text{ A}$ $V_{GE} = 0 \text{ V, I}_{F} = 50 \text{ A, T}_{J} = 175^{\circ}\text{C}$	V <sub>F</sub>	_ _	2.00 2.30	2.60 -	V
Reverse recovery time	$T_J = 25^{\circ} C$ $I_F = 40 \text{ A, V}_R = 400 \text{ V}$ $di_F/dt = 200 \text{ A/}\mu\text{s}$	t <sub>rr</sub>	-	240	-	ns
Reverse recovery charge		Q <sub>rr</sub>	-	2.5	-	μς
Reverse recovery current		I <sub>rrm</sub>	-	18	-	Α
Reverse recovery time	T <sub>J</sub> = 175°C	t <sub>rr</sub>	_	392	-	ns
Reverse recovery charge	I <sub>F</sub> = 40 A, V <sub>R</sub> = 400 V di <sub>F</sub> /dt = 200 A/μs	Q <sub>rr</sub>	-	5.36	-	μс
Reverse recovery current		I <sub>rrm</sub>	-	25.80	_	Α

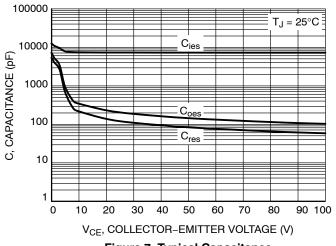
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



#### **TYPICAL CHARACTERISTICS**

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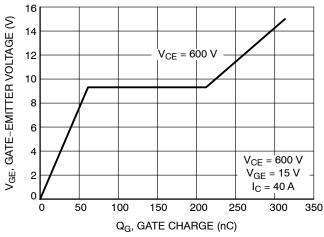
60



€  $T_J = 25^{\circ}C$ FORWARD CURRENT 50 <sub>J</sub> = 150°C 40 30 20 <u> 년</u> 10 0 0 0.5 2.5 3.0 4.0 3.5

Figure 7. Typical Capacitance

V<sub>F</sub>, FORWARD VOLTAGE (V) Figure 8. Diode Forward Characteristics



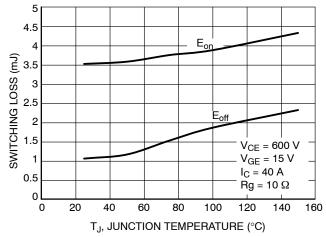
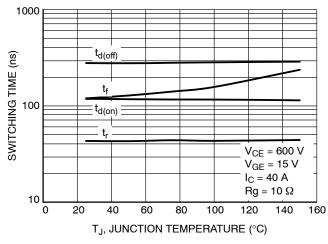


Figure 9. Typical Gate Charge

Figure 10. Switching Loss vs. Temperature



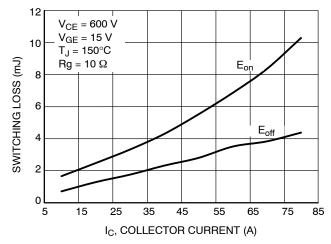


Figure 11. Switching Time vs. Temperature

Figure 12. Switching Loss vs. I<sub>C</sub>

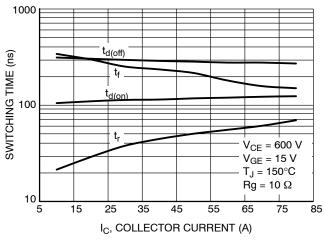


Figure 13. Switching Time vs. I<sub>C</sub>

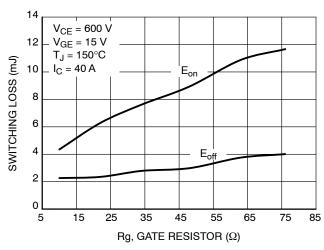


Figure 14. Switching Loss vs. Rg

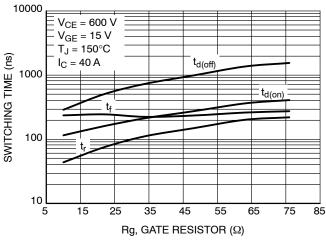


Figure 15. Switching Time vs. Rg

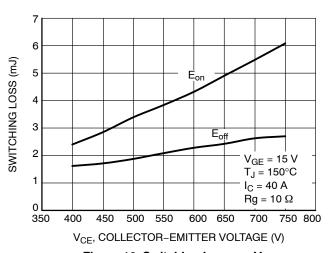


Figure 16. Switching Loss vs. V<sub>CE</sub>

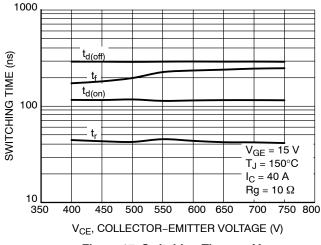


Figure 17. Switching Time vs.  $V_{CE}$ 

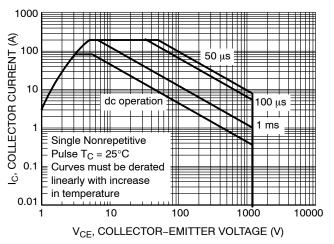


Figure 18. Safe Operating Area

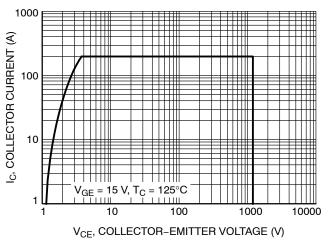


Figure 19. Reverse Bias Safe Operating Area

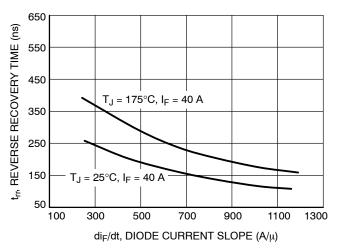


Figure 20.  $t_{rr}$  vs.  $di_F/dt$  ( $V_R = 400 \text{ V}$ )

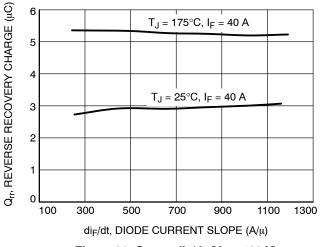


Figure 21.  $Q_{rr}$  vs.  $di_F/dt$  ( $V_R = 400 \text{ V}$ )

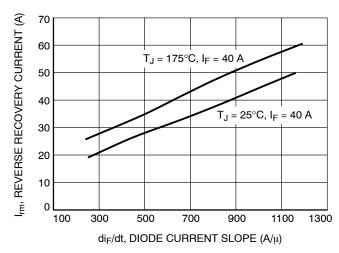


Figure 22.  $I_{rm}$  vs.  $di_F/dt$  ( $V_R = 400 \text{ V}$ )

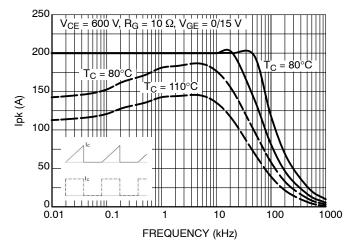


Figure 23. Collector Current vs. Switching Frequency

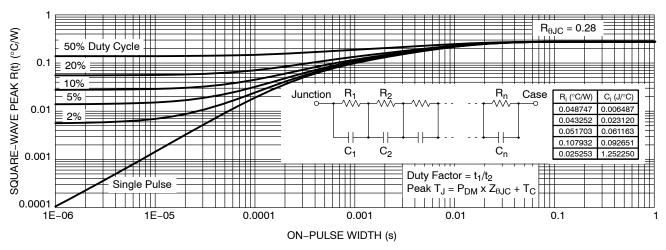


Figure 24. IGBT Transient Thermal Impedance

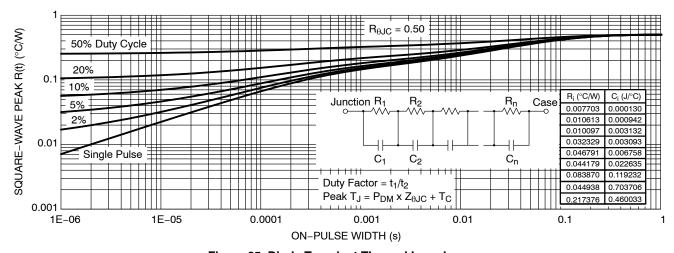


Figure 25. Diode Transient Thermal Impedance

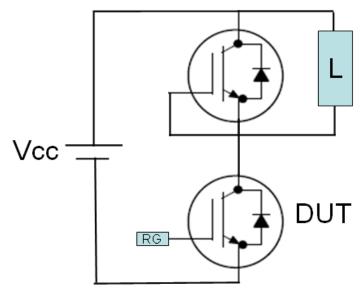


Figure 26. Test Circuit for Switching Characteristics

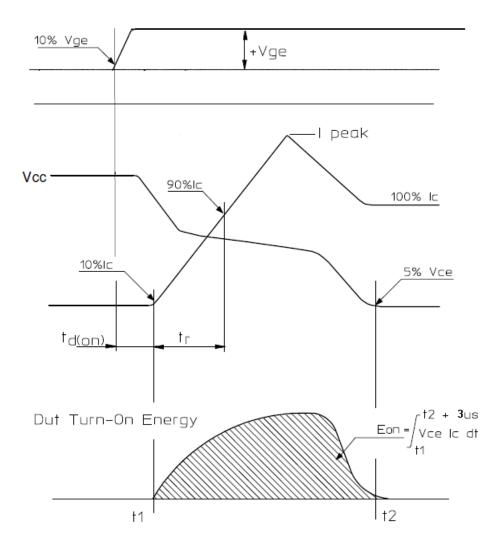


Figure 27. Definition of Turn On Waveform

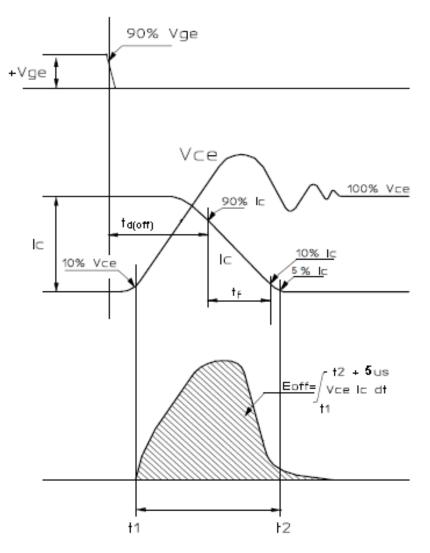
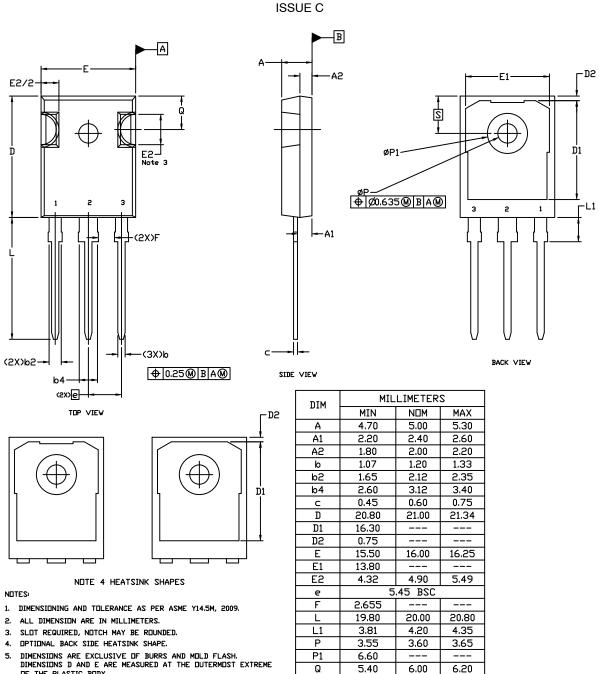


Figure 28. Definition of Turn Off Waveform

#### **PACKAGE DIMENSIONS**

TO-247 CASE 340AM



- 5. DIMENSIONS ARE EXCLUSIVE OF BURRS AND MOLD FLASH.
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  OF THE PLASTIC BODY.
- 6. DIMENSIONS AT TO BE MEASURED IN THE REGION DEFINED BY L1.
- 7. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

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 IKP20N60TXKSA1

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 IDW40E65D2FKSA1