# Switch Mode Power Rectifier

## **DPAK Surface Mount Package**

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

#### **Features**

- Ultrafast 30 Nanosecond Recovery Time
- 175°C Operating Junction Temperature
- High Voltage Capability of 600 V
- Low Forward Drop
- Low Leakage Specified @ 125°C Case Temperature
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Mechanical Characteristics**

- Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- ESD Ratings:
  - Machine Model = C > 400 V
  - Human Body Model = 3B (> 8 kV)

#### **Applications**

- Boost Rectifier for SMPS PFC Operating in Continuous Conduction Mode (CCM)
- LED Lighting Power Conversion
- Automotive Diesel Piezo Injection
- Thin and Ultra Thin Flat Panel Display
- Output Rectification in High Frequency High Output Voltage Applications



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## PLANAR ULTRAFAST RECTIFIER 6.0 AMPERES, 600 VOLTS



DPAK CASE 369C



#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NHPD660T4G	DPAK (Pb-Free)	2,500/Tape & Reel 16 mm
NRVHPD660T4G	DPAK (Pb-Free)	2,500/Tape & Reel 16 mm

- †For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
- \*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	600	V
Average Rectified Forward Current (Rated V <sub>R</sub> , T <sub>C</sub> = 145°C)	I <sub>F(AV)</sub>	6.0	А
Peak Repetitive Forward Current (Rated V <sub>R</sub> , Square Wave, 20 kHz, T <sub>C</sub> = 135°C)	I <sub>FRM</sub>	12.0	А
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, 60 Hz)	I <sub>FSM</sub>	60	А
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### THERMAL CHARACTERISTICS

Characteristics	Symbol	Value	Unit
Thermal Resistance – Junction–to–Case	$R_{ heta JC}$	4.2	°C/W
Thermal Resistance – Junction–to–Ambient (Note 1)	$R_{ heta JA}$	95.7	°C/W

<sup>1.</sup> Rating applies when surface mounted on the minimum pad sizes recommended.

### **ELECTRICAL CHARACTERISTICS**

Characteristic	Test Conditions	Symbol	Тур	Max	Unit	
Instantaneous Forward Voltage (Note 2)	$(i_F = 6 \text{ A}, T_C = 125^{\circ}\text{C})$ $(i_F = 6 \text{ A}, T_C = 25^{\circ}\text{C})$	V <sub>F</sub>	1.45 2.4	1.8 3.0	V	
Instantaneous Reverse Current (Note 2)	(Rated DC Voltage, T <sub>C</sub> = 125°C) (Rated DC Voltage, T <sub>C</sub> = 25°C)	i <sub>R</sub>	35 0.035	300 30	μА	
Reverse Recovery Time	$(I_F = 0.5 \text{ A}, I_{rr} = 0.25 \text{ A}, I_R = 1 \text{ A})$ $(I_F = 1 \text{ A}, dI_F/dt = -50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V})$	t <sub>rr</sub>	- -	30 50	ns	
Reverse Recovery Time Peak Reverse Recovery Current Total Reverse Recovery Charge Softness Factor	$(I_F = 6 \text{ A}, d_{IF}/d_t = -200 \text{ A/}\mu\text{s}, T_C = 25^{\circ}\text{C})$	t <sub>rr</sub> I <sub>RM</sub> Q <sub>rr</sub> S	30 2.3 37 2	50 3 50 -	ns A nC -	
Reverse Recovery Time Peak Reverse Recovery Current Total Reverse Recovery Charge Softness Factor	$(I_F = 6 \text{ A}, d_{IF}/d_t = -200 \text{ A/}\mu\text{s}, T_C = 125^{\circ}\text{C})$	t <sub>rr</sub> I <sub>RM</sub> Q <sub>rr</sub> S	45 5.5 150 0.35	- - - -	ns A nC -	
Forward Recovery Time Forward Voltage Time	$(I_F = 6 \text{ A}, d_{IF}/d_t = 120 \text{ A/}\mu\text{s}, T_C = 25^{\circ}\text{C})$	t <sub>fr</sub> V <sub>FP</sub>	- -	200 6	ns V	

<sup>2.</sup> Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **TYPICAL CHARACTERISTICS**

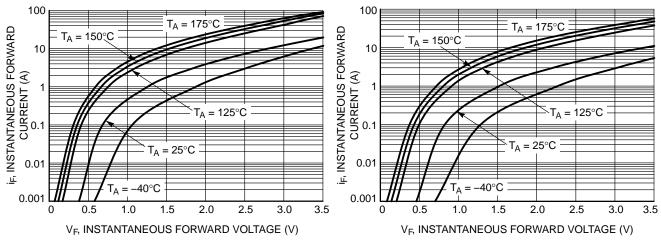


Figure 1. Typical Instantaneous Forward Characteristics

Figure 2. Maximum Instantaneous Forward Characteristics

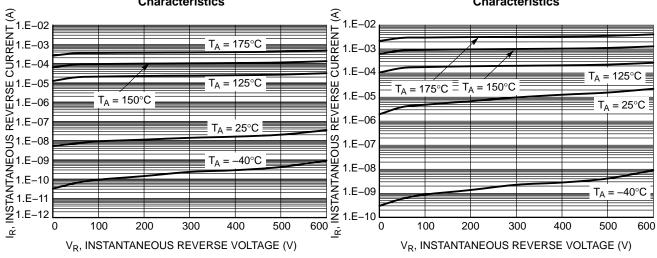


Figure 3. Typical Reverse Characteristics

Figure 4. Maximum Reverse Characteristics

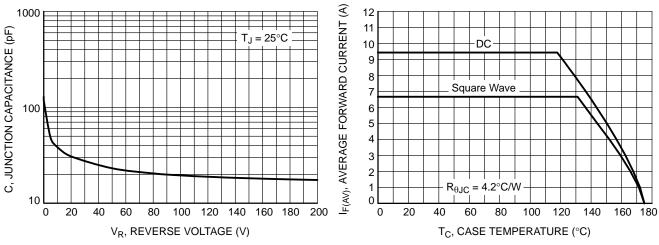


Figure 5. Typical Junction Capacitance

Figure 6. Current Derating

#### **TYPICAL CHARACTERISTICS**

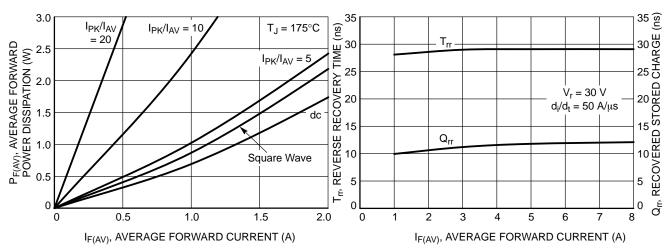


Figure 7. Forward Power Dissipation

Figure 8. Typical Recovery Characteristics

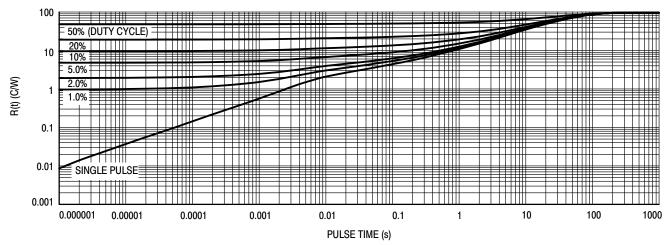


Figure 9. Thermal Response

В

NOTE 7

|  $\oplus$  | 0.005 (0.13) lacktriangledown C

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**TOP VIEW** 

L3

b2 e

L2 GAUGE

## **DPAK (SINGLE GAUGE)** CASE 369C ISSUE F SCALE 1:1 Α

DETAIL A

C SEATING

C-

SIDE VIEW

**DATE 21 JUL 2015** 

#### NOTES:

z

**BOTTOM VIEW** 

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: INCHES.
- 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DI-
- MENSIONS b3, L3 and Z.
  4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
  5. DIMENSIONS D AND E ARE DETERMINED AT THE
- OUTERMOST EXTREMES OF THE PLASTIC BODY.

  6. DATUMS A AND B ARE DETERMINED AT DATUM
- 7. OPTIONAL MOLD FEATURE.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.028	0.045	0.72	1.14
b3	0.180	0.215	4.57	5.46
С	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
E	0.250	0.265	6.35	6.73
е	0.090	BSC	2.29 BSC	
Н	0.370	0.410	9.40	10.41
L	0.055	0.070	1.40	1.78
L1	0.114 REF		2.90 REF	
L2	0.020 BSC		0.51 BSC	
L3	0.035	0.050	0.89	1.27
L4		0.040		1.01
Z	0.155		3.93	

#### ALTERNATE CONSTRUCTIONS **DETAIL A** ROTATED 90° CW **GENERIC** STYLE 1: STYLE 2: STYLE 3: STYLE 4: STYLE 5: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR PIN 1. ANODE 2. CATHODE 3. ANODE 4. CATHODE PIN 1. GATE 2. ANODE 3. CATHODE 4. ANODE PIN 1. GATE 2. DRAIN

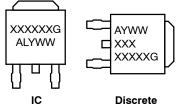
Z

**BOTTOM VIEW** 

С

3. EMITTE 4. COLLE	ER .	3. SOURCE 4. DRAIN	3. AN	ODE THODE	3. GATE 4. ANODE	3.	CATHODE ANODE
STYLE 6: PIN 1. MT1 2. MT2 3. GATE	STYLE 7: PIN 1. GATE 2. COLLE 3. EMITT	PI	'LE 8: N 1. N/C 2. CATHODE 3. ANODE		ODE THODE SISTOR ADJUS	2.	0: CATHODE ANODE CATHODE
4. MT2	<ol><li>COLLE</li></ol>	ECTOR	<ol><li>CATHODE</li></ol>	4. CA	THODE	4.	ANODE

# **MARKING DIAGRAM\***



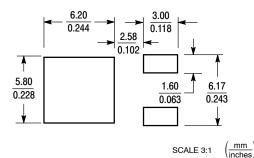
XXXXXX = Device Code = Assembly Location Α L = Wafer Lot Υ = Year WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking.

= Pb-Free Package

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## **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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