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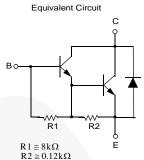
March 2016

# MJD122 NPN Silicon Darlington Transistor

#### **Features**

- D-PAK for Surface Mount Applications
- · High DC Current Gain
- Built-in a Damper Diode at E-C
- Lead Formed for Surface Mount Applications
- Electrically Similar to Popular TIP122
- Complement to MJD127





### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MJD122TF	MJD122	TO-252 3L (DPAK)	Tape and Reel

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	100	V
V <sub>CEO</sub>	Collector-Emitter Voltage	100	V
$V_{EBO}$	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current (DC)	8	Α
I <sub>CP</sub>	Collector Current (Pulse)	16	Α
I <sub>B</sub>	Base Current	120	mA
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> = 25°C)	20	W
	Collector Dissipation (T <sub>A</sub> = 25°C)	1.75	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 65 to 150	°C

### **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Test Condition	Min.	Max.	Units
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(1)</sup>	$I_C = 30 \text{ mA}, I_B = 0$	100		V
I <sub>CEO</sub>	Collector Cut-off Current	$V_{CE} = 50 \text{ V}, I_{B} = 0$		10	μΑ
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 100 \text{ V}, I_{E} = 0$		10	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 5 \text{ V}, I_{C} = 0$		2	mA
h <sub>FE</sub>	DC Current Gain <sup>(1)</sup>	V <sub>CE</sub> = 4 V, I <sub>C</sub> = 4 A V <sub>CE</sub> = 4 V, I <sub>C</sub> = 8 A	1000 100	12K	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(1)</sup>	$I_C = 4 \text{ A}, I_B = 16 \text{ mA}$ $I_C = 8 \text{ A}, I_B = 80 \text{ mA}$		2 4	V V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(1)</sup>	$I_C = 8 \text{ A}, I_B = 80 \text{ mA}$		4.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(1)</sup>	$V_{CE} = 4 \text{ V}, I_{C} = 4 \text{ A}$		2.8	V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0 f= 0.1MHz		200	pF

#### Note:

1. Pulse test: pw  $\leq 300~\mu s,~duty~cycle \leq 2\%.$ 

## **Typical Performance Characteristics**

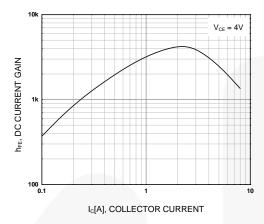


Figure 1. DC current Gain

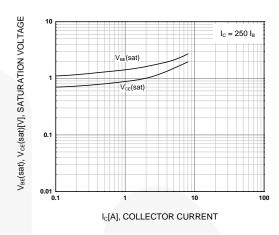


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

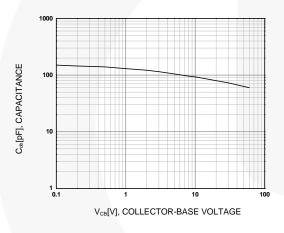


Figure 3. Collector Output Capacitance

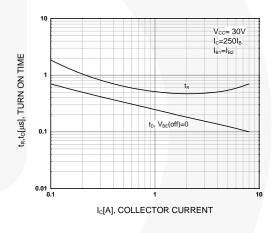


Figure 4. Turn On Time

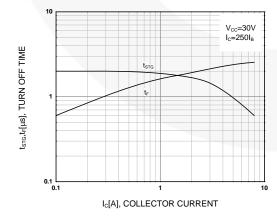


Figure 5. Turn Off Time

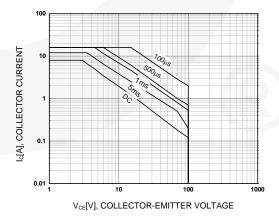


Figure 6. Safe Operating Area

# **Typical Performance Characteristics** (Continued)

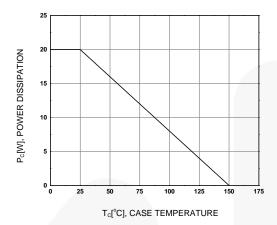
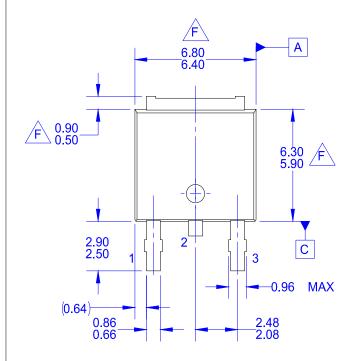
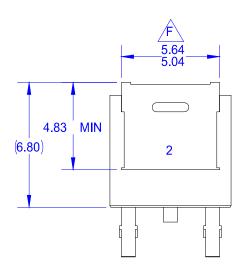
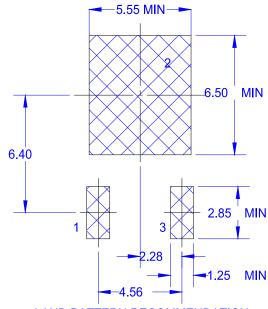


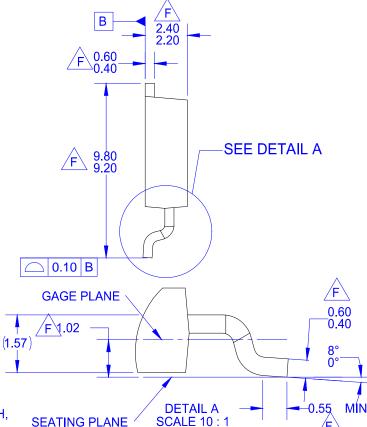
Figure 7. Power Derating Curve







LAND PATTERN RECOMMENDATION



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