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# **Single Schmitt-Trigger Inverter**

The NLU1G14 MiniGate™ is an advanced high-speed CMOS Schmitt-trigger inverter in ultra-small footprint.

The NLU1G14 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

The NLU1G14 can be used to enhance noise immunity or to square up slowly changing waveforms.

#### **Features**

- High Speed:  $t_{PD} = 4.0 \text{ ns}$  (Typ) @  $V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation:  $I_{CC} = 1 \mu A$  (Max) at  $T_A = 25^{\circ}C$
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra-Small Packages
- These are Pb-Free Devices

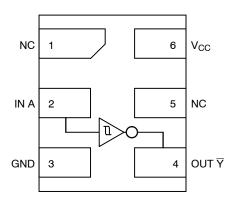


Figure 1. Pinout (Top View)

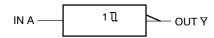


Figure 2. Logic Symbol

### **PIN ASSIGNMENT**

1	NC
2	IN A
3	GND
4	OUT ₹
5	NC
6	V <sub>CC</sub>

# **FUNCTION TABLE**

Α	Y
L	H
H	L



# ON Semiconductor®

www.onsemi.com

# **MARKING DIAGRAMS**



UDFN6 **MU SUFFIX** CASE 517AA





UDFN6 1.0 x 1.0 CASE 517BX





**UDFN6** 1.45 x 1.0 CASE 517AQ



= Device Marking = Date Code

#### ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

# **MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	DC Supply Voltage	−0.5 to +7.0	V
V <sub>IN</sub>	DC Input Voltage	-0.5 to +7.0	V
V <sub>OUT</sub>	DC Output Voltage	-0.5 to +7.0	V
I <sub>IK</sub>	DC Input Diode Current V <sub>IN</sub> < GND	-20	mA
lok	DC Output Diode Current V <sub>OUT</sub> < GND	±20	mA
I <sub>O</sub>	DC Output Source/Sink Current	±12.5	mA
I <sub>CC</sub>	DC Supply Current Per Supply Pin	±25	mA
I <sub>GND</sub>	DC Ground Current per Ground Pin	±25	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
TL	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
TJ	Junction Temperature Under Bias	150	°C
MSL	Moisture Sensitivity	Level 1	
F <sub>R</sub>	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
V <sub>ESD</sub>	ESD Withstand Voltage  Human Body Model (Note 2)  Machine Model (Note 3)  Charged Device Model (Note 4)	> 2000 > 200 N/A	V
I <sub>LATCHUP</sub>	Latchup Performance Above V <sub>CC</sub> and Below GND at 125°C (Note 5)	±500	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.

- 2. Tested to EIA / JESD22-A114-A.
- 3. Tested to EIA / JESD22-A115-A.
- 4. Tested to JESD22-C101-A.
- 5. Tested to EIA / JESD78.

# **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage	1.65	5.5	V
V <sub>IN</sub>	Digital Input Voltage	0	5.5	V
V <sub>OUT</sub>	Output Voltage	0	5.5	V
T <sub>A</sub>	Operating Free-Air Temperature	-55	+125	°C
Δt/ΔV	Input Transition Rise or Fall Rate $ \begin{array}{c} V_{CC} = 3.3 \ V \pm 0.3 \ V \\ V_{CC} = 5.0 \ V \pm 0.5 \ V \\ \end{array} $	0 0	No Limit No Limit	ns/V

# DC ELECTRICAL CHARACTERISTICS

			V <sub>CC</sub>		T <sub>A</sub> = 25 °C		<b>T</b> A = +	⊦85°C		55°C to 5°C	
Symbol	Parameter	Conditions	(V)	Min	Тур	Max	Min	Max	Min	Max	Unit
V <sub>T+</sub>	Positive Threshold Voltage		3.0 4.5 5.5	1.85 2.86 3.50	2.0 3.0 3.6	2.2 3.15 3.85		2.2 3.15 3.85		2.2 3.15 3.85	V
V <sub>T-</sub>	Negative Threshold Voltage		3.0 4.5 5.5	0.9 1.35 1.65	1.5 2.3 2.9	1.65 2.46 3.05	0.9 1.35 1.65		0.9 1.35 1.65		V
V <sub>H</sub>	Hysteresis Voltage		3.0 4.5 5.5	0.30 0.40 0.50	0.57 0.67 0.74	1.20 1.40 1.60	0.30 0.40 0.50	1.20 1.40 1.60	0.30 0.40 0.50	1.20 1.40 1.60	V
V <sub>OH</sub>	Minimum High-Level Output	$V_{IN} \le V_{T-MIN}$ $I_{OH} = -50 \mu\text{A}$	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4		1.9 2.9 4.4		V
	Voltage	$\begin{aligned} &V_{IN} \leq V_{T-MIN} \\ &I_{OH} = -4 \text{ mA} \\ &I_{OH} = -8 \text{ mA} \end{aligned}$	3.0 4.5	2.58 3.94			2.48 3.80		2.34 3.66		
V <sub>OL</sub>	Maximum Low-Level Output	$V_{IN} \ge V_{T+MAX}$ $I_{OL} = 50 \mu A$	2.0 3.0 4.5		0 0 0	0.1 0.1 0.1		0.1 0.1 0.1		0.1 0.1 0.1	V
	Voltage	$V_{IN} \ge V_{T+MAX}$ $I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$	3.0 4.5			0.36 0.36		0.44 0.44		0.52 0.52	
I <sub>IN</sub>	Input Leakage Current	$0 \le V_{IN} \le 5.5 V$	0 to 5.5			±0.1		±1.0		±1.0	μΑ
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = 5.5 V or GND	5.5			1.0		10		40	μΑ

# AC ELECTRICAL CHARACTERISTICS (Input $t_r$ = $t_f$ = 3.0 ns)

		V <sub>CC</sub>	Test		T <sub>A</sub> = 25 °	С	T <sub>A</sub> =	+85°C	T <sub>A</sub> = -5 +12	55°C to 5°C	
Symbol	Parameter	(V)	Condition	Min	Тур	Max	Min	Max	Min	Max	Unit
t <sub>PLH</sub> ,	Propagation Delay,	3.0 to	C <sub>L</sub> = 15 pF		7.0	12.8	1.0	15.0	1.0	17.0	ns
t <sub>PHL</sub>	Input A to Output ₹	3.6	C <sub>L</sub> = 50 pF		8.5	16.3	1.0	18.5	1.0	20.5	
		4.5 to	C <sub>L</sub> = 15 pF		4.0	8.6	1.0	10.0	1.0	11.5	
		5.5	C <sub>L</sub> = 50 pF		5.5	10.6	1.0	12.0	1.0	13.5	
C <sub>IN</sub>	Input Capacitance				5	10		10		10.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 6)	5.0			7.0						pF

<sup>6.</sup> C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no–load dynamic power consumption: P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

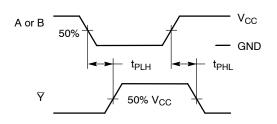
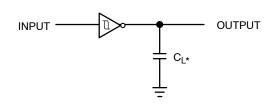


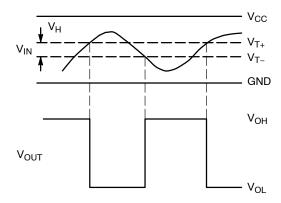
Figure 3. Switching Waveforms

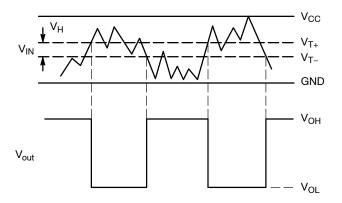


\*Includes all probe and jig capacitance.

A 1-MHz square input wave is recommended for propagation delay tests.

Figure 4. Test Circuit





(a) A Schmitt-Trigger Squares Up Inputs With Slow Rise and Fall Times

(b) A Schmitt-Trigger Offers Maximum Noise Immunity

Figure 5. Typical Schmitt-Trigger Applications

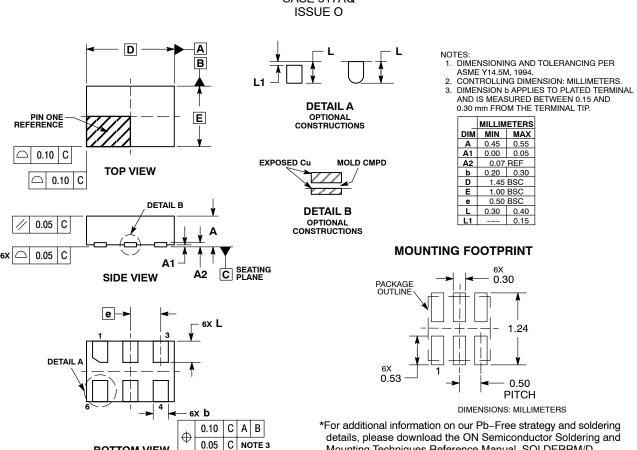
# **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>			
NLU1G14MUTCG	UDFN6, 1.2 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel			
NLU1G14AMUTCG	UDFN6, 1.45 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel			
NLU1G14CMUTCG	UDFN6, 1.0 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel			

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# **PACKAGE DIMENSIONS**

# UDFN6 1.45x1.0, 0.5P CASE 517AQ

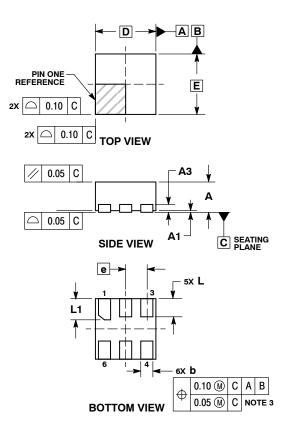


Mounting Techniques Reference Manual, SOLDERRM/D.

**BOTTOM VIEW** 

# **PACKAGE DIMENSIONS**

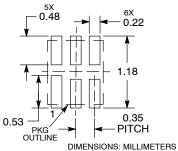
UDFN6 1.0x1.0, 0.35P CASE 517BX ISSUE O



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
   CONTROLLING DIMENSION: MILLIMETERS.
   DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
   PACKAGE DIMENSIONS EXCLUSIVE OF
- BURRS AND MOLD FLASH.

	MILLIMETERS					
DIM	MIN MAX					
Α	0.45	0.55				
A1	0.00	0.05				
А3	0.13	REF				
b	0.12	0.22				
D	1.00 BSC					
E	1.00 BSC					
е	0.35 BSC					
L	0.25 0.35					
L1	0.30 0.40					

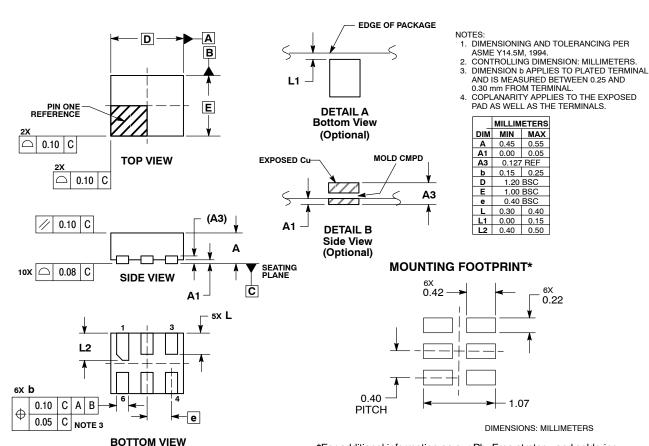
# **RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **PACKAGE DIMENSIONS**

# UDFN6, 1.2x1.0, 0.4P CASE 517AA ISSUE D



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NLX2G04CMUTCG NLX2G04AMUTCG NLU1GU04CMUTCG NLU1GT14AMUTCG NLU1G04CMUTCG NL17SZU04P5T5G
74LVC06ADTR2G 74LVC04ADR2G NLV37WZ04USG NLX3G14FMUTCG NL17SZ04P5T5G NLV17SG14DFT2G 74ACT14SC
BU4069UBF-E2 EMPP008Z NC7WZ14P6X NLV14106BDTR2G NLV74AC14DTR2G SN74HCT04DE4 ODE-3-120023-1F12
74VHCT04AM SV004IE5-1C TC74HC04APF TC7SH04F,LJ(CT TC7W14FK,LF 74VHC14MTCX 74LCX14MTC
SN74LVC1GU04DBVR NLU1G14BMX1TCG NLU2G04AMX1TCG NLU2G14AMX1TCG NLU3G14AMX1TCG NLVVHC1G04DFT2G
NLX2G04CMX1TCG NLX3G14AMX1TCG 74HC14T14-13