**ON Semiconductor** 

Is Now

# Onsemi

To learn more about onsemi<sup>™</sup>, please visit our website at <u>www.onsemi.com</u>

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product factures, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and asfety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or by customer's technical experts. onsemi products and actal performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application, Buyer shall indemnify and hold onsemi and its officers, employees, subsidiari

# Dual Non-Inverting Schmitt-Trigger Buffer

The NLU2G17 MiniGate<sup>™</sup> is an advanced high–speed CMOS dual non–inverting Schmitt–trigger buffer in ultra–small footprint.

The NLU2G17 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

The NLU2G17 can be used to enhance noise immunity or to square up slowly changing waveforms.

#### Features

- High Speed:  $t_{PD} = 4.0 \text{ ns} (Typ) @ V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation:  $I_{CC} = 1 \ \mu A$  (Max) at  $T_A = 25^{\circ}C$
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra-Small Packages
- These are Pb–Free Devices

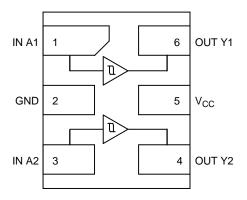


Figure 1. Pinout (Top View)

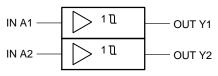


Figure 2. Logic Symbol

**PIN ASSIGNMENT** 

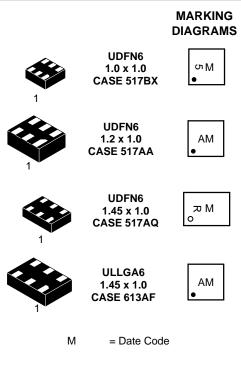
1	IN A1
2	GND
3	IN A2
4	OUT Y2
5	V <sub>CC</sub>
6	OUT Y1

FUNCTION TABLE			
Α	Y		
L H	L H		



## **ON Semiconductor®**

www.onsemi.com



#### ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

#### MAXIMUM RATINGS

Symbol	Paramet	Value	Unit	
V <sub>CC</sub>	DC Supply Voltage	-0.5 to +7.0	V	
V <sub>IN</sub>	DC Input Voltage		-0.5 to +7.0	V
V <sub>OUT</sub>	DC Output Voltage		-0.5 to +7.0	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < GND	-20	mA
I <sub>OK</sub>	DC Output Diode Current	V <sub>OUT</sub> < GND	±20	mA
Ι <sub>Ο</sub>	DC Output Source/Sink Current	±12.5	mA	
I <sub>CC</sub>	DC Supply Current Per Supply Pin	±25	mA	
I <sub>GND</sub>	DC Ground Current per Ground Pin	±25	mA	
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C
ΤL	Lead Temperature, 1 mm from Case for 10 S	econds	260	°C
TJ	Junction Temperature Under Bias	150	°C	
MSL	Moisture Sensitivity		Level 1	
F <sub>R</sub>	Flammability Rating Oxygen	Index: 28 to 34	UL 94 V–0 @ 0.125 in	
I <sub>LATCHUP</sub>	Latchup Performance Above $V_{CC}$ and Below	±500	mA	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.
Tested to EIA / JESD78.

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage	1.65	5.5	V
V <sub>IN</sub>	Digital Input Voltage	0	5.5	V
V <sub>OUT</sub>	Output Voltage	0	5.5	V
T <sub>A</sub>	Operating Free–Air Temperature	-55	+125	°C
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate $V_{CC}$ = 3.3 V ± 0.3 V $V_{CC}$ = 5.0 V ± 0.5 V	0 0	No Limit No Limit	ns/V

#### DC ELECTRICAL CHARACTERISTICS

			V <sub>CC</sub>		T <sub>A</sub> = 25 °C	;	<b>T</b> <sub>A</sub> = -	⊦85°C		55°C to 25°C	
Symbol	Parameter	Conditions	(V)	Min	Тур	Max	Min	Max	Min	Max	Unit
V <sub>T+</sub>	Positive Threshold Voltage		3.0 4.5 5.5		2.0 3.0 3.6	2.2 3.15 3.85		2.2 3.15 3.85		2.2 3.15 3.85	V
V <sub>T-</sub>	Negative Threshold Voltage		3.0 4.5 5.5	0.9 1.35 1.65	1.5 2.3 2.9	1.65 2.46 3.05	0.9 1.35 1.65		0.9 1.35 1.65		V
V <sub>H</sub>	Hysteresis Voltage		3.0 4.5 5.5	0.30 0.40 0.50	0.57 0.67 0.74	1.20 1.40 1.60	0.30 0.40 0.50	1.20 1.40 1.60	0.30 0.40 0.50	1.20 1.40 1.60	V
V <sub>OH</sub>	Minimum High–Level Output	$V_{IN} \ge V_{T+MAX}$ $I_{OH} = -50 \mu A$	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4		1.9 2.9 4.4		V
	Voltage	$\begin{array}{l} V_{IN} \geq V_{T+MAX} \\ I_{OH} = -4 \text{ mA} \\ I_{OH} = -8 \text{ mA} \end{array}$	3.0 4.5	2.58 3.94			2.48 3.80		2.34 3.66		
V <sub>OL</sub>	Maximum Low–Level Output Voltage	$V_{IN} \le V_{T-MIN}$ $I_{OL} = 50 \ \mu A$	2.0 3.0 4.5		0 0 0	0.1 0.1 0.1		0.1 0.1 0.1		0.1 0.1 0.1	V
	vollage	$V_{IN} \le V_{T-MIN}$ $I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$	3.0 4.5			0.36 0.36		0.44 0.44		0.52 0.52	
I <sub>IN</sub>	Input Leakage Current	$0 \le V_{IN} \le 5.5 V$	0 to 5.5			±0.1		±1.0		±1.0	μΑ
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = 5.5 V or GND	5.5			1.0		10		40	μΑ

#### AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0 \text{ ns}$ )

		V <sub>CC</sub>	Test		T <sub>A</sub> = 25 °	с	T <sub>A</sub> =	+85°C	T <sub>A</sub> = -5 +12	55°C to 5°C	
Symbol	Parameter	(V)	Condition	Min	Тур	Max	Min	Max	Min	Max	Unit
t <sub>PLH</sub> ,	Propagation Delay,	3.0 to	C <sub>L</sub> = 15 pF		7.0	12.8	1.0	15	1.0	17	ns
t <sub>PHL</sub>	Input Ă to Output Ÿ	3.6	C <sub>L</sub> = 50 pF		8.5	16.3	1.0	18.5	1.0	20.5	
		4.5 to	C <sub>L</sub> = 15 pF		4.0	8.6	1.0	10	1.0	11.5	
		5.5	C <sub>L</sub> = 50 pF		5.5	10.6	1.0	12	1.0	13.5	
C <sub>IN</sub>	Input Capacitance				5.0	10		10		10	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 3)	5.0			7.0						pF

3.  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation  $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$ .  $C_{PD}$  is used to determine the no-load dynamic power consumption:  $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$ .

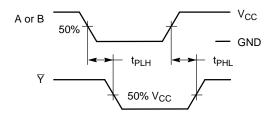
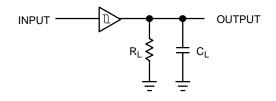
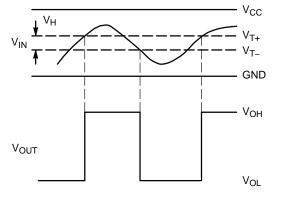


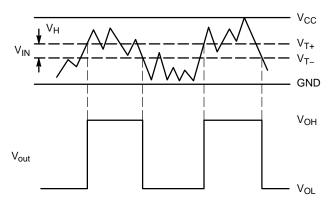
Figure 3. Switching Waveforms



A 1-MHz square input wave is recommended for propagation delay tests.

#### Figure 4. Test Circuit





(a) A Schmitt-Trigger Squares Up Inputs With Slow Rise and Fall Times

(b) A Schmitt–Trigger Offers Maximum Noise Immunity

#### Figure 5. Typical Schmitt-Trigger Applications

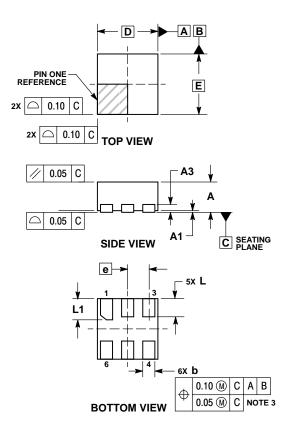
#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>		
NLU2G17MUTCG	UDFN6, 1.2 x 1.0, 0.4P (Pb–Free)	3000 / Tape & Reel		
NLU2G17AMUTCG	UDFN6, 1.45 x 1.0, 0.5P (Pb–Free)	3000 / Tape & Reel		
NLU2G17CMUTCG	UDFN6, 1.0 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel		
NLU2G17AMX1TCG	ULLGA6, 1.45 x 1.0, 0.5P (Pb–Free)	3000 / Tape & Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

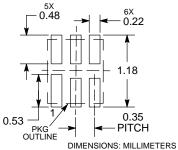
UDFN6 1.0x1.0, 0.35P CASE 517BX ISSUE O



- NOTES:
   DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
   CONTROLLING DIMENSION: MILLIMETERS.
   DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
   PACKAGE DIMENSIONS EXCLUSIVE OF BUIDES AND MOLD EI ASH ASH.

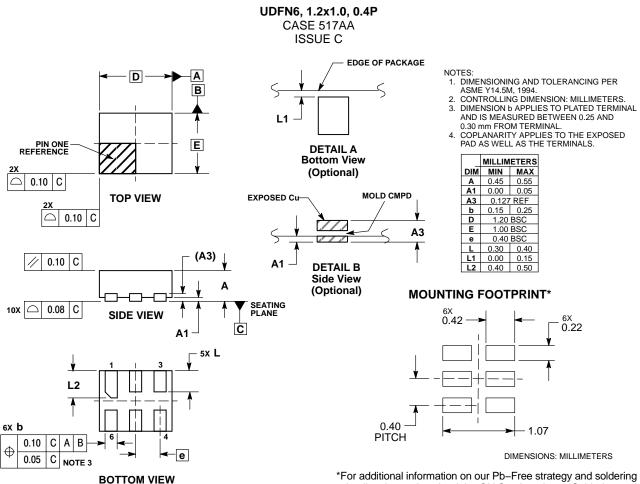
BURRS AND MOLD FL					
	MILLIMETERS				
DIM	MIN	MAX			
Α	0.45	0.55			
A1	0.00	0.05			
A3	0.13 REF				
b	0.12 0.22				
D	1.00	BSC			
E	1.00	BSC			
е	0.35	0.35 BSC			
L	0.25	0.35			
L1	0.30	0.40			

# RECOMMENDED SOLDERING FOOTPRINT\*



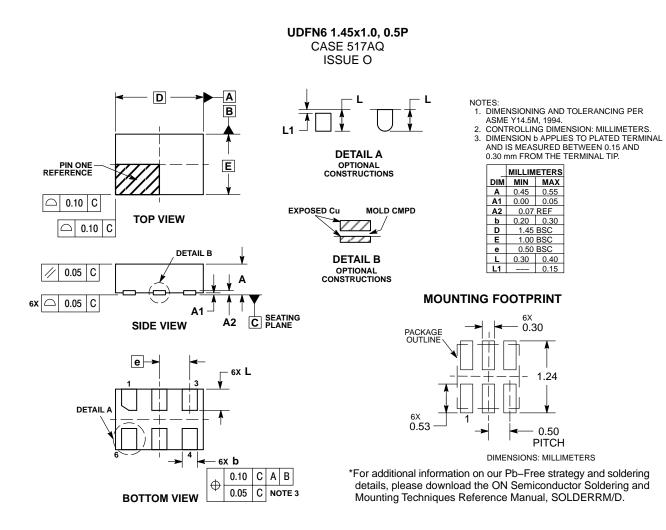
\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### PACKAGE DIMENSIONS



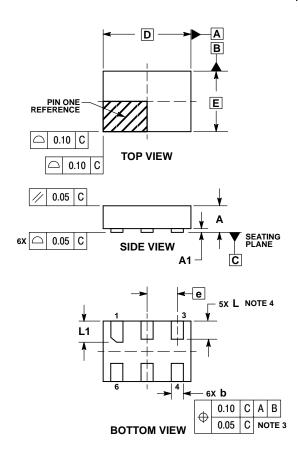
\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### PACKAGE DIMENSIONS



#### PACKAGE DIMENSIONS

ULLGA6 1.45x1.0, 0.5P CASE 613AF **ISSUE A** 

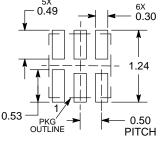


NOTES DIMENSIONING AND TOLERANCING PER

- ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. 3.
- DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP. A MAXIMUM OF 0.05 PULL BACK OF THE
- PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

	MILLIMETERS				
DIM	MIN	MAX			
Α		0.40			
A1	0.00	0.05			
b	0.15	0.25			
D	1.45 BSC				
Е	1.00	BSC			
е	0.50	BSC			
L	0.25	0.35			
L1	0.30	0.40			

#### **MOUNTING FOOTPRINT** SOLDERMASK DEFINED\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MiniGate is a trademark of Semiconductor Components Industries, LLC (SCILLC).

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harnless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

## **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Buffers & Line Drivers category:

Click to view products by ON Semiconductor manufacturer:

Other Similar products are found below :

LXV200-024SW 74AUP2G34FW3-7 HEF4043BP PI74FCT3244L MC74HCT365ADTR2G Le87401NQC Le87402MQC 028192B 042140C 051117G 070519XB NL17SZ07P5T5G NLU1GT126AMUTCG 74AUP1G17FW5-7 74LVC2G17FW4-7 CD4502BE 5962-8982101PA 5962-9052201PA 74LVC1G125FW4-7 NL17SH17P5T5G NL17SH125P5T5G NLV37WZ07USG RHRXH162244K1 74AUP1G34FW5-7 74AUP1G07FW5-7 74LVC2G126RA3-7 NLX2G17CMUTCG 74LVCE1G125FZ4-7 Le87501NQC 74AUP1G126FW5-7 TC74HC4050AP(F) 74LVCE1G07FZ4-7 NLX3G16DMUTCG NLX2G06AMUTCG NLVVHC1G50DFT2G NLU2G17AMUTCG LE87100NQC LE87290YQC LE87290YQCT LE87511NQC LE87511NQCT LE87557NQC LE87557NQCT LE87614MQC LE87614MQCT 74AUP1G125FW5-7 NLU2G16CMUTCG MC74LCX244MN2TWG NLV74VHC125DTR2G NL17SG126DFT2G