NLSV1T244

1-Bit Dual-Supply Non-Inverting Level Translator

The NLSV1T244 is a 1-bit configurable dual-supply voltage level translator. The input A_n and output B_n ports are designed to track two different power supply rails, V_{CCA} and V_{CCB} respectively. Both supply rails are configurable from 0.9 V to 4.5 V allowing universal low-voltage translation from the input A_n to the output B_n port.

Features

- Wide V_{CCA} and V_{CCB} Operating Range: 0.9 V to 4.5 V
- High-Speed w/ Balanced Propagation Delay
- Inputs and Outputs have OVT Protection to 4.5 V
- Non-preferential V_{CCA} and V_{CCB} Sequencing
- Outputs at 3–State until Active V_{CC} is Reached
- Power–Off Protection
- Outputs Switch to 3-State with V_{CCB} at GND
- Ultra–Small Packaging: 1.2 mm x 1.0 mm UDFN6
- NLVSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q100 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

• Mobile Phones, PDAs, Other Portable Devices

Important Information

• ESD Protection for All Pins: HBM (Human Body Model) > 3000 V



Figure 1. Logic Diagram



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ORDERING INFORMATION

Device	Package	Shipping [†]
NLSV1T244MUTBG,	UDFN6	3000 / Tape &
NLVSV1T244MUTBG	(Pb-Free)	Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

PIN ASSIGNMENT

PIN	FUNCTION
V _{CCA}	Input Port DC Power Supply
V _{CCB}	Output Port DC Power Supply
GND	Ground
A	Input Port
В	Output Port
ŌE	Output Enable

TRUTH TABLE

In	Inputs			
ŌĒ	A	В		
L	L	L		
L	н	н		
н	х	3–State		

MAXIMUM RATINGS

Symbol	Rating		Value	Condition	Unit
V _{CCA} , V _{CCB}	DC Supply Voltage		-0.5 to +5.5		V
VI	DC Input Voltage	А	-0.5 to +5.5		V
V _C	Control Input	OE	-0.5 to +5.5		V
V _O	DC Output Voltage (Power Down)	В	-0.5 to +5.5	$V_{CCA} = V_{CCB} = 0$	V
	(Active Mode)	В	-0.5 to +5.5		V
	(Tri-State Mode)	В	-0.5 to +5.5		V
I _{IK}	DC Input Diode Current		-20	V _I < GND	mA
I _{OK}	DC Output Diode Current		-50	V _O < GND	mA
Ι _Ο	DC Output Source/Sink Current		±50		mA
I _{CCA} , I _{CCB}	DC Supply Current Per Supply Pin		±100		mA
I _{GND}	DC Ground Current per Ground Pin		±100		mA
T _{STG}	Storage Temperature		-65 to +150		°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit	
V _{CCA} , V _{CCB}	Positive DC Supply Voltage		0.9	4.5	V
VI	Bus Input Voltage		GND	4.5	V
V _C	Control Input	ŌĒ	GND	4.5	V
V _{IO}	Bus Output Voltage (Power Down Mode)	В	GND	4.5	V
	(Active Mode)	В	GND	V _{CCB}	V
	(Tri-State Mode)	В	GND	4.5	V
T _A	Operating Temperature Range		-40	+85	°C
$\Delta t / \Delta V$	Input Transition Rise or Rate V _I , from 30% to 70% of V _{CC} ; V _{CC} = 3.3 V \pm 0.3 V		0	10	nS

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

					-40°C te	o +85°C	
Symbol	Parameter	Test Conditions	V _{CCA} (V)	V _{CCB} (V)	Min	Max	Unit
V _{IH}	Input HIGH Voltage		3.6 – 4.5	0.9 – 4.5	2.2	-	V
	(A, OE)		2.7 – 3.6		2.0	-	
			2.3 – 2.7		1.6	-	
			1.4 – 2.3		0.65 * V _{CCA}	-	
			0.9 – 1.4		0.9 * V _{CCA}	-	
V _{IL}	Input LOW Voltage		3.6 - 4.5	0.9 – 4.5	-	0.8	V
	(A, OE)		2.7 – 3.6		-	0.8	
			2.3 – 2.7	1	_	0.7	
			1.4 – 2.3		_	0.35 * V _{CCA}	
			0.9 – 1.4	1	_	0.1 * V _{CCA}	
V _{OH}	Output HIGH Voltage	$I_{OH} = -100 \ \mu A; \ V_I = V_{IH}$	0.9 - 4.5	0.9 – 4.5	V _{CCB} - 0.2	-	V
		I_{OH} = -0.5 mA; V_I = V_{IH}	0.9	0.9	0.75 * V _{CCB}	-	
		$I_{OH} = -2 \text{ mA}; V_I = V_{IH}$	1.4	1.4	1.05	-	
		$I_{OH} = -6 \text{ mA}; V_I = V_{IH}$	1.65	1.65	1.25	-	
			2.3	2.3	2.0	-	
		$I_{OH} = -12 \text{ mA}; V_I = V_{IH}$	2.3	2.3	1.8	-	
			2.7	2.7	2.2	-	
		$I_{OH} = -18 \text{ mA}; V_I = V_{IH}$	2.3	2.3	1.7	-	
			3.0	3.0	2.4	-	
		$I_{OH} = -24 \text{ mA}; V_I = V_{IH}$	3.0	3.0	2.2	-	
V _{OL}	Output LOW Voltage	I_{OL} = 100 μ A; V _I = V _{IL}	0.9 - 4.5	0.9 – 4.5	_	0.2	V
		$I_{OL} = 0.5 \text{ mA}; V_I = V_{IL}$	1.1	1.1	_	0.3	
		$I_{OL} = 2 \text{ mA}; V_I = V_{IL}$	1.4	1.4	-	0.35	
		$I_{OL} = 6 \text{ mA}; V_I = V_{IL}$	1.65	1.65	_	0.3	
		$I_{OL} = 12 \text{ mA}; V_I = V_{IL}$	2.3	2.3	-	0.4	
			2.7	2.7	-	0.4	
		I_{OL} = 18 mA; $V_I = V_{IL}$	2.3	2.3	_	0.6	
			3.0	3.0	-	0.4	
		I_{OL} = 24 mA; V_I = V_{IL}	3.0	3.0	_	0.55	
lı	Input Leakage Current	$V_I = V_{CCA}$ or GND	0.9 – 4.5	0.9 – 4.5	-1.0	1.0	μΑ
I _{OFF}	Power–Off Leakage Current	<u>OE</u> = 0 V	0 0.9 – 4.5	0.9 – 4.5 0	-1.0 -1.0	1.0 1.0	μA
I _{CCA}	Quiescent Supply Current		0.9 – 4.5	0.9 – 4.5	-	1.0	μΑ
I _{CCB}	Quiescent Supply Current		0.9 – 4.5	0.9 – 4.5	-	1.0	μΑ
I _{CCA} + I _{CCB}	Quiescent Supply Current	$\label{eq:VI} \begin{array}{l} V_{I} = V_{CCA} \text{ or } GND; \\ I_{O} = 0, V_{CCA} = V_{CCB} \end{array}$	0.9 – 4.5	0.9 – 4.5	-	2.0	μΑ
ΔI _{CCA}	Increase in I_{CC} per Input Voltage, Other Inputs at V_{CCA} or GND	$V_{I} = V_{CCA} - 0.6 V;$ $V_{I} = V_{CCA}$ or GND	4.5 3.6	4.5 3.6	-	10 5.0	μΑ
ΔI_{CCB}	Increase in I_{CC} per Input Voltage, Other Inputs at $V_{CCA} \mbox{ or GND}$	$V_I = V_{CCA} - 0.6 V;$ $V_I = V_{CCA}$ or GND	4.5 3.6	4.5 3.6	-	10 5.0	μΑ
I _{OZ}	I/O Tri–State Output Leakage Current	$T_A = 25^{\circ}C, \overline{OE} = 0 V$	0.9 – 4.5	0.9 – 4.5	-1.0	1.0	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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TOTAL STATIC POWER CONSUMPTION (I_{CCA} + I_{CCB})

	–40°C to +85°C										
	V _{CCB} (V)										
	4	.5	3	.3	2	.8	1	.8	0	.9	
V _{CCA} (V)	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Unit
4.5		2		2		2		2		< 1.5	μΑ
3.3		2		2		2		2		< 1.5	μΑ
2.8		< 2		< 1		< 1		< 0.5		< 0.5	μΑ
1.8		< 1		< 1		< 0.5		< 0.5		< 0.5	μΑ
0.9		< 0.5		< 0.5		< 0.5		< 0.5		< 0.5	μA

NOTE: Connect ground before applying supply voltage V_{CCA} or V_{CCB}. This device is designed with the feature that the power–up sequence of V_{CCA} and V_{CCB} will not damage the IC.

AC ELECTRICAL CHARACTERISTICS

			-40°C to +85°C										
			V _{CCB} (V)										
			4	.5	3	.3	2	.8	1	.8	1	.2	
Symbol	Parameter	V _{CCA} (V)	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Unit
t _{PLH} ,	Propagation	4.5		1.6		1.8		2.0		2.1		2.3	nS
t _{PHL} (Note 1)	Delay,	3.3		1.7		1.9		2.1		2.3		2.6	
(11010-1)	A to B	2.8		1.9		2.1		2.3		2.5		2.8	
		1.8		2.1		2.4		2.5		2.7		3.0	
		1.2		2.4		2.7		2.8		3.0		3.3	
t _{PZH} ,	Output	4.5		2.6		3.8		4.0		4.1		4.3	nS
t _{PZL}	Enable,	3.3		3.7		3.9		4.1		4.3		4.6	
(NOLE T)	O to B	2.5		3.9		4.1		4.3		4.5		4.8	
		1.8		4.1		4.4		4.5		4.7		5.0	
		1.2		4.4		4.7		4.8		5.0		5.3	
t _{PHZ} ,	Output	4.5		2.6		3.8		4.0		4.1		4.3	nS
t _{PLZ}	Disable,	3.3		3.7		3.9		4.1		4.3		4.6	
(NOLE I)	OE to B	2.5		3.9		4.1		4.3		4.5		4.8	
		1.8		4.1		4.4		4.5		4.7		5.0	
		1.2		4.4		4.7		4.8		5.0		5.3	
t _{OSHL} ,	Output to	4.5		0.15		0.15		0.15		0.15		0.15	nS
t _{OSLH}	Output Skew,	3.3		0.15		0.15		0.15		0.15		0.15	
(Note 1) Tim	Tim	2.5		0.15		0.15		0.15		0.15		0.15	
		1.8		0.15		0.15		0.15		0.15		0.15	
		1.2		0.15		0.15		0.15		0.15		0.15	

1. Propagation delays defined per Figure 2.

CAPACITANCE

Symbol	Parameter	Test Conditions	Typ (Note 2)	Unit
C _{IN}	Control Pin Input Capacitance	$V_{CCA} = V_{CCB} = 3.3 \text{ V}, V_I = 0 \text{ V or } V_{CCA/B}$	3.5	pF
C _{I/O}	I/O Pin Input Capacitance	V_{CCA} = V_{CCB} = 3.3 V, V_{I} = 0 V or $V_{CCA/B}$	5.0	pF
C _{PD}	Power Dissipation Capacitance	V_{CCA} = V_{CCB} = 3.3 V, V_{I} = 0 V or V_{CCA},f = 10 MHz	5.0	pF

2. Typical values are at $T_A = +25^{\circ}C$. 3. C_{PD} is defined as the value of the IC's equivalent capacitance from which the operating current can be calculated from: $I_{CC(operating)} \cong C_{PD} \times V_{CC} \times f_{IN}$ where $I_{CC} = I_{CCA} + I_{CCB}$.

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Figure 2. AC (Propagation Delay) Test Circuit

Test	Switch
t _{PLH} , t _{PHL}	OPEN
t _{PLZ} , t _{PZL}	V _{CCO} x 2
t _{PHZ} , t _{PZH}	GND
C_L = 15 pF or equivalent (include R_L = 2 k Ω or equivalent Z_{OUT} of pulse generator = 50 Ω	es probe and jig capacitance)



 $\label{eq:waveform 1 - Propagation Delays} \begin{matrix} t_{R} = t_{F} = 2.0 \text{ ns}, 10\% \text{ to } 90\%; f = 1 \text{ MHz}; t_{W} = 500 \text{ ns} \end{matrix}$



Waveform 2 – Output Enable and Disable Times t_R = t_F = 2.0 ns, 10% to 90%; f = 1 MHz; t_W = 500 ns

Figure 3. A	AC (Propagation	Delay) Test Ci	rcuit Waveforms
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	V _{cc}							
Symbol	3.0 V – 4.5 V	2.3 V – 2.7 V	1.65 V – 1.95 V	1.4 V – 1.6 V	0.9 V – 1.3 V			
V _{mA}	V _{CCA} /2							
V _{mB}	V _{CCB} /2							
V _X	V _{OL} x 0.1							
VY	V _{OH} x 0.9							





DIMENSIONS: MILLIMETERS

1.07

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PITCH

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