ON Semiconductor

Is Now



To learn more about onsemi™, please visit our website at www.onsemi.com

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,

Dual Bilateral Analog Switch / Digital Multiplexer

The NLX2G66 is a dual single pole, single throw (SPST) analog switch / digital multiplexer. This single supply voltage IC is designed with a sub-micron CMOS technology to provide low propagation delays (t_{nd}) and ON resistance (R_{ON}), while maintaining low power dissipation. This bi-lateral switch can be used with either analog or digital signals that may vary across the full power supply range from V_{CC} to GND.

Features

- Wide V_{CC} Operating Range: 1.65 V to 5.5 V
- OVT up to +5.5 V for Control Pin
- R_{ON}: Typically 5.5 Ω at V_{CC} = 4.5 V and I_S = 32 mA
- Rail-to-Rail Input/Output
- High On-Off Output Voltage Ratio
- High Degree of Linearity
- Ultra-Small Pb-Free, Halide-Free, RoHS-Compliant Packages
- ESD Performance: > 5000 V HBM, > 400 V MM

Typical Applications

• Cell Phones, PDAs, MP3 and other Portable Media Players

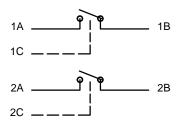


Figure 1. Analog Symbol

PIN ASSIGNMENTS

UDFN8	WLCSP8	Description
1	A1	1A
2	B1	1B
3	C1	2C
4	D1	GND
5	D2	2A
6	C2	2B
7	B2	1C
8	A2	V _{CC}

This document contains information on some products that are still under development. ON Semiconductor reserves the right to change or discontinue these products without notice.

1



ON Semiconductor®

www.onsemi.com

MARKING DIAGRAMS



UDFN8 **MU SUFFIX** CASE 517BZ





UDFN8 **MU SUFFIX** CASE 517CA



= Specific Device Code = Date Code = Pb-Free Package



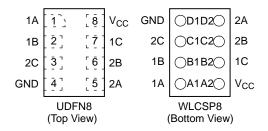
XX

WLCSP8 **FC SUFFIX** CASE 567MR

XXXX **AYWW**

= Assembly Location = Year WW = Work Week

PIN ASSIGNMENTS



FUNCTION TABLE

Control Input (C)	Switch
L	OFF
Н	ON

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

Table 1. MAXIMUM RATINGS

Symbol	Rating		Value	Unit
V _{CC}	Positive DC Supply Voltage		-0.5 to +7.0	V
V _S	Switch Input / Output Voltage	(Pins 1A, 1B, 2A and 2B)	-0.5 to + V _{CC} + 0.5	V
VI	Digital Control Input Voltage	(Pins 1C and 2C)	-0.5 to +7.0	V
l _{ok}	I/O port diode current		±50	mA
I _{IK}	Control input diode current		- 50	mA
I _{I/O}	Continuous DC Current Through Analog Switch		±100	mA
lι	Latch-up Current, (Above V _{CC} and below GND	at 125°C)	±100	mA
Ts	Storage Temperature		-65 to +150	°C
V _{ESD}	ESD Withstand Voltage: Human Body Mo Machine Model (` '	≥ 5000 > 400	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Table 2. RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter		Min	Max	Unit
V _{CC}	Positive DC Supply Voltage		1.65	5.5	V
V _S	Switch Input / Output Voltage	(Pins 1A, 2A, 1B and 2B)	GND	V _{CC}	V
VI	Digital Control Input Voltage	(Pins 1C and 2C)	GND	5.5	V
T _A	Operating Temperature Range		-55	+125	°C
t _r , t _f	Input Transition Rise or Fall Time $V_{CC} = < 3.0 \text{ V}$		0	20	ns/V
	(ON/OFF Control Input)	$V_{CC} = \ge 3.0 \text{ V}$	0	10	

Table 3. ELECTRICAL CHARACTERISTICS

					Guaran	teed Limit	:	
				25	5°C	–55° to	125°C	1
Symbol	Parameter	Condition	V _{CC}	Min	Max	Min	Max	Unit
V _{IH}	High-Level Input Voltage, Control Input		1.65 to 1.95			V _{CC} x 0.65		V
			2.3 to 5.5			V _{CC} x 0.7		
V _{IL}	Low-Level Input Voltage, Control Input		1.65 to 1.95				V _{CC} x 0.35	V
			2.3 to 5.5				V _{CC} x 0.30	
lį	Input Leakage Current, Control Input	$V_{I} = V_{CC}$ or GND	5.5		±0.1		±1	μΑ
I _{S(ON)}	ON-State Switch Leakage Current	$V_{IS} = V_{CC}$ or GND, $V_{I} = V_{IH}$, $V_{OS} = Open$	5.5		±0.1		±1	μΑ
I _{S(OFF)}	OFF–State Switch Leakage Current	$\begin{aligned} & V_{IS} = V_{CC} \text{ and } V_{OS} = \\ & GND, or V_{IS} = GND and \\ & V_{OS} = V_{CC} GND, V_{I} = V_{IL}, \end{aligned}$	5.5		±0.1		±1	μΑ
I _{CC}	Quiescent Supply Current	$V_I = V_{CC}$ or GND	5.5		1.0		10	μΑ
ΔI_{CC}	Supply Current Change	$V_{I} = V_{CC} - 0.6$	5.5				500	μΑ
CI	Control Input Capacitance		5				3.0	pF
C _{I/O(Off)}	Switch OFF Input / Output Capacitance	See Figure 3	5				6.0	pF
C _{I/O(On)}	Switch ON Input / Output Capacitance	See Figure 4	5				13	pF

Table 4. SWITCHING CHARACTERISTICS

				Guarant	eed Limit	
				–55° to	125°C	1
Symbol	Parameter	Condition	V _{CC}	Min	Max	Unit
t _{PLH} , t _{PHL}	Propagation Delay,	$C_L = 30 \text{ pF}, R_L = 1 \text{ k}\Omega$	1.8		6.5	ns
	A to B, B to A		2.5		3.3	1
		$C_L = 50 \text{ pF}, R_L = 500 \Omega$	3.3		2.5	1
			5.0		2.2	1
t _{EN}	Enable Time,	$C_L = 50 \text{ pF}, R_L = 500 \Omega$	1.8		10	ns
(t _{PZL} , t _{PZH})	C to Analog Output (A or B)	See Figure 6	2.5		6.5	1
			3.3		5.5	1
			5.0		4.9	1
t _{DIS}	Disable Time,	$C_L = 50 \text{ pF}, R_L = 500 \Omega$	1.8		9.0	ns
(t _{PLZI} , t _{PHZ})	C to Analog Output (A or B)	See Figure 6	2.5		7.2	1
			3.3		6.5	1
			5.0		6.0	1

Table 5. ANALOG SWITCH CHARACTERISTICS

					25°C	–55° to	125°C	
Symbol Parameter		Conditions	Conditions		Тур	Min	Max	Unit
R _{ON}	On-Resistance	$V_{IS} = V_{CC}$ or GND,	I _S = 4 ma	1.65	12		30	Ω
		V _I = V _{IH} , See Figure 2	I _S = 8 ma	2.3	9		20	1
			I _S = 24 ma	3.0	7.5		15	1
			I _S = 32 ma	4.5	5.5		13	1
R _{ON(peak)}	Peak On–Resistance	$V_{IS} = GND \text{ to } V_{CC}; V_I = V_{IH},$	I _S = 4 ma	1.65	74.5		220	Ω
		See Figure 2	I _S = 8 ma	2.3	20		75	1
			I _S = 24 ma	3.0	11.5		25	1
			I _S = 32 ma	4.5	7.5		17	1
ΔR_{ON}	On-Resistance	$V_{IS} = GND$ to V_{CC} ; $V_I = V_{IH}$,	I _S = 4 ma	1.65			8.0	Ω
	Mismatch between Switches	See Figure 2	I _S = 8 ma	2.3			5.0	
			I _S = 24 ma	3.0			3.0	
			I _S = 32 ma	4.5			2.0	
BW	Bandwidth (f _{-3dB})	$R_L = 50 \Omega$, $C_L = 5 pF$,		1.65			> 270	MHz
	f _{IN} = Sine Wave See Figure 8		2.3			> 270	1	
				3.0			> 270	
				4.5			> 270	

Table 5. ANALOG SWITCH CHARACTERISTICS (continued)

				25°C	
Symbol	Parameter	er Conditions	v _{cc}	Тур	Unit
ISO _{Off}	Off—Channel Feedthrough Isolation	$R_L = 600 \Omega$, $C_L = 50 pF$,	1.65	-70	dB
		f _{IN} = 1 MHz Sine Wave See Figure 9	2.3	-70	
			3.0	-70	
			4.5	-70	
		$R_L = 50 \Omega$, $C_L = 5 pF$,	1.65	-60	
		f _{IN} = 1 MHz Sine Wave See Figure 9	2.3	-60	
			3.0	-60	
			4.5	-60	
XTalk	Crosstalk	$R_L = 600 \Omega$, $C_L = 50 pF$,	1.65	-100	dB
	Between Switches	setween Switches f _{IN} = 1 MHz Sine Wave See Figure 10	2.3	-100	
			3.0	-100	
			4.5	-100	
		$R_L = 50 \Omega$, $C_L = 5 pF$,	1.65	-90	
		f _{IN} = 1 MHz Sine Wave See Figure 10	2.3	-90	
			3.0	-90	
			4.5	-90	
	Feedthrough Noise,	$R_L = 600 \Omega$, $C_L = 50 pF$,	1.65	10	mV_{pp}
	Control to Switch	$f_{IN} = 1$ MHz Square Wave, $t_r = t_f = 2$ ns, See Figure 11	2.3	10	
			3.0	10	
			4.5	15	
THD	Total Harmonic	$C_L = 50 \text{ pF}, R_L = 50 \Omega,$	2.3	0.025	%
	Distortion	f _{IN} = 600 Hz to 20 KHz Sine Wave, See Figure 12	3.0	0.015	
			4.5	0.01	

Table 6. POWER DISSIPATION CHARACTERISTICS

				25°C	
Symbol	Parameter	Conditions	V _{CC}	Тур	Unit
C _{PD}	Power Dissipation Capacitance	f = 10 MHz	1.65	8.0	pF
	Capacitance		2.3	8.9	
			3.0	9.6	
			4.5	10.9	

Table 7. DEVICE ORDERING INFORMATION

Device Order Number	Package	Shipping [†]
NLX2G66DMUTAG	UDFN8-0.5P, 1.95 mm x 1.0 mm (Pb-Free)	3000 / Tape & Reel
NLX2G66DMUTCG	UDFN8-0.5P, 1.95 mm x 1.0 mm (Pb-Free)	3000 / Tape & Reel
NLX2G66MU3TCG (In Development)	UDFN8-0.35P, 1.45 mm x 1.0 mm (Pb-Free)	3000 / Tape & Reel
NLX2G66FCTAG	WLCSP8, 1.888 mm x 0.888 mm (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

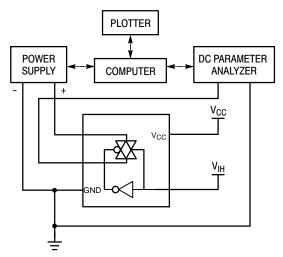


Figure 2. On Resistance Test Set-Up

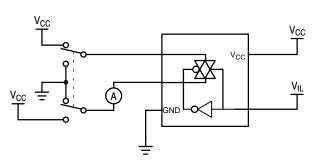


Figure 3. Maximum Off-Channel Leakage Current Test Set-Up

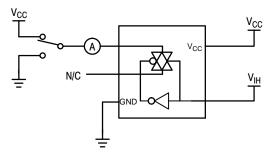


Figure 4. Maximum On-Channel Leakage Current Test Set-Up

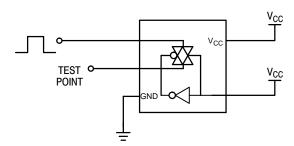


Figure 5. Propagation Delay Test Set-Up

Switch to Position 2 when testing t_{PLZ} and t_{PZL} Switch to Position 1 when testing t_{PHZ} and t_{PZH}

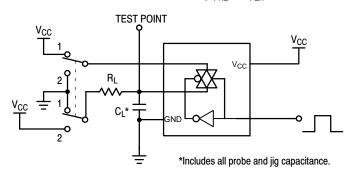


Figure 6. Propagation Delay Output Enable/Disable Test Set-Up

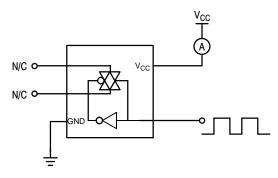
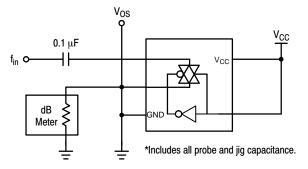


Figure 7. Power Dissipation Capacitance Test Set-Up



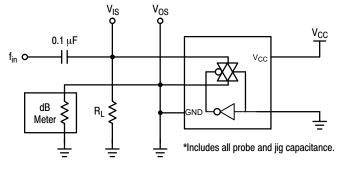


Figure 8. Maximum On-Channel Bandwidth
Test Set-Up

Figure 9. Off-Channel Feedthrough Isolation
Test Set-Up

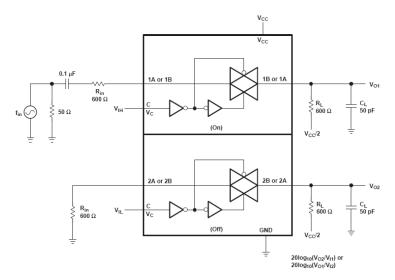


Figure 10. Crosstalk (between Switches)

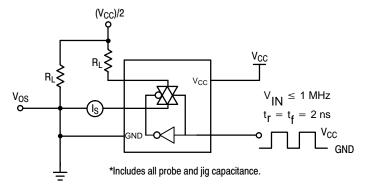


Figure 11. Feedthrough Noise, ON/OFF Control to Analog Out, Test Set-Up

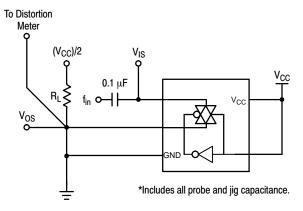


Figure 12. Total Harmonic Distortion Test Set-Up

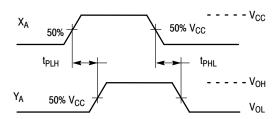


Figure 13. Propagation Delay, Analog In to Analog Out Waveforms

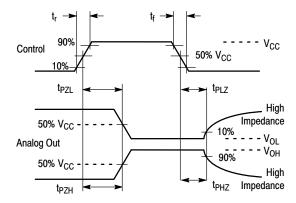
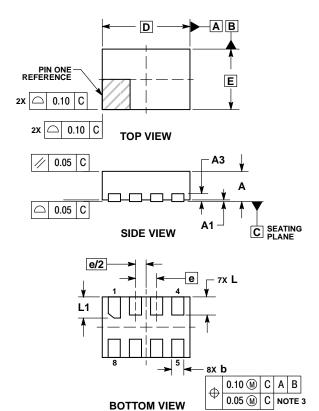


Figure 14. Propagation Delay, ON/OFF Control

PACKAGE DIMENSIONS

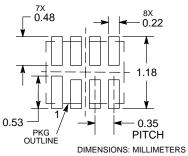
UDFN8 1.45x1.0, 0.35P CASE 517BZ ISSUE O



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
 4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.45	0.55		
A1	0.00	0.05		
A3	0.13 REF			
b	0.15	0.25		
D	1.45	BSC		
E	1.00	BSC		
е	0.35	BSC		
L	0.25	0.35		
L1	0.30	0.40		

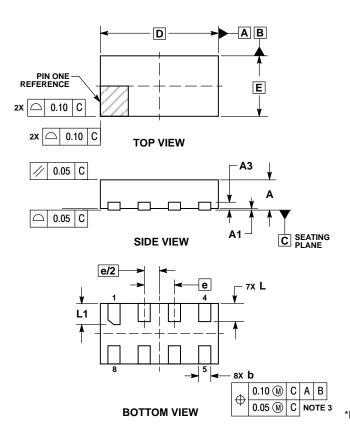
RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

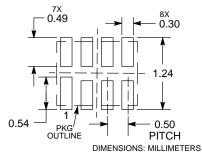
UDFN8 1.95x1.0, 0.5P CASE 517CA ISSUE O



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
 4. PACKAGE DIMENSIONS EXCLUSIVE OF BURRS AND MOLD FLASH.

	MILLIMETERS		
DIM	MIN MAX		
Α	0.45	0.55	
A1	0.00	0.05	
A3	0.13 REF		
b	0.15	0.25	
D	1.95	BSC	
Е	1.00	BSC	
е	0.50	BSC	
L	0.25	0.35	
11	0.30	0.40	

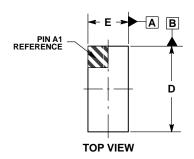
RECOMMENDED SOLDERING FOOTPRINT*

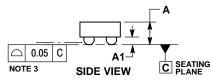


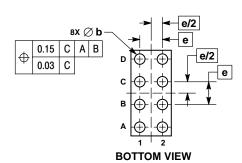
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

WLCSP8, 1.888x0.888 CASE 567MR **ISSUE O**





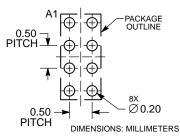


NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

	MILLIMETERS	
DIM	MIN	MAX
Α	i	0.50
A1	0.15	0.19
b	0.21	0.25
D	1.858	1.918
E	0.858	0.918
е	0.50 BSC	

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and the (III) are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA **Phone**: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax**: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Analogue Switch ICs category:

Click to view products by ON Semiconductor manufacturer:

Other Similar products are found below:

FSA3051TMX NLAS4684FCTCG NLAS5223BLMNR2G NLX2G66DMUTCG 425541DB 425528R 099044FB NLAS5123MNR2G PI5A4157CEX NLAS4717EPFCT1G PI5A3167CCEX SLAS3158MNR2G PI5A392AQE PI5A4157ZUEX PI5A3166TAEX FSA634UCX XS3A1T3157GMX TC4066BP(N,F) DG302BDJ-E3 PI5A100QEX HV2605FG-G HV2301FG-G RS2117YUTQK10 RS2118YUTQK10 RS2227XUTQK10 ADG452BRZ-REEL7 MAX4066ESD+ MAX391CPE+ MAX4730EXT+T MAX314CPE+ BU4066BCFV-E2 MAX313CPE+ BU4S66G2-TR NLASB3157MTR2G TS3A4751PWR NLAST4599DFT2G NLAST4599DTT1G DG300BDJ-E3 DG2503DB-T2-GE1 TC4W53FU(TE12L,F) HV2201FG-G 74HC2G66DC.125 DG3257DN-T1-GE4 ADG619BRMZ-REEL ADG1611BRUZ-REEL7 DG2535EDQ-T1-GE3 LTC201ACN#PBF 74LV4066DB,118 ISL43410IUZ FSA2275AUMX