MURB1620CTG, NRVUB1620CTT4G

SWITCHMODE Power Rectifier

D²PAK Power Surface Mount Package

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

Features

- Package Designed for Power Surface Mount Applications
- Ultrafast 35 Nanosecond Recovery Times
- 175°C Operating Junction Temperature
- Epoxy Meets UL 94 V-0 @ 0.125 in
- High Temperature Glass Passivated Junction
- Low Leakage Specified @ 150°C Case Temperature
- Short Heat Sink Tab Manufactured Not Sheared!
- Similar in Size to Industrial Standard TO-220 Package
- NRVUB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant*

Mechanical Characteristics:

- Case: Epoxy, Molded, Epoxy Meets UL 94, V-0
- Weight: 1.7Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL1 Requirements
- ESD Ratings:
 - Machine Model = C (> 400 V)
 - ♦ Human Body Model = 3B (> 8000 V)



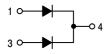
ON Semiconductor®

http://onsemi.com

ULTRAFAST RECTIFIER16 AMPERES, 200 VOLTS



D²PAK CASE 418B STYLE 3



MARKING DIAGRAM



A = Assembly Location

Y = Year

WW = Work Week

U1620 = Device Code

G = Pb-Free Package

AKA = Diode Polarity

ORDERING INFORMATION

Device	Package	Shipping [†]
MURB1620CTG	D ² PAK (Pb-Free)	50 Units / Rail
MURB1620CTT4G	D ² PAK (Pb-Free)	800 / Tape & Reel
NRVUB1620CTT4G	D ² PAK (Pb-Free)	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MURB1620CTG, NRVUB1620CTT4G

MAXIMUM RATINGS (Per Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	200	V
Average Rectified Forward Current (Rated V _R , T _C = 150°C) Total Device	I _{F(AV)}	8.0 16	Α
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz, T _C = 150°C)	I _{FM}	16	Α
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	100	Α
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-65 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	3	°C/W
Maximum Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	50	°C/W
Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	TL	260	°C

ELECTRICAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Max	Unit
Maximum Instantaneous Forward Voltage (Note 1) ($i_F = 8$ A, $T_C = 150^{\circ}$ C) ($i_F = 8$ A, $T_C = 25^{\circ}$ C)	VF	0.895 0.975	V
Maximum Instantaneous Reverse Current (Note 1) (Rated DC Voltage, T _C = 150°C) (Rated DC Voltage, T _C = 25°C)	i _R	250 5	μΑ
Maximum Reverse Recovery Time $ \begin{aligned} &(I_F=1 \text{ A, di/dt}=50 \text{ A/}\mu\text{s}) \\ &(I_F=0.5 \text{ A, } I_R=1 \text{ A, } I_{REC}=0.25 \text{ A}) \end{aligned} $	t _{rr}	35 25	ns

^{1.} Pulse Test: Pulse Width = 300 µs, Duty Cycle ≤ 2.0%

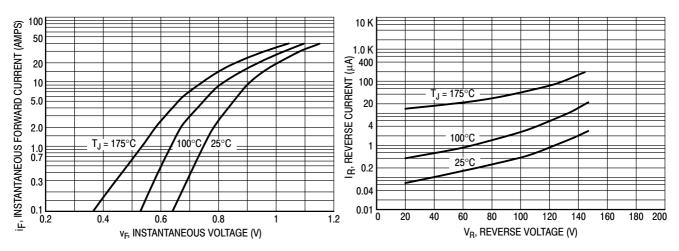


Figure 1. Typical Forward Voltage, Per Leg

Figure 2. Typical Reverse Current, Per Leg*

MURB1620CTG, NRVUB1620CTT4G

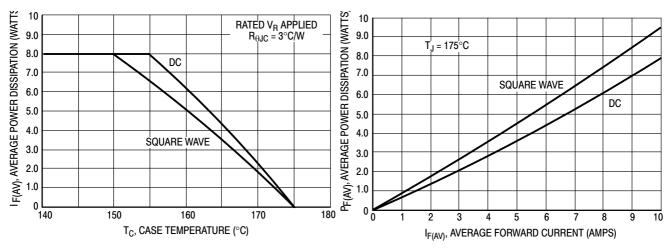


Figure 3. Current Derating Case, Per Leg

Figure 4. Power Dissipation, Per Leg

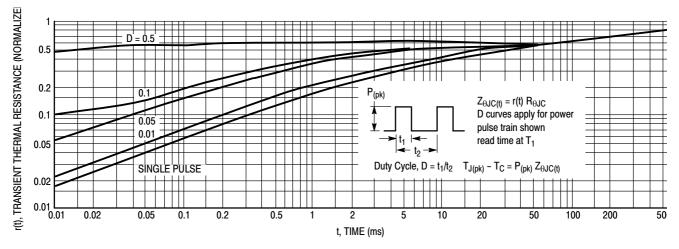


Figure 5. Thermal Response

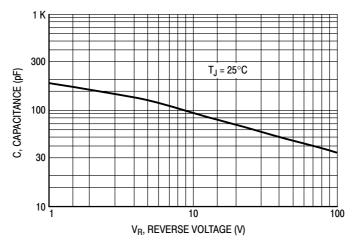


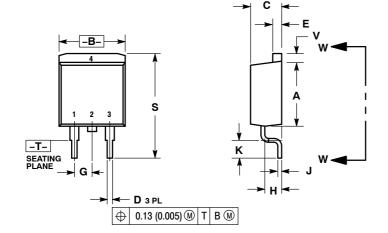
Figure 6. Typical Capacitance, Per Leg



D²PAK 3 CASE 418B-04 **ISSUE L**

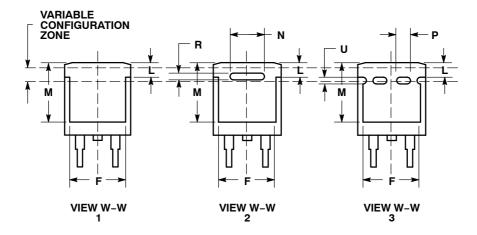
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SCALE 1:1



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH. 418B-01 THRU 418B-03 OBSOLETE,
- NEW STANDARD 418B-04.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.340	0.380	8.64	9.65
В	0.380	0.405	9.65	10.29
С	0.160	0.190	4.06	4.83
D	0.020	0.035	0.51	0.89
E	0.045	0.055	1.14	1.40
F	0.310	0.350	7.87	8.89
G	0.100 BSC		2.54 BSC	
Н	0.080	0.110	2.03	2.79
J	0.018	0.025	0.46	0.64
K	0.090	0.110	2.29	2.79
L	0.052	0.072	1.32	1.83
М	0.280	0.320	7.11	8.13
N	0.197 REF		5.00 REF	
Р	0.079 REF		2.00 REF	
R	0.039 REF		0.99 REF	
S	0.575	0.625	14.60	15.88
V	0.045	0.055	1.14	1.40



STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR

STYLE 2: PIN 1. GATE 2. DRAIN

3. SOURCE 4. DRAIN

STYLE 3:

STYLE 4: PIN 1. ANODE 2. CATHODE 3. ANODE 4. CATHODE

PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR STYLE 5:

PIN 1. CATHODE 2. ANODE 3. CATHODE 4. ANODE

STYLE 6:

PIN 1. NO CONNECT 2. CATHODE 3. ANODE 4. CATHODE

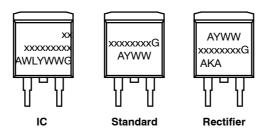
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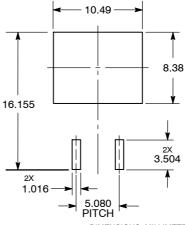
GENERIC MARKING DIAGRAM*



xx = Specific Device Code
A = Assembly Location
WL = Wafer Lot

Y = Year
WW = Work Week
G = Pb-Free Package
AKA = Polarity Indicator

SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot " ■", may or may not be present.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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