NST3906DP6T5G

Dual General Purpose Transistor

The NST3906DP6T5G device is a spin-off of our popular SOT-23/SOT-323/SOT-563 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-963 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

Features

- h_{FF}, 100-300
- Low $V_{CE(sat)}$, $\leq 0.4 \text{ V}$
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- This is a Pb-Free Device

MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Collector - Emitter Voltage		V_{CEO}	-40	V
Collector - Base Voltage		V _{CBO}	-40	V
Emitter - Base Voltage		V _{EBO}	-5.0	V
Collector Current - Continuous		Ic	-200	mA
Electrostatic Discharge	HBM MM	ESD Class	2 B	

THERMAL CHARACTERISTICS

Characteristic (Single Heated)	Symbol	Max	Unit
Total Device Dissipation T _A = 25°C Derate above 25°C (Note 1)	P _D	240 1.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{ heta JA}$	520	°C/W
Total Device Dissipation T _A = 25°C Derate above 25°C (Note 2)	P _D	280 2.2	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	446	°C/W
Characteristic (Dual Heated) (Note 3)	Symbol	Max	Unit
Total Device Dissipation T _A = 25°C Derate above 25°C (Note 1)	P _D	350 2.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	357	°C/W
Total Device Dissipation T _A = 25°C Derate above 25°C (Note 2)	P _D	420 3.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	297	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-4 @ 100 mm², 1 oz. copper traces, still air.

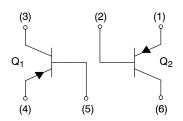
2. FR-4 @ 500 mm², 1 oz. copper traces, still air.

- 3. Dual heated values assume total power is sum of two equally powered channels.



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NST3906DP6T5G



SOT-963 CASE 527AD

MARKING DIAGRAM



= Device Code M = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
NST3906DP6T5G	SOT-963 (Pb-Free)	8000/Tape & Reel

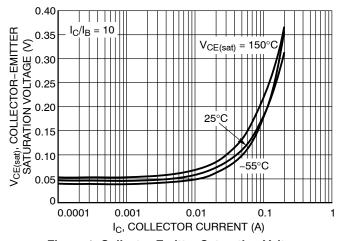
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NST3906DP6T5G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

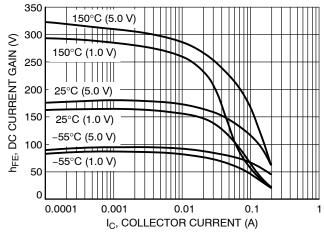
	Characteristic	Symbol	Min	Max	Unit
OFF CHARACTE	RISTICS	-			•
Collector - Emitter B	reakdown Voltage (Note 4) (I _C = 1.0 mAdc, I _B = 0)	V _{(BR)CEO}	-40	-	V
Collector - Base Bre	akdown Voltage (I _C = 10 μAdc, I _E = 0)	V _{(BR)CBO}	-40	_	V
Emitter – Base Brea	down Voltage (I _E = 10 μAdc, I _C = 0)	V _{(BR)EBO}	-5.0	-	V
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)		I _{CEX}	-	-50	nA
ON CHARACTER	STICS (Note 4)				
DC Current Gain $(I_C = -0.1 \text{ mA}, V_C)$ $(I_C = -1.0 \text{ mA}, V_C)$ $(I_C = -10 \text{ mA}, V_C)$ $(I_C = -50 \text{ mA}, V_C)$ $(I_C = -100 \text{ mA}, V_C)$	= -1.0 V) = = -1.0 V) = = -1.0 V)	h _{FE}	60 80 100 60 30	- 300 - -	_
Collector – Emitter S ($I_C = -10 \text{ mA}, I_B = (I_C = -50 \text{ mA}, I_B = -50 \text{ mA})$	-1.0 mA)	V _{CE(sat)}	- -	-0.25 -0.4	V
Base – Emitter Satur ($I_C = -10 \text{ mA}, I_B = (I_C = -50 \text{ mA}, I_B = -50 \text{ mA})$	-1.0 mA)	V _{BE(sat)}	-0.65 -	-0.85 -0.95	V
SMALL-SIGNAL	CHARACTERISTICS				
Current - Gain - Bai	ndwidth Product (I _C = 10 mAdc, V _{CE} = 20 Vdc, f = 100 MHz)	f _T	250	-	MHz
Output Capacitance	$(V_{CB} = -5.0 \text{ V}, I_E = 0 \text{ mA}, f = 1.0 \text{ MHz})$	C _{obo}	-	4.5	pF
Input Capacitance (V _{EB} = -0.5 V, I _E = 0 mA, f = 1.0 MHz)		C _{ibo}	-	10.0	pF
Noise Figure (V_{CE} = -5.0 V, I_{C} = -100 μ A, R_{S} = 1.0 k Ω , f = 1.0 kHz)		NF	-	4.0	dB
SWITCHING CHA	RACTERISTICS	•		•	•
Delay Time	$(V_{CC} = -3.0 \text{ V}, V_{BE} = 0.5 \text{ V})$	t _d	_	35	
Rise Time	$(I_C = -10 \text{ mA}, I_{B1} = -1.0 \text{ mA})$	t _r	-	35	ns
Storage Time	$(V_{CC} = -3.0 \text{ V}, I_C = -10 \text{ mA})$	t _s	-	250	

^{4.} Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 2.0%.



 $(I_{B1} = I_{B2} = -1.0 \text{ mA})$

Figure 1. Collector Emitter Saturation Voltage vs. Collector Current



ns

50

Figure 2. DC Current Gain vs. Collector Current

NST3906DP6T5G

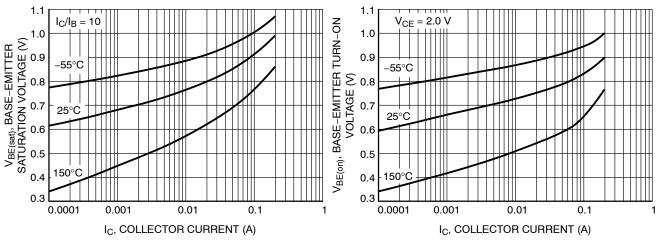


Figure 3. Base Emitter Saturation Voltage vs. Collector Current

Figure 4. Base Emitter Turn-On Voltage vs.
Collector Current

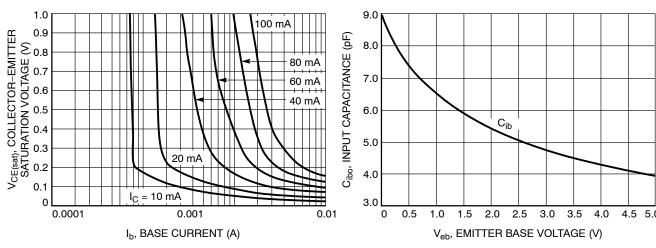


Figure 5. Saturation Region

Figure 6. Input Capacitance

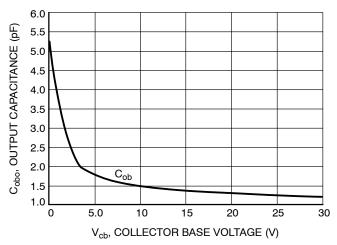


Figure 7. Output Capacitance

MECHANICAL CASE OUTLINE

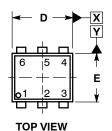


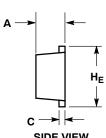


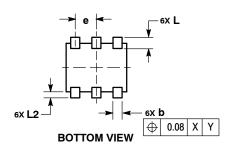
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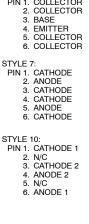




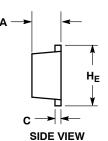




STYLE 1: PIN 1. EMITTER 1 2. BASE 1 3. COLLECTOR 2 4. EMITTER 2 5. BASE 2 6. COLLECTOR 1
STYLE 4: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR
STYLE 7: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. ANODE 6. CATHODE



STYLE 2: PIN 1. EMITTER 1 2. EMITTER2 3. BASE 2 4. COLLECTOR 2 5. BASE 1 6. COLLECTOR 1	STYLE 3: PIN 1. CATHODE 1 2. CATHODE 1 3. ANODE/ANODE 2 4. CATHODE 2 5. CATHODE 2 6. ANODE/ANODE 1
STYLE 5:	STYLE 6:
PIN 1. CATHODE 2. CATHODE	PIN 1. CATHODE 2. ANODE
3. ANODE	3. CATHODE
4. ANODE	4. CATHODE
5. CATHODE 6. CATHODE	5. CATHODE 6. CATHODE
STYLE 8:	STYLE 9:
PIN 1. DRAIN	PIN 1. SOURCE 1
2. DRAIN 3. GATE	2. GATE 1 3. DRAIN 2
4. SOURCE	4. SOURCE 2
5. DRAIN	5. GATE 2
6. DRAIN	6. DRAIN 1



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME
- DIMENSIONING AND TOLEHANCING PER ASM Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.34	0.37	0.40
b	0.10	0.15	0.20
С	0.07	0.12	0.17
D	0.95	1.00	1.05
E	0.75	0.80	0.85
е	0.35 BSC		
HE	0.95	1.00	1.05
L	0.19 REF		
L2	0.05	0.10	0.15

GENERIC MARKING DIAGRAM*

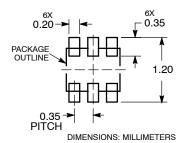


= Specific Device Code = Month Code Μ

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

RECOMMENDED MOUNTING FOOTPRINT



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