onsemi

PNP General Purpose Transistor

NST3906F3T5G

The NST3906F3T5G device is a spin-off of our popular SOT-23/SOT-323/SOT-563/SOT-963 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-1123 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

Features

- h_{FE}, 100-300
- Low $V_{CE(sat)}$, $\leq 0.4 \text{ V}$
- Reduces Board Space
- This is a Pb–Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-40	Vdc
Collector - Base Voltage	V _{CBO}	-40	Vdc
Emitter – Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	Ι _C	-200	mAdc

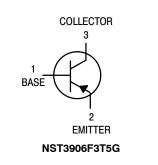
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, T _A = 25°C Derate above 25°C	P _D (Note 1)	290 2.3	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Note 1)	432	°C/W
Total Device Dissipation, T _A = 25°C Derate above 25°C	P _D (Note 2)	347 2.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Note 2)	360	°C/W
Thermal Resistance, Junction-to-Lead 3	R _{ΨJL} (Note 2)	143	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	– 55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

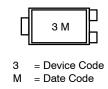
1. 100 mm² 1 oz, copper traces.

2. 500 mm² 1 oz, copper traces.





MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping [†]
NST3906F3T5G	SOT-1123 (Pb-Free)	8000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NST3906F3T5G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (Note 3) ($I_C = 1.0 \text{ mAdc}, I_B = 0$)	V _{(BR)CEO}	-40	-	Vdc
Collector – Base Breakdown Voltage ($I_C = 10 \ \mu Adc$, $I_E = 0$)	V _{(BR)CBO}	-40	-	Vdc
Emitter – Base Breakdown Voltage ($I_E = 10 \ \mu Adc$, $I_C = 0$)	V _{(BR)EBO}	-5.0	-	Vdc
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{BE} = 3.0 Vdc)	I _{CEX}	_	-50	nAdc

ON CHARACTERISTICS (Note 3)

$\label{eq:constraint} \begin{array}{ c c c c } DC \ Current \ Gain \\ (I_C = -0.1 \ mAdc, \ V_{CE} = -1.0 \ Vdc) \\ (I_C = -1.0 \ mAdc, \ V_{CE} = -1.0 \ Vdc) \\ (I_C = -10 \ mAdc, \ V_{CE} = -1.0 \ Vdc) \\ (I_C = -50 \ mAdc, \ V_{CE} = -1.0 \ Vdc) \\ (I_C = -100 \ mAdc, \ V_{CE} = -1.0 \ Vdc) \end{array}$	h _{FE}	60 80 100 60 30	_ 300 _ _	-
Collector – Emitter Saturation Voltage ($I_C = -10 \text{ mAdc}, I_B = -1.0 \text{ mAdc}$) ($I_C = -50 \text{ mAdc}, I_B = -5.0 \text{ mAdc}$)	V _{CE(sat)}	- -	-0.25 -0.4	Vdc
Base – Emitter Saturation Voltage $(I_C = -10 \text{ mAdc}, I_B = -1.0 \text{ mAdc})$ $(I_C = -50 \text{ mAdc}, I_B = -5.0 \text{ mAdc})$	V _{BE(sat)}	-0.65 -	-0.85 -0.95	Vdc

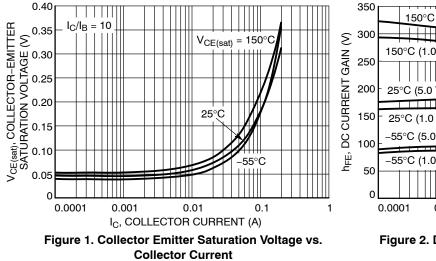
SMALL-SIGNAL CHARACTERISTICS

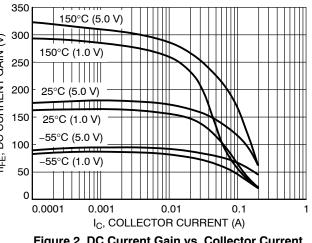
Current – Gain – Bandwidth Product (I_C = 10 mAdc, V_{CE} = 20 Vdc, f = 100 MHz)	f _T	250	-	MHz
Output Capacitance (V_{CB} = -5.0 V, I_E = 0 mA, f = 1.0 MHz)	C _{obo}	-	4.5	pF
Input Capacitance (V_{EB} = -0.5 V, I_E = 0 mA, f = 1.0 MHz)	C _{ibo}	-	10.0	pF
Noise Figure (V _{CE} = -5.0 Vdc, I _C = -100 μ Adc, R _S = 1.0 kΩ, f = 1.0 kHz)	NF	-	4.0	dB

SWITCHING CHARACTERISTICS

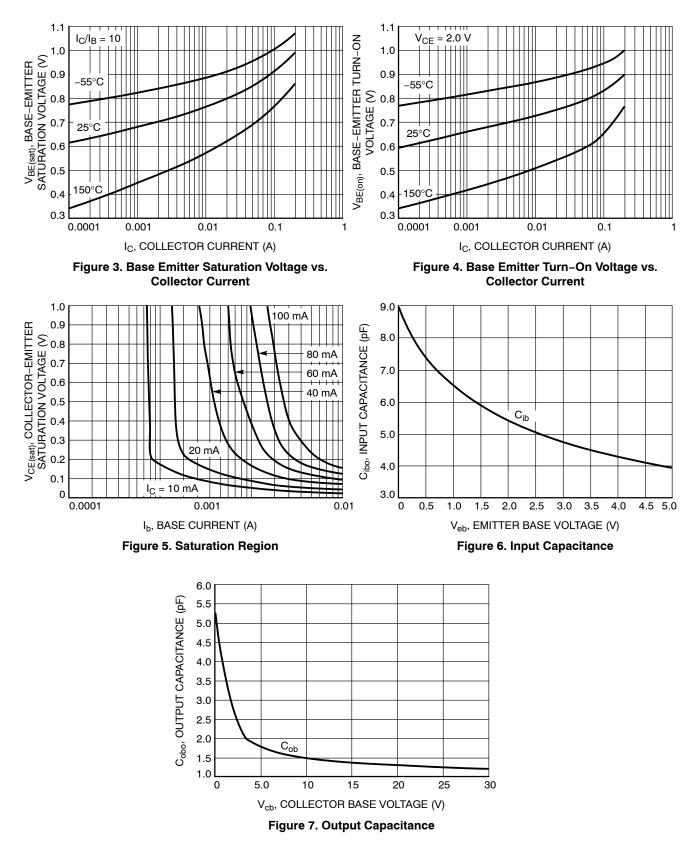
Delay Time	$(V_{CC} = -3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc})$	t _d	-	35	20
Rise Time	$(I_{C} = -10 \text{ mAdc}, I_{B1} = -1.0 \text{ mAdc})$	t _r	-	35	ns
Storage Time	$(V_{CC} = -3.0 \text{ Vdc}, I_C = -10 \text{ mAdc})$	t _s	-	250	20
Fall Time	(I _{B1} = I _{B2} = -1.0 mAdc)	t _f	-	50	ns

3. Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle \leq 2.0%.





NST3906F3T5G



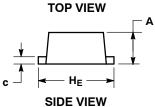




SCALE 8:1



SOT-1123 CASE 524AA ISSUE C





SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. GATE
2. EMITTER	2. N/C	2. ANODE	2. CATHODE	2. SOURCE
3. COLLECTOR	3. CATHODE	3. CATHODE	3. ANODE	3. DRAIN

DOCUMENT NUMBER:	98AON23134D	Electronic versions are uncontrolled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED	
DESCRIPTION:	SOT-1123, 3-LEAD, 1.0X0	.6X0.37, 0.35P	PAGE 1 OF 1
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DATE 29 NOV 2011

- NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS.
- CONTROLLING DIMENSION: MILLIMETERS.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE
- MINIMUM THICKNESS OF BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIM	MILLIMETERS		
DIM	MIN	MAX		
Α	0.34	0.40		
b	0.15	0.28		
b1	0.10	0.20		
С	0.07	0.17		
D	0.75	0.85		
Е	0.55	0.65		
е	0.35	0.40		
HE	0.95	1.05		
L	0.185 REF			
L2	0.05	0.15		

GENERIC MARKING DIAGRAM*

X = Specific Device Code M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " •", may or may not be present.

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