# 100 V, 3.0 A, Low V<sub>CE(sat)</sub> PNP Transistor

ON Semiconductor's  $e^2$ PowerEdge family of low  $V_{CE(sat)}$  transistors are surface mount devices featuring ultra low saturation voltage ( $V_{CE(sat)}$ ) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical applications are DC–DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e<sup>2</sup>PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

#### **Features**

- Complement to NSS1C301ET4G
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

#### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating	Symbol	Max	Unit
Collector-Base Voltage	V <sub>CBO</sub>	140	Vdc
Collector-Emitter Voltage	V <sub>CEO</sub>	100	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	6.0	Vdc
Collector Current – Continuous	I <sub>C</sub>	3.0	Adc
Collector Current – Peak	I <sub>CM</sub>	6.0	Adc
Base Current	I <sub>B</sub>	0.5	Adc
Total Power Dissipation  @ T <sub>C</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	33 0.26	W W/°C
Total Power Dissipation (Note 1)  @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	2.1 0.017	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

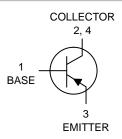
 These ratings are applicable when surface mounted on the minimum pad sizes recommended.



#### ON Semiconductor®

www.onsemi.com

## 100 VOLTS, 3.0 AMPS PNP LOW V<sub>CE(sat)</sub> TRANSISTOR





DPAK CASE 369C STYLE 1

#### **MARKING DIAGRAM**



Y = Year WW = Work Week 1C30E = Device Code G = Pb-Free

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NSS1C300ET4G	DPAK (Pb-Free)	2500/ Tape & Reel
NSV1C300ET4G	DPAK (Pb-Free)	2500/ Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	3.8	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	59.5	°C/W

<sup>2.</sup> These ratings are applicable when surface mounted on the minimum pad sizes recommended.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	<u>.                                      </u>				
Collector – Emitter Breakdown Voltage $(I_C = -10 \text{ mAdc}, I_B = 0)$	V <sub>(BR)CEO</sub>	-100	-	_	Vdc
Collector – Base Breakdown Voltage (I <sub>C</sub> = -0.1 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	-140	-	-	Vdc
Emitter – Base Breakdown Voltage (I <sub>E</sub> = -0.1 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	-6.0	-	-	Vdc
Collector Cutoff Current (V <sub>CB</sub> = -140 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	-	-0.1	μAdc
Emitter Cutoff Current (V <sub>EB</sub> = -6.0 Vdc)	I <sub>EBO</sub>	_	-	-0.1	μAdc
ON CHARACTERISTICS			•		•
DC Current Gain (Note 3) $ \begin{aligned} &(I_C = -0.1 \text{ A, } V_{CE} = -2.0 \text{ V}) \\ &(I_C = -0.5 \text{ A, } V_{CE} = -2.0 \text{ V}) \\ &(I_C = -1.0 \text{ A, } V_{CE} = -2.0 \text{ V}) \\ &(I_C = -3.0 \text{ A, } V_{CE} = -2.0 \text{ V}) \end{aligned} $	h <sub>FE</sub>	180 180 120 50		- - 360 -	_
Collector – Emitter Saturation Voltage (Note 3) $ \begin{array}{l} (I_C = -0.1 \text{ A, } I_B = -10 \text{ mA}) \\ (I_C = -1.0 \text{ A, } I_B = -0.100 \text{ A}) \\ (I_C = -2.0 \text{ A, } I_B = -0.200 \text{ A}) \\ (I_C = -3.0 \text{ A, } I_B = -0.300 \text{ A}) \end{array} $	V <sub>CE(sat)</sub>	- - - -	- - -	-0.070 -0.150 -0.250 -0.400	V
Base – Emitter Saturation Voltage (Note 3) $(I_C = -1.0 \text{ A}, I_B = -0.1 \text{ A})$	V <sub>BE(sat)</sub>	_	-	-1.0	V
Base – Emitter Turn–on Voltage (Note 3) (I <sub>C</sub> = -1.0 A, V <sub>CE</sub> = -2.0 V)	V <sub>BE(on)</sub>	_	-	-0.900	V
Cutoff Frequency ( $I_C = -500 \text{ mA}, V_{CE} = -10 \text{ V}, f = 100 \text{ MHz}$ )	f <sub>T</sub>	-	100	-	MHz
Input Capacitance (V <sub>EB</sub> = 5.0 V, f = 1.0 MHz)	Cibo	_	360	_	pF
Output Capacitance (V <sub>CB</sub> = 10 V, f = 1.0 MHz)	Cobo	_	60	_	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulsed Condition: Pulse Width = 300 msec, Duty Cycle ≤ 2%.

#### TYPICAL CHARACTERISTICS

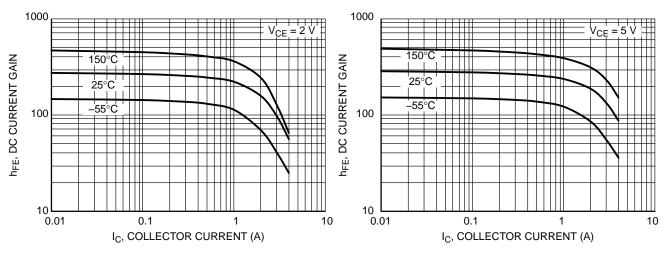


Figure 1. DC Current Gain

Figure 2. DC Current Gain

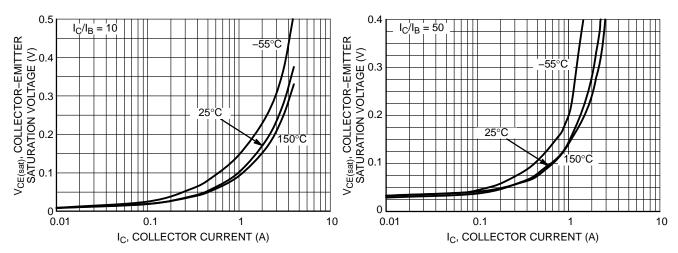


Figure 3. Collector-Emitter Saturation Voltage

Figure 4. Collector-Emitter Saturation Voltage

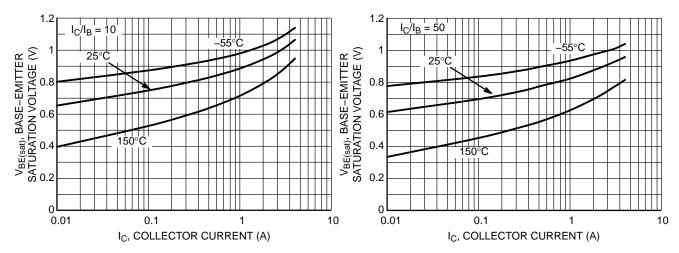


Figure 5. Base-Emitter Saturation Voltage

Figure 6. Base-Emitter Saturation Voltage

#### TYPICAL CHARACTERISTICS

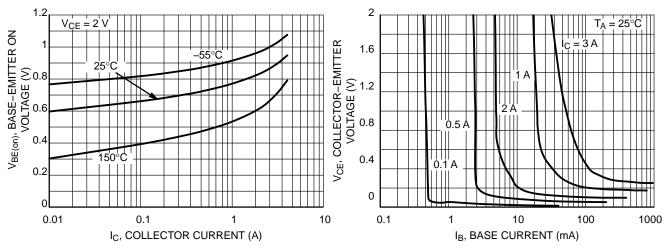


Figure 7. Base-Emitter On Voltage

Figure 8. Collector Saturation Region

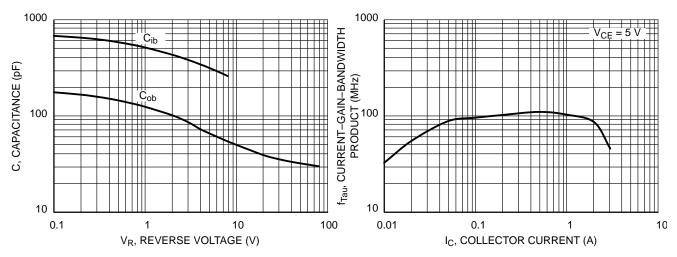


Figure 9. Capacitance

Figure 10. Current-Gain-Bandwidth Product

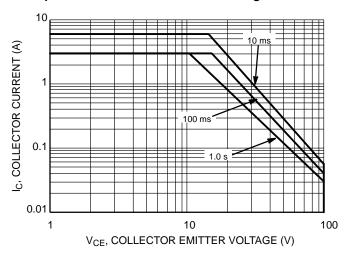


Figure 11. Safe Operating Area

#### **TYPICAL CHARACTERISTICS**

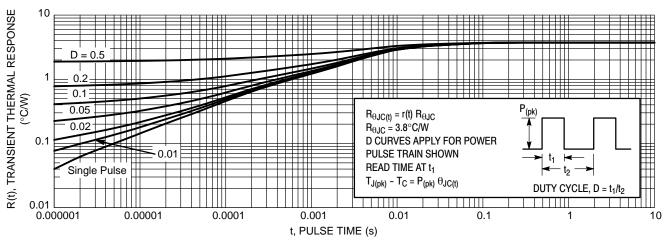


Figure 12. Typical Transient Thermal Response, Junction-to-Case

В

NOTE 7

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**TOP VIEW** 

L3

b2 e

L2 GAUGE

## **DPAK (SINGLE GAUGE)** CASE 369C **ISSUE F** SCALE 1:1 Α

DETAIL A

C SEATING

C-

SIDE VIEW

**DATE 21 JUL 2015** 

#### NOTES:

z

**BOTTOM VIEW** 

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: INCHES.
- 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DI-
- MENSIONS b3, L3 and Z.
  4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
  5. DIMENSIONS D AND E ARE DETERMINED AT THE
- OUTERMOST EXTREMES OF THE PLASTIC BODY.

  6. DATUMS A AND B ARE DETERMINED AT DATUM
- 7. OPTIONAL MOLD FEATURE.

	INCHES		MILLIM	ETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.086	0.094	2.18	2.38	
A1	0.000	0.005	0.00	0.13	
b	0.025	0.035	0.63	0.89	
b2	0.028	0.045	0.72	1.14	
b3	0.180	0.215	4.57	5.46	
С	0.018	0.024	0.46	0.61	
c2	0.018	0.024	0.46	0.61	
D	0.235	0.245	5.97	6.22	
E	0.250	0.265	6.35	6.73	
е	0.090	BSC	2.29	BSC	
Н	0.370	0.410	9.40	10.41	
L	0.055	0.070	1.40	1.78	
L1	0.114	0.114 REF		2.90 REF	
L2	0.020 BSC		0.51 BSC		
L3	0.035	0.050	0.89	1.27	
L4		0.040		1.01	
Z	0.155		3.93		

#### ALTERNATE CONSTRUCTIONS **DETAIL A** ROTATED 90° CW **GENERIC** STYLE 1: STYLE 2: STYLE 3: STYLE 4: STYLE 5: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR PIN 1. ANODE 2. CATHODE 3. ANODE 4. CATHODE PIN 1. GATE 2. ANODE 3. CATHODE 4. ANODE PIN 1. GATE 2. DRAIN

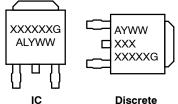
Z

**BOTTOM VIEW** 

С

3. EMITTE 4. COLLE	ER .	3. SOURCE 4. DRAIN	3. ANC 4. CAT	DE	3. GATE 4. ANODE	3.	CATHODE ANODE
STYLE 6: PIN 1. MT1 2. MT2 3. GATE	STYLE 7: PIN 1. GATE 2. COLLE 3. EMITT	ECTOR	E 8: 1. N/C 2. CATHODE 3. ANODE	STYLE 9: PIN 1. ANO 2. CATI 3. RES		2. /	0: CATHODE ANODE CATHODE
4. MT2	4. COLLE		4. CATHODE	4. CAT			ANODE

## **MARKING DIAGRAM\***



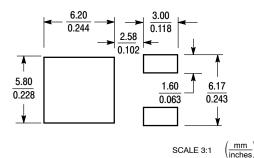
XXXXXX = Device Code = Assembly Location Α L = Wafer Lot Υ = Year WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking.

= Pb-Free Package

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#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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