Silicon Switching Diode

Features

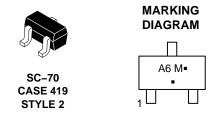
- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



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A6 = Specific Device Code M = Date Code = Pb-Free Package

(*Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BAS16WT1G	SC-70 (Pb-Free)	3000 / Tape & Reel
SBAS16WT1G	SC–70 (Pb–Free)	3000 / Tape & Reel
NSVBAS16WT3G	SC–70 (Pb–Free)	10000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Rating	Symbol	Value	Unit			
Continuous Reverse Voltage	V _R	100	V			
Recurrent Peak Forward Current	I _R	200	mA			
Peak Forward Surge Current Pulse Width = 10 μ s	I _{FM(surge)}	500	mA			
Total Power Dissipation, One Diode Loaded $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$ Mounted on a Ceramic Substrate (10 x 8 x 0.6 mm)	P _D	200 1.6	mW mW/°C			
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C			

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient One Diode Loaded Mounted on a Ceramic Substrate (10 x 8 x 0.6 mm)	$R_{ hetaJA}$	625	°C/W

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Forward Voltage $(I_F = 1.0 \text{ mA})$ $(I_F = 10 \text{ mA})$ $(I_F = 50 \text{ mA})$ $(I_F = 150 \text{ mA})$	V _F	- - - -	715 866 1000 1250	mV
Reverse Current $(V_R = 100 V)$ $(V_R = 75 V, T_J = 150^{\circ}C)$ $(V_R = 25 V, T_J = 150^{\circ}C)$	I _R	- - -	1.0 50 30	μΑ
Capacitance ($V_R = 0, f = 1.0 \text{ MHz}$)	C _D	-	2.0	pF
Reverse Recovery Time $(I_F = I_R = 10 \text{ mA}, R_L = 50 \Omega)$ (Figure 1)	t _{rr}	-	6.0	ns
Stored Charge (I _F = 10 mA to V _R = 6.0 V, R _L = 500 Ω) (Figure 2)	QS	-	45	PC
Forward Recovery Voltage ($I_F = 10 \text{ mA}, t_r = 20 \text{ ns}$) (Figure 3)	V _{FR}	-	1.75	V

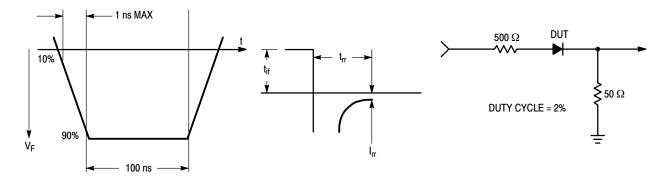


Figure 1. Reverse Recovery Time Equivalent Test Circuit

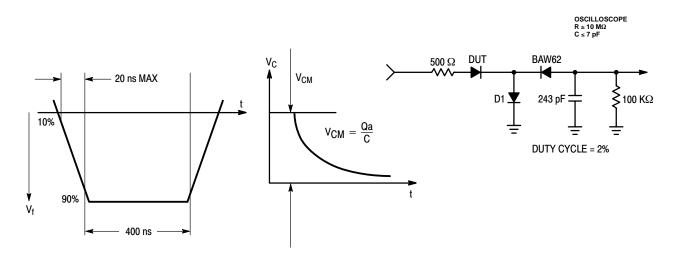


Figure 2. Stored Charge Equivalent Test Circuit

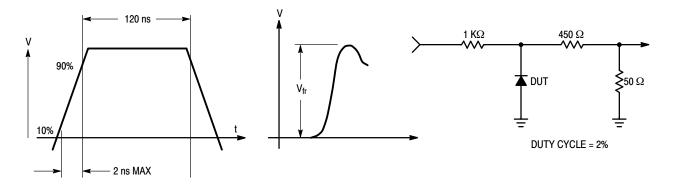


Figure 3. Forward Recovery Voltage Equivalent Test Circuit

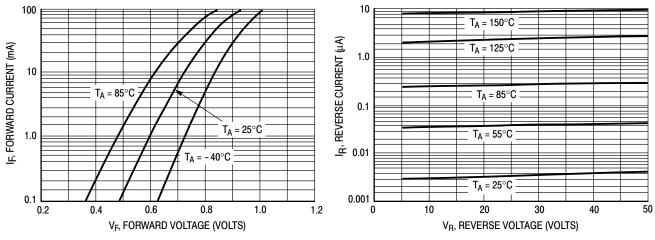




Figure 5. Leakage Current

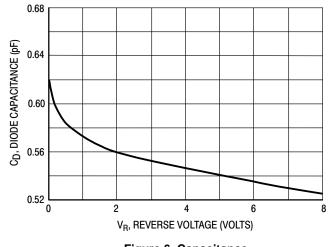
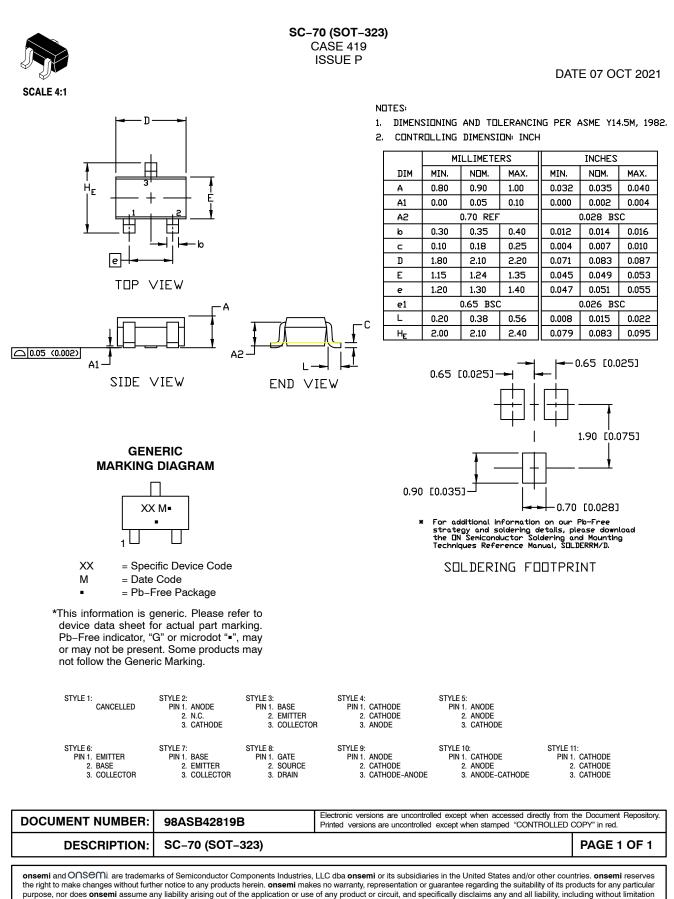


Figure 6. Capacitance

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