# **High Voltage Transistor**

# **PNP Silicon**

The MMBT5401M3 device is a spin-off of our popular SOT-23 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

#### **Features**

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

| Rating                         | Symbol           | Value | Unit |
|--------------------------------|------------------|-------|------|
| Collector - Emitter Voltage    | V <sub>CEO</sub> | -150  | Vdc  |
| Collector - Base Voltage       | V <sub>CBO</sub> | -160  | Vdc  |
| Emitter - Base Voltage         | V <sub>EBO</sub> | -5.0  | Vdc  |
| Collector Current - Continuous | Ic               | -60   | mAdc |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

| Characteristic   | Symbol                            | Max         | Unit        |
|--|-----------------------------------|-------------|-------------|
| Total Device Dissipation FR-5 Board (Note 1) T <sub>A</sub> = 25°C Derate Above 25°C | P <sub>D</sub>                    | 130         | mW<br>mW/°C |
| Thermal Resistance,<br>Junction-to-Ambient (Note 1)                                  | $R_{\theta JA}$                   | 470         | °C/W        |
| Junction and Storage Temperature   | T <sub>J</sub> , T <sub>stg</sub> | -55 to +150 | °C          |

<sup>1.</sup> FR-5 @ 100 mm<sup>2</sup>, 1.0 oz. copper traces, still air.

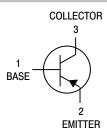


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SOT-723 CASE 631AA



## **MARKING DIAGRAM**



RJ = Specific Device Code M = Date Code

# **ORDERING INFORMATION**

| Device           | Package              | Shipping <sup>†</sup> |
|------------------|----------------------|-----------------------|
| MMBT5401M3T5G    | SOT-723<br>(Pb-Free) | 8000 / Tape &<br>Reel |
| NSVMMBT5401M3T5G | SOT-723<br>(Pb-Free) | 8000 / Tape &<br>Reel |

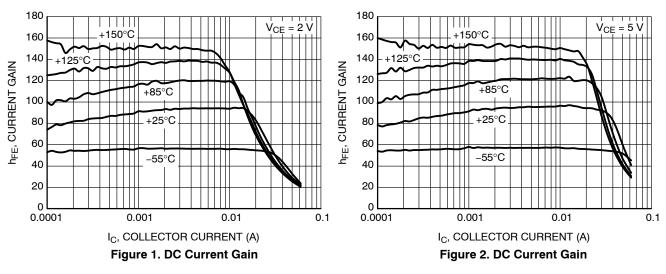
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

| Characteristic  | Symbol               | Min            | Тур            | Max            | Unit |
|---|----------------------|----------------|----------------|----------------|------|
| OFF CHARACTERISTICS   |                      |                |                |                |      |
| Collector – Emitter Breakdown Voltage $(I_C = -1.0 \text{ mA}, I_B = 0)$  | V <sub>(BR)CEO</sub> | -150           | _              | -              | V    |
| Collector – Base Breakdown Voltage $(I_C = -100 \mu A, I_E = 0)$  | V <sub>(BR)CBO</sub> | -160           | -              | -              | V    |
| Emitter – Base Breakdown Voltage ( $I_E = -10 \mu A, I_C = 0$ )   | V <sub>(BR)EBO</sub> | -5.0           | -              | -              | V    |
| Collector-Base Cutoff Current (V <sub>CB</sub> = -120 V, I <sub>E</sub> = 0)  | I <sub>CBO</sub>     | -              | -1.6           | -100           | nA   |
| Emitter Cutoff Current (V <sub>BE</sub> = -5 V)   | I <sub>EBO</sub>     | -              | -0.20          | -100           | nA   |
| ON CHARACTERISTICS  |                      |                |                |                |      |
| DC Current Gain $ \begin{array}{l} (I_C = -1.0 \text{ mA, V}_{CE} = -5.0 \text{ V}) \\ (I_C = -10 \text{ mA, V}_{CE} = -5.0 \text{ V}) \\ (I_C = -50 \text{ mA, V}_{CE} = -5.0 \text{ V}) \end{array} $ | h <sub>FE</sub>      | 50<br>60<br>20 | 80<br>90<br>40 | -<br>240<br>-  | -    |
| Collector – Emitter Saturation Voltage<br>( $I_C = -10 \text{ mA}$ , $I_B = -1.0 \text{ mA}$ )<br>( $I_C = -50 \text{ mA}$ , $I_B = -5.0 \text{ mA}$ )  | V <sub>CE(sat)</sub> |                | -0.09<br>-0.15 | -0.25<br>-0.60 | V    |
| Base – Emitter Saturation Voltage<br>( $I_C = -10$ mA, $I_B = -1.0$ mA)<br>( $I_C = -50$ mA, $I_B = -5.0$ mA)   | V <sub>BE(sat)</sub> | -<br>-         | -0.76<br>-0.92 | -1.0<br>-1.0   | V    |
| SMALL-SIGNAL CHARACTERISTICS  |                      |                |                |                |      |
| Current – Gain — Bandwidth Product<br>(I <sub>C</sub> = -10 mA, V <sub>CE</sub> = -5.0 V, f = 100 MHz)  | f <sub>T</sub>       | 100            | 180            | 300            | MHz  |
| Input Capacitance<br>(V <sub>EB</sub> = -3 V, I <sub>C</sub> = 0, f = 1.0 MHz)  | C <sub>ibo</sub>     | -              | 12.5           | 15             | pF   |
| Output Capacitance<br>(V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1.0 MHz)  | C <sub>obo</sub>     | -              | 1.5            | 6.0            | pF   |
| Small Signal Current Gain ( $I_C = -1.0 \text{ mA}, V_{CE} = -10 \text{ V}, f = 1.0 \text{ kHz}$ )  | h <sub>fe</sub>      | 40             | -              | 200            | _    |
| Noise Figure (I <sub>C</sub> = -200 $\mu$ A, V <sub>CE</sub> = -5.0 V, R <sub>S</sub> = 10 $\Omega$ , f = 1.0 kHz)  | NF                   | _              | _              | 8.0            | dB   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **TYPICAL CHARACTERISTICS**



V<sub>CE</sub>, COLLECTOR-EMITTER VOLTAGE (V) 1.6 40 mA  $I_C/I_B = 10$ 30 mA 50 mA V<sub>CE(sat)</sub>, COLLECTOR-EMITTER SATURATION VOLTAGE (V) 1.4 20 mA 1.2 10 mA 1.0 60 mA  $I_C = 1.0 \text{ mA}$ 0.2 8.0 0.6 0.4 +85°C +25°C 0.2 0.005 0.05 0.5 5 50 0.0001 0.001 0.01 IB, BASE CURRENT (mA) I<sub>C</sub>, COLLECTOR CURRENT (A)

Figure 3. Collector Saturation Region

Figure 4. Collector-Emitter Saturation Region

-55°C

0.1

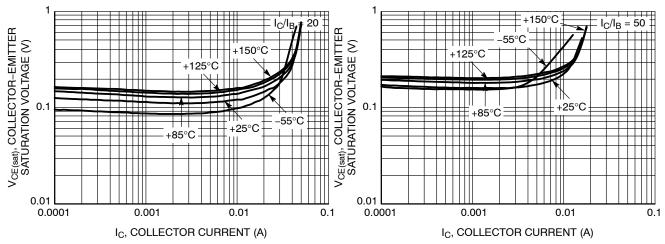


Figure 5. Collector-Emitter Saturation Region

Figure 6. Collector-Emitter Saturation Region

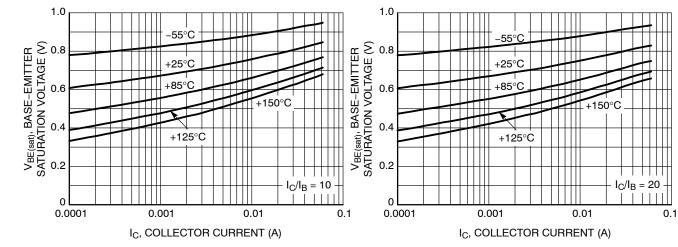


Figure 7. Base-Emitter Saturation Voltage

Figure 8. Base-Emitter Saturation Voltage

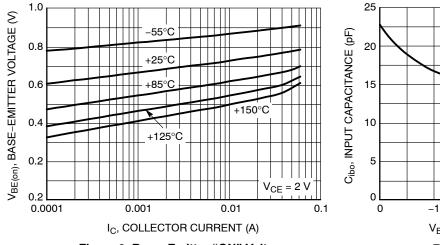


Figure 9. Base-Emitter "ON" Voltage

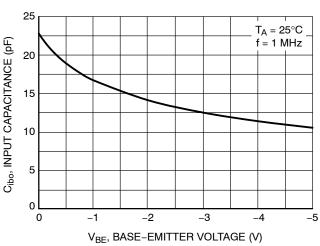


Figure 10. Input Capacitance

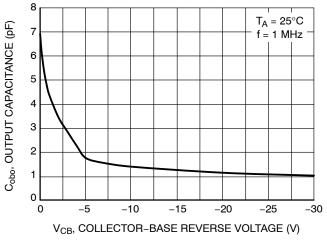


Figure 11. Output Capacitance

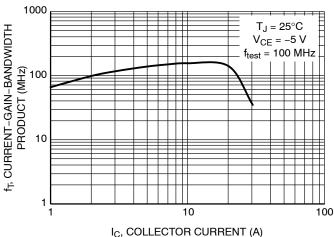


Figure 12. Current Gain Bandwidth Product

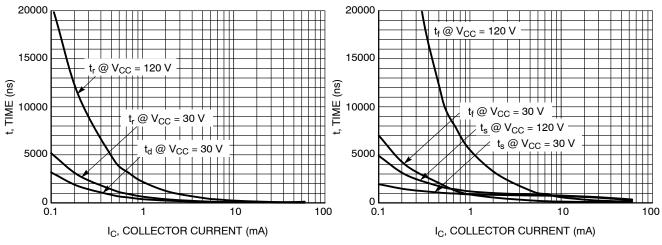


Figure 13. Turn-On Time

Figure 14. Turn-Off Time

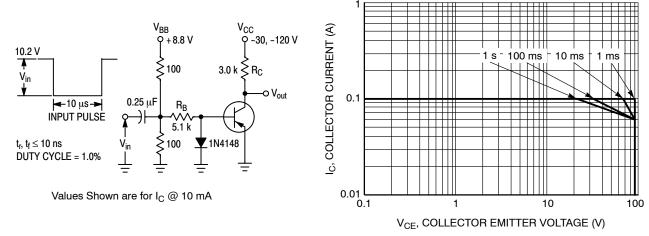


Figure 15. Switching Time Test Circuit

Figure 16. Safe Operating Area



SOT-723 CASE 631AA-01 ISSUE D

**DATE 10 AUG 2009** 

# NOTES:

- NOTES.

  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

  2. CONTROLLING DIMENSION: MILLIMETERS.

  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD
- FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

|     | MILLIMETERS |      |      |  |
|-----|-------------|------|------|--|
| DIM | MIN         | NOM  | MAX  |  |
| Α   | 0.45        | 0.50 | 0.55 |  |
| b   | 0.15        | 0.21 | 0.27 |  |
| b1  | 0.25        | 0.31 | 0.37 |  |
| С   | 0.07        | 0.12 | 0.17 |  |
| D   | 1.15        | 1.20 | 1.25 |  |
| E   | 0.75        | 0.80 | 0.85 |  |
| е   | 0.40 BSC    |      |      |  |
| ΗE  | 1.15        | 1.20 | 1.25 |  |
| L   | 0.29 REF    |      |      |  |
| 12  | 0.15        | 0.20 | 0.25 |  |

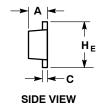
# **L2** 0.15 0.20 0.25 **GENERIC MARKING DIAGRAM\***

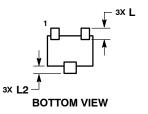


= Specific Device Code XX Μ = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

# -X-2X b ⊕ 0.08 X Y **TOP VIEW**

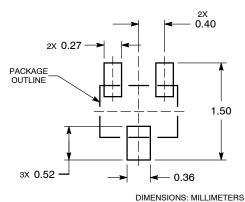




STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

**RECOMMENDED SOLDERING FOOTPRINT\*** 



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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|------------------|-------------|---|-------------|
| DESCRIPTION:     | SOT-723     |   | PAGE 1 OF 1 |

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NTE15 NTE16001