

Silicon Carbide (SiC) MOSFET - EliteSiC, 28 mohm, 1700 V, M1, D2PAK-7L NTBG028N170M1

Features

- Typ. $R_{DS(on)} = 28 \text{ m}\Omega$
- Ultra Low Gate Charge (typ. $Q_{G(tot)} = 222 \text{ nC}$)
- Low Effective Output Capacitance (typ. $C_{oss} = 200 \text{ pF}$)
- 100% Avalanche Tested
- RoHS Compliant

Typical Applications

- UPS
- DC-DC Converter
- Boost Converter

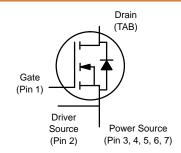
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	1700	V
Gate-to-Source Voltage	Э		V_{GS}	-15/+25	V
Recommended Operation Values of Gate–to–Source Voltage		T _C < 175°C	V_{GSop}	-5/+20	V
Continuous Drain Current (Note 2)	Steady $T_C = 25^{\circ}C$ State		I _D	71	Α
Power Dissipation (Note 2)			P _D	428	W
Continuous Drain Current (Note 2)	Steady State	T _C = 100°C	I _D	53	Α
Power Dissipation (Note 2)			P _D	214	W
Pulsed Drain Current (Note 3)	T _A = 25°C		I _{DM}	195	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	–55 to +175	°C
Source Current (Body Diode)			I _S	99	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 30 A, L = 1 mH) (Note 4)			E _{AS}	450	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			TL	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Surface mounted on a FR-4 board using1 in2 pad of 2 oz copper.
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 3. Repetitive rating, limited by max junction temperature.
- 4. EAS of 450 mJ is based on starting $T_J = 25^{\circ}C$; L = 1 mH, $I_{AS} = 30$ A, $V_{DD} = 120$ V, $V_{GS} = 18$ V.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
1700 V	40 mΩ @ 20 V	71 A



N-CHANNEL MOSFET



D2PAK-7L CASE 418BJ

MARKING DIAGRAM

AYWWZZ BG028N 170M1

A = Assembly Location

Y = Year

WW = Work Week
ZZ = Lot Traceability

BG028N170M1 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping [†]
NTBG028N170M1	D2PAK-7L	800 ea/ Tape&Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Тур	Max	Unit
Junction-to-Case - Steady State (Note 2)	$R_{\theta JC}$	0.35		°C/W
Junction-to-Ambient - Steady State (Notes 1, 2)	$R_{\theta JA}$		40	

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D =$: 1 mA	1700			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 1 mA, referenced to 25°C			0.44		V/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$,	T _J = 25°C			100	μΑ
		V _{DS} = 1700 V	T _J = 175°C			1	mA
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +25/-15 \text{ V},$	$V_{DS} = 0 V$			±1	μΑ
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D =$	= 20 mA	1.8	3.0	4.3	V
Recommended Gate Voltage	V_{GOP}			-5		+20	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 20 V, I _D = 60 A	A, T _J = 25°C		28	40	mΩ
		$V_{GS} = 20 \text{ V}, I_D = 60 \text{ A}$	A, T _J = 175°C		57		
Forward Transconductance	9FS	V _{DS} = 20 V, I _D	= 60 A		27		S
CHARGES, CAPACITANCES & GATE RES	SISTANCE						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 800 V			4160		pF
Output Capacitance	C _{OSS}				200		
Reverse Transfer Capacitance	C _{RSS}				15		
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/20 \text{ V}, V_{DS} = 800 \text{ V},$ $I_{D} = 60 \text{ A}$			222		nC
Threshold Gate Charge	Q _{G(TH)}				40		
Gate-to-Source Charge	Q _{GS}				72		
Gate-to-Drain Charge	Q_{GD}				53		
Gate-Resistance	R_{G}	f = 1 MHz			6.1		Ω
SWITCHING CHARACTERISTICS	-	-	•	-	•	-	=
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -5/20$) V,		47		ns
Rise Time	t _r	V _{DS} = 1200 I _D = 60 A			18		
Turn-Off Delay Time	t _{d(OFF)}	$\ddot{R}_{G} = 2~\Omega$ inductive lo	2		121		
Fall Time	t _f	inductive to	au		13		
Turn-On Switching Loss	E _{ON}	1			1311		μJ
Turn-Off Switching Loss	E _{OFF}				683		
Total Switching Loss	E _{tot}				1994		
DRAIN-SOURCE DIODE CHARACTERIST	rics						
Continuous Drain-Source Diode Forward Current	I _{SD}	$V_{GS} = -5 \text{ V, T}_{J}$	= 25°C			99	Α
Pulsed Drain–Source Diode Forward Current (Note 3)	I _{SDM}					195	
Forward Diode Voltage	V_{SD}	$V_{GS} = -5 \text{ V}, I_{SD} = 60$	A, T _J = 25°C		4.3		V
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/20 \text{ V, } I_{SD} = 60 \text{ A,}$ $dI_S/dt = 1000 \text{ A/}\mu\text{s}$			33		ns
Reverse Recovery Charge	Q_{RR}				247		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

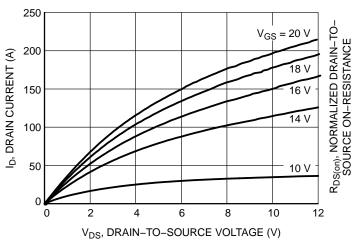


Figure 1. On-Region Characteristics

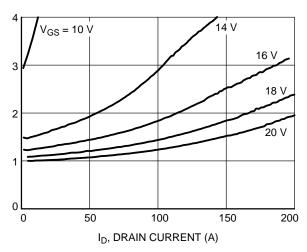


Figure 2. Normalized On–Resistance vs. Drain Current and Gate Voltage

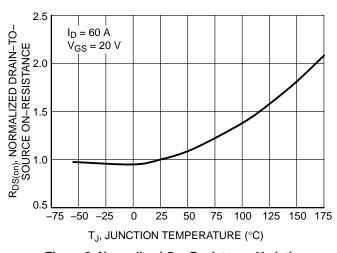


Figure 3. Normalized On–Resistance Variation with Temperature

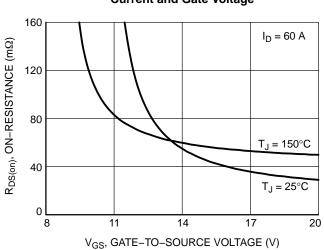


Figure 4. On-Resistance vs. Gate-to-Source Voltage

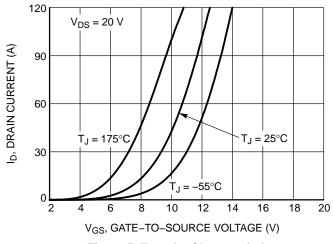


Figure 5. Transfer Characteristics

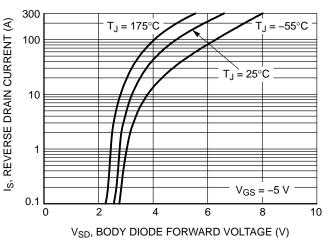
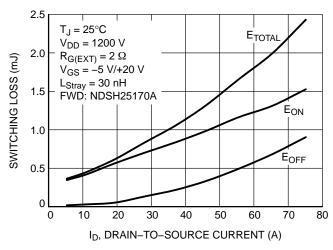


Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS



3.0 $T_J = 25^{\circ}C$ $V_{DD} = 1200 \text{ V}$ 2.5 $R_{G(EXT)} = 2 \Omega$ $V_{GS} = -5 \text{ V/+20 V}$ $L_{Stray} = 30 \text{ nH}$ FWD: NDSH25170A SWITCHING LOSS (mJ) **E**TOTAL 2.0 **EON** 1.5 1.0 **E**OFF 0.5 0 30 40 70 10 50 60 90 ID, DRAIN-TO-SOURCE CURRENT (A)

Figure 7. SW Loss vs. ID 25°C

Figure 8. SW Loss vs. ID 125°C

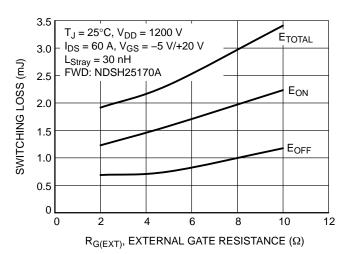


Figure 9. SW Loss vs. Rg

TYPICAL CHARACTERISTICS

10K

1K

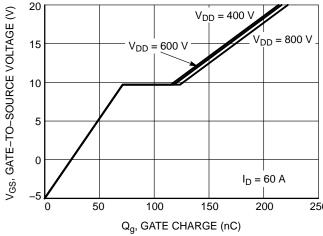
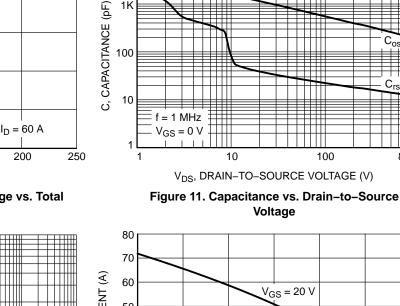


Figure 10. Gate-to-Source Voltage vs. Total Charge



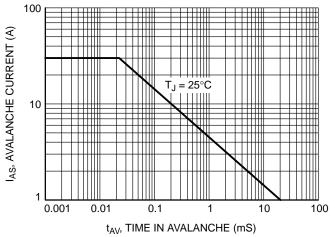
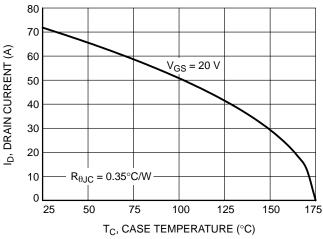


Figure 12. Unclamped Inductive Switching Capability



Coss

800

100

Figure 13. Maximum Continuous Drain **Current vs. Case Temperature**

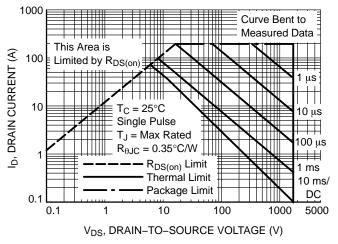


Figure 14. Maximum Rated Forward Biased Safe Operating Area

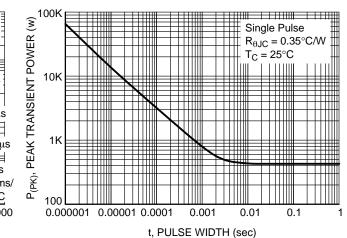


Figure 15. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS

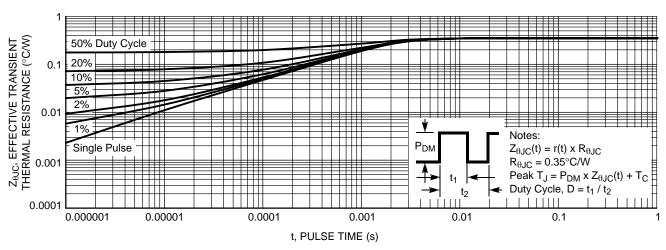


Figure 16. Transient Thermal Impedance

1

D²PAK7 (TO-263-7L HV) CASE 418BJ **ISSUE B**

DATE 16 AUG 2019

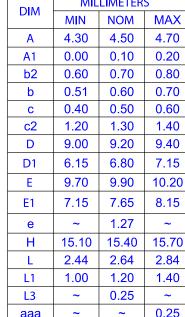
NOTES:

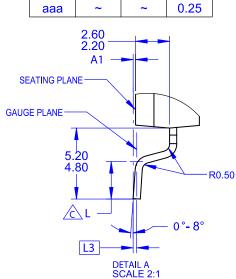
- A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED. B. ALL DIMENSIONS ARE IN MILLIMETERS.
- OUT OF JEDEC STANDARD VALUE.

 D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.

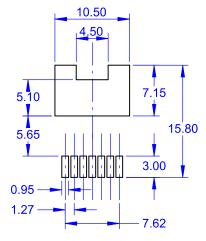
 E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.30	4.50	4.70		
A1	0.00	0.10	0.20		
b2	0.60	0.70	0.80		
b	0.51	0.60	0.70		
С	0.40	0.50	0.60		
c2	1.20	1.30	1.40		
D	9.00	9.20	9.40		
D1	6.15	6.80	7.15		
Е	9.70	9.90	10.20		
E1	7.15	7.65	8.15		
е	~	1.27	~		
Н	15.10	15.40	15.70		
L	2.44	2.64	2.84		
L1	1.00	1.20	1.40		
L3	~	0.25	~		
aaa	~	~	0.25		

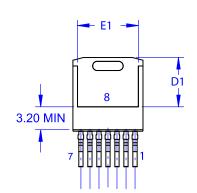




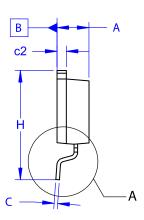
A	— E —	_ L
D		
1		
b2 →		
e	h —	_



LAND PATTERN RECOMMENDATION



⊕ | aaa | B | A | M |



GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code = Assembly Location

= Year WW = Work Week = Pb-Free Package

*This information is generic. Please refer to

device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot "■", may
or may not be present. Some products may
not follow the Generic Marking.
· ·

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7 NTE2384 NTE6400A DMC2700UDMQ-7 DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B

IPS60R3K4CEAKMA1 DMN1006UCA6-7 DMN16M9UCA6-7 STF5N65M6 IRF40H233XTMA1 IPSA70R950CEAKMA1

IPSA70R2K0CEAKMA1 STU5N65M6 C3M0021120D DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13

IPS60R360PFD7SAKMA1 DMN2990UFB-7B SSM3K35CT,L3F IPLK60R1K0PFD7ATMA1 2N7002W-G