# onsemi

## **MOSFET** - Power, Single N-Channel, TOLL 60 V, 0.75 mΩ, 470 A

# NTBLS0D7N06C

#### Features

- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Q<sub>G</sub> and Capacitance to Minimize Driver Losses
- Lowers Switching Noise/EMI
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Typical Applications**

- Power Tools, Battery Operated Vacuums
- UAV/Drones, Material Handling
- BMS/Storage, Home Automation

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V <sub>DSS</sub>	60	V
Gate-to-Source Voltage	Э		V <sub>GS</sub>	±20	V
Continuous Drain Current $R_{\theta JC}$ (Note 2)	Steady	T <sub>C</sub> = 25°C	۱ <sub>D</sub>	470	A
Power Dissipation $R_{\theta JC}$ (Note 2)	State	T <sub>C</sub> = 25°C	PD	314	W
Continuous Drain Current R <sub>θJA</sub> (Notes 1, 2)	Steady State	T <sub>A</sub> = 25°C	Ι <sub>D</sub>	54	A
Power Dissipation $R_{\theta JA}$ (Notes 1, 2)	Slale	T <sub>A</sub> = 25°C	PD	4.2	W
Pulsed Drain Current	T <sub>A</sub> = 25	$T_A = 25^{\circ}C$ , $t_p = 10 \ \mu s$		900	А
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	–55 to +175	°C	
Source Current (Body Diode)		I <sub>S</sub>	260	А	
Single Pulse Drain-to-Source Avalanche Energy ( $I_{L(pk)} = 40 \text{ A}$ )		E <sub>AS</sub>	800	mJ	
Lead Temperature Soldering Reflow for Solder- ing Purposes (1/8" from case for 10 s)			ΤL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

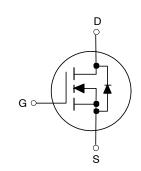
#### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 2)	$R_{\theta JC}$	0.48	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	36	

1. Surface-mounted on FR4 board using a 1 in<sup>2</sup> pad size, 2 oz. Cu pad.

The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
60 V	$0.75~\mathrm{m}\Omega$ @ 10 V	470.4
60 V	1.2 mΩ @ 6 V	470 A





H-PSOF8L CASE 100CU

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTBLS0D7N06C	H-PSOF8L (Pb-Free)	2000 / Tape & Reel

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

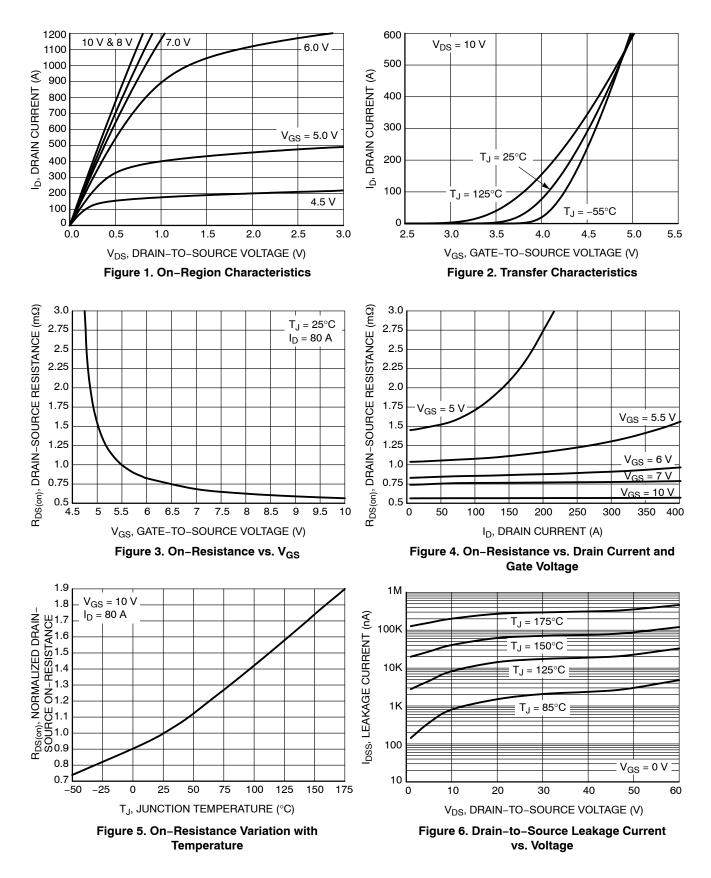
#### Table 1. ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = $25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Cond	litions	Min	Тур	Max	Units
OFF CHARACTERISTICS					4		
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$I_{D} = 250 \ \mu A, \ V_{GS} = 0 \ V$		60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	$I_D = 661 \ \mu$ A, ref to 25°C			26.5		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	$T_J = 25^{\circ}C$			10	μA
		$V_{GS} = 0 V$	T <sub>J</sub> = 125°C			100	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>C</sub>	<sub>àS</sub> = 20 V			100	nA
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{GS} = V_{DS}, I_{D}$	= 661 μA	2.0	2.8	4.0	V
Negative Threshold Temperature Coefficient	V <sub>GS(th)</sub> /T <sub>J</sub>	I <sub>D</sub> = 661 μA, r	ef to 25°C		9.8		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I	<sub>D</sub> = 80 A		0.56	0.75	mΩ
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 6 V, I <sub>I</sub>	<sub>D</sub> = 66 A		0.85	1.20	mΩ
Forward Transconductance	9 <sub>FS</sub>	V <sub>DS</sub> = 10 V, I	<sub>D</sub> = 80 A		310		S
Gate-Resistance	R <sub>G</sub>	T <sub>A</sub> = 25°C			0.6		Ω
CHARGES & CAPACTIANCES						-	
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 30 V, f = 10 kHz			13730		pF
Output Capacitance	C <sub>oss</sub>				6912		pF
Reverse Transfer Capacitance	C <sub>rss</sub>				92		pF
Total Gate Charge	Q <sub>G(tot)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 30 V,			170		nC
Threshold Gate Charge	Q <sub>G(th)</sub>	I <sub>D</sub> = 80	) A		39		nC
Gate-to-Source Charge	Q <sub>gs</sub>				62		nC
Gate-to-Drain Charge	Q <sub>gd</sub>				16		nC
Total Gate Charge	Q <sub>G(tot)</sub>	$V_{GS}$ = 6 V, $V_{DS}$ = 30 V, I <sub>D</sub> = 80 A			102		nC
SWITCHING CHARACTERISTICS, $V_{GS} = 1$	<b>0 V</b> (Note 3)					-	
Turn–On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10 V, V I <sub>D</sub> = 80 A, R	<sub>DS</sub> = 30 V,		37		ns
Rise Time	tr	I <sub>D</sub> = 80 A, R <sub>G</sub> = 6 Ω			57		ns
Turn-Off Delay Time	t <sub>d(off)</sub>				146		ns
Fall Time	t <sub>f</sub>			105		ns	
DRAIN-SOURCE DIODE CHARACTERIST	ICS						
Forward Diode Voltage	V <sub>SD</sub>	$I_{\rm S}$ = 80 A, $V_{\rm GS}$ = 0 V	$T_{\rm J} = 25^{\circ}C$		0.79	1.2	V
		$I_{\rm S}$ = 80 A, $V_{\rm GS}$ = 0 V	T <sub>J</sub> = 125°C		0.66		V
Reverse Recovery Time	t <sub>er</sub>	$V_{ee} = 0 V dl_e/d_e$	- 100 A/us	1	132		ns

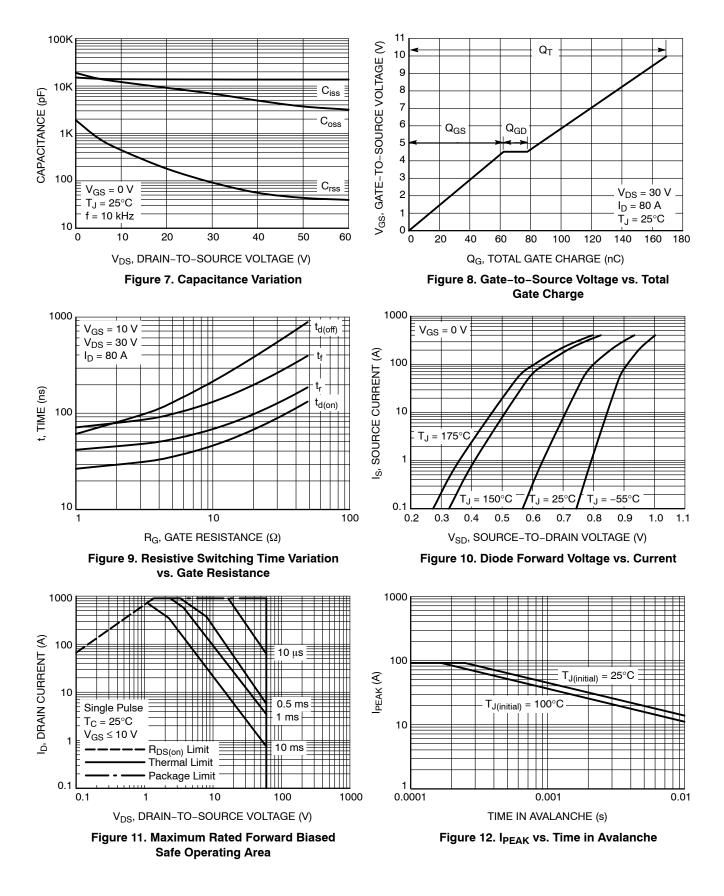
		15 - 00 / 1, • GS - 0 •	13 - 120 0	0.00	•
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0 V, dI_S/d_1$		132	ns
Charge Time	t <sub>a</sub>	I <sub>S</sub> = 66	3 A	64	ns
Discharge Time	t <sub>b</sub>			68	ns
Reverse Recovery Charge	Q <sub>rr</sub>			386	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 3. Switching characteristics are independent of operating junction temperatures

#### **TYPICAL CHARACTERISTICS**



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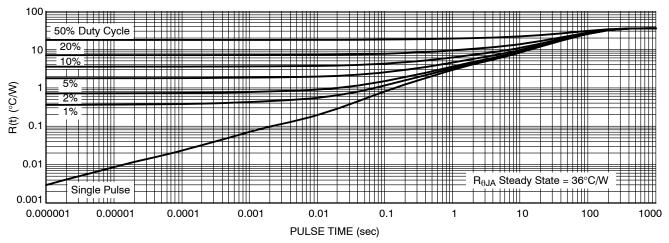
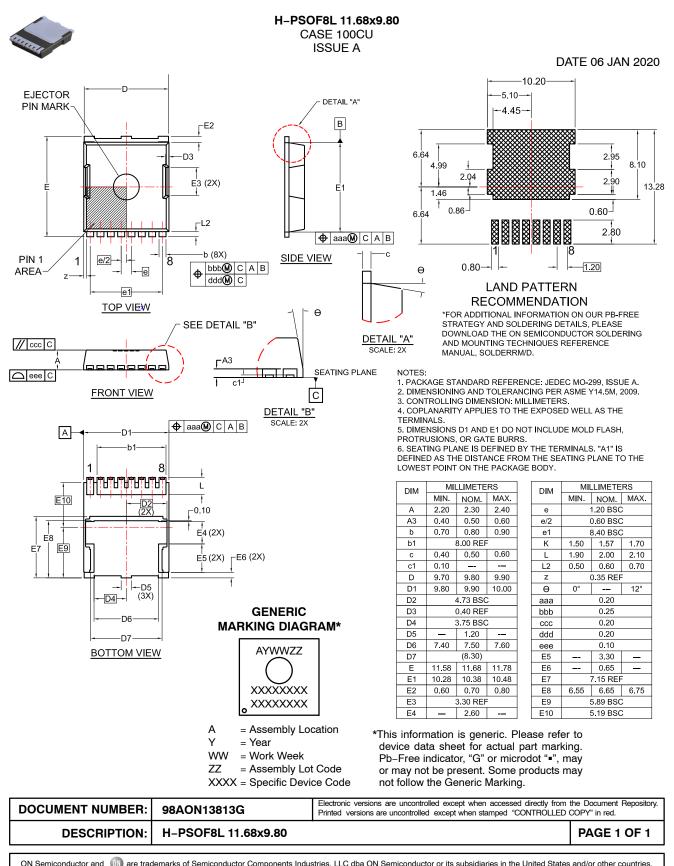


Figure 13. Thermal Characteristics (Junction-to-Ambient)





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