MOSFET – SiC Power, Single N-Channel, TO247-4L 1200 V, 40 mΩ, 58 A

NTH4L040N120SC1

Features

- Typ. $R_{DS(on)} = 40 \text{ m}\Omega$
- Ultra Low Gate Charge $(Q_{G(tot)} = 106 \text{ nC})$
- High Speed Switching with Low Capacitance (Coss = 137 pF)
- 100% Avalanche Tested
- $T_J = 175^{\circ}C$
- This Device is Pb-Free and is RoHS Compliant

Typical Applications

- UPS
- DC/DC Converter
- Boost Inverter

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	1200	٧
Gate-to-Source Voltage			V _{GS} -15/+25		V
Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-5/+20	V	
Continuous Drain Current (Note 2)	Steady T _C = 25°C State		I _D	58	Α
Power Dissipation (Note 2)			P _D	319	W
Continuous Drain Current (Notes 1, 2)	Steady State	T _C = 100°C	I _D	41	Α
Power Dissipation (Notes 1, 2)			P _D	160	W
Pulsed Drain Current (Note 3)	T _A = 25°C		I _{DM}	232	Α
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C	
Source Current (Body Diode)			I _S	32	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 34 A, L = 1 mH) (Note 4)			E _{AS}	578	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)		TL	300	ç	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

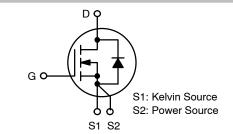
- JA is constant value to follow guide table of LV/HV discrete final datasheet generation.
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 3. Repetitive rating, limited by max junction temperature.
- 4. EAS of 578 mJ is based on starting $T_J = 25^{\circ}\dot{C}$; L = 1 mH, $I_{AS} = 34$ A, $V_{DD} = 120$ V, $V_{GS} = 20$ V.



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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
1200 V	56 mΩ @ 20 V	58 A



N-CHANNEL MOSFET



MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

NTH4L040N120SC1 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
NTH4L040N120SC1	TO247-4L	30 ea / Tube

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Note 2)	$R_{ heta JC}$	0.47	°C/W
Junction-to-Ambient - Steady State (Notes 1, 2)	$R_{\theta JA}$	40	

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA		1200	_	_	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 1 mA, referenced to 25°C		-	0.45	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	$T_J = 25^{\circ}C$	-	-	100	μΑ
		V _{DS} = 1200 V	T _J = 175°C	-	-	1	mA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = +25/-15 V, V _{DS} = 0 V		ı	-	±1	μΑ
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 10 \text{ m/s}$	١	1.8	3	4.3	V
Recommended Gate Voltage	V_{GOP}			-5	-	+20	V
Drain-to-Source On Resistance	R _{DS(on)}	$V_{GS} = 20 \text{ V}, I_D = 35 \text{ A},$	T _J = 25°C	_	40	56	mΩ
		$V_{GS} = 20 \text{ V}, I_D = 35 \text{ A},$	T _J = 175°C	_	70	100	
Forward Transconductance	9 _{FS}	$V_{DS} = 20 \text{ V}, I_D = 35 \text{ A}$		_	20	-	S
CHARGES, CAPACITANCES & GATE RES	SISTANCE						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V	V _{DS} = 800 V	-	1762	_	pF
Output Capacitance	C _{OSS}			-	137	-	
Reverse Transfer Capacitance	C _{RSS}			-	11	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/20 \text{ V}, V_{DS} = 600 \text{ V},$ $I_{D} = 47 \text{ A}$		-	106	-	nC
Threshold Gate Charge	Q _{G(TH)}			-	16	-	
Gate-to-Source Charge	Q_{GS}			-	34	-	
Gate-to-Drain Charge	Q_GD			-	26	-	
Gate-Resistance	R_{G}	f = 1 MHz		1	2.4	_	Ω
SWITCHING CHARACTERISTICS, VGS =	10 V						
Turn-On Delay Time	t _{d(ON)}	$\begin{array}{l} V_{GS} = -5/20 \text{ V,} \\ V_{DS} = 800 \text{ V,} \\ I_D = 47 \text{ A,} \\ R_G = 4.7 \Omega \\ \text{inductive load} \end{array}$		-	17	30	ns
Rise Time	t _r			-	20	36	
Turn-Off Delay Time	t _{d(OFF)}			-	32	51	
Fall Time	t _f			-	10	20	
Turn-On Switching Loss	E _{ON}			1	411	-	μJ
Turn-Off Switching Loss	E _{OFF}			1	205	-	
Total Switching Loss	E _{tot}			1	616	-	
DRAIN-SOURCE DIODE CHARACTERIST	ics						
Continuous Drain-Source Diode Forward Current	I _{SD}	V _{GS} = -5 V, T _J = 25°C		-	-	32	Α
Pulsed Drain-Source Diode Forward Current (Note 3)	I _{SDM}			-	-	232	
Forward Diode Voltage	V _{SD}	V _{GS} = -5 V, I _{SD} = 17.5 A, T _J = 25°C		_	3.7	_	V
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/20 \text{ V, } I_{SD} = 47 \text{ A,}$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}$		-	24	_	ns
Reverse Recovery Charge	Q_{RR}			_	124.8	_	nC

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS						
Reverse Recovery Energy	E _{REC}	$V_{GS} = -5/20 \text{ V}, I_{SD} = 47 \text{ A},$	-	8.4	-	μJ
Peak Reverse Recovery Current	I _{RRM}	$dI_S/dt = 1000 A/\mu s$	_	10.4	-	Α
Charge time	Ta		_	12.4	-	ns
Discharge time	Tb		_	11.6	-	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

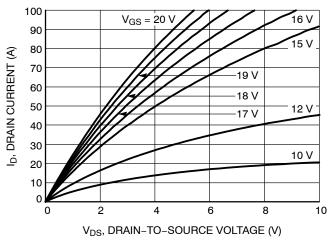


Figure 1. On-Region Characteristics

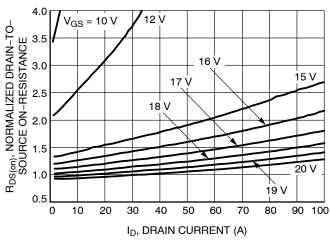


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

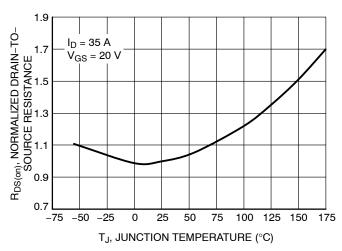


Figure 3. On–Resistance Variation with Temperature

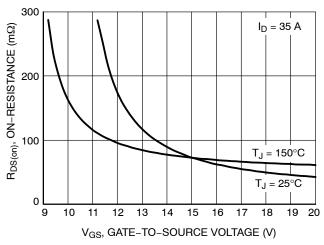


Figure 4. On-Resistance vs. Gate-to-Source Voltage

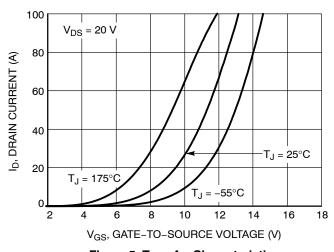


Figure 5. Transfer Characteristics

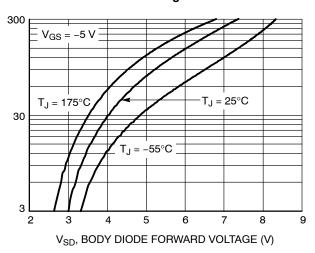


Figure 6. Diode Forward Voltage vs. Current

REVERSE DRAIN CURRENT (A)

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TYPICAL CHARACTERISTICS

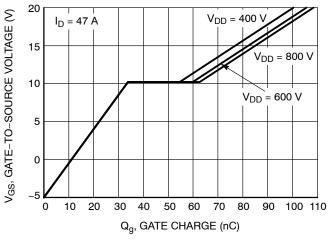


Figure 7. Gate-to-Source Voltage vs. Total Charge

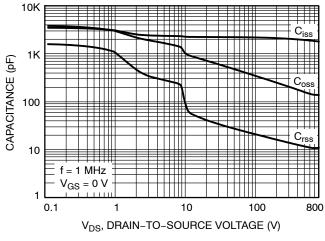


Figure 8. Capacitance vs. Drain-to-Source Voltage

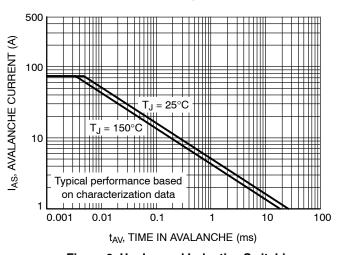


Figure 9. Unclamped Inductive Switching Capability

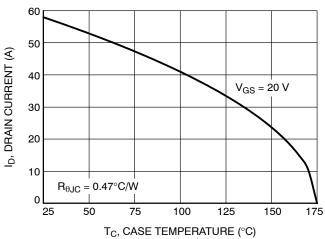


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

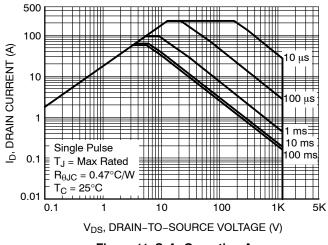


Figure 11. Safe Operating Area

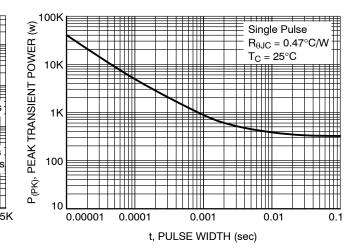


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS

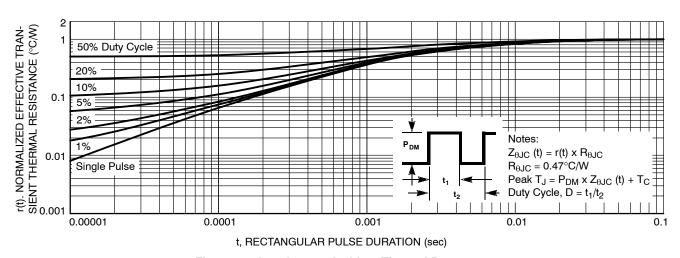
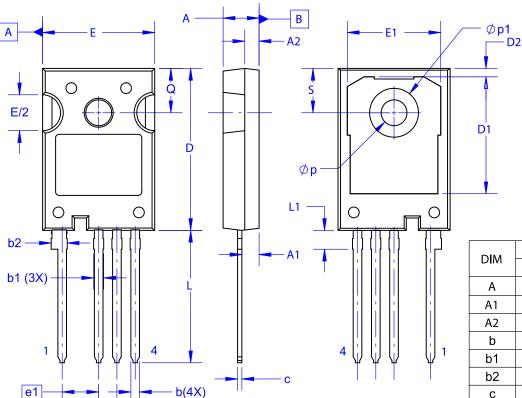


Figure 13. Junction-to-Ambient Thermal Response

TO-247-4LD CASE 340CJ **ISSUE A**

DATE 16 SEP 2019



NOTES:

e 2X-0.254 M

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 B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
 FLASH, AND TIE BAR EXTRUSIONS.
 C. ALL DIMENSIONS ARE IN MILLIMETERS.
 D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MIN	NOM	MAX		
Α	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	1.07	1.20	1.33		
b1	1.20	1.40	1.60		
b2	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.25	16.50		
D2	0.97	1.17	1.37		
е	2.54 BSC				
e1	5.08 BSC				
E	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E/2	4.80	5.00	5.20		
L	18.22	18.42	18.62		
L1	2.42	2.62	2.82		
р	3.40	3.60	3.80		
p1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97	6.17	6.37		

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