<u>MOSFET</u> – Power, N-Channel, SUPERFET[®] III, FRFET[®]

650 V, 65 A, 40 m Ω

NTH4L040N65S3F

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

Features

- 700 V @ $T_J = 150^{\circ}C$
- Typ. $R_{DS(on)} = 32 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 158 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 1366 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

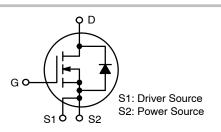
- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar



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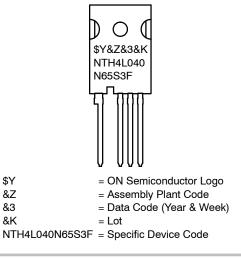
V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
650 V	40 m Ω	65 A	



POWER MOSFET



MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Parameter		Value	Unit	
V _{DSS}	Drain to Source Voltage		650	V	
V _{GSS}	Gate to Source Voltage	– DC	±30	V	
		– AC (f > 1 Hz)	±30	-	
ID	Drain Current	– Continuous (T _C = 25°C)	65	А	
		– Continuous (T _C = 100°C)	45		
I _{DM}	Drain Current	– Pulsed (Note 1)	162.5	А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	Pulsed Avalanche Energy (Note 2)		mJ	
I _{AS}	Avalanche Current (Note 2)	he Current (Note 2)		A	
E _{AR}	Repetitive Avalanche Energy (Note 1)		4.46	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		50		
PD	Power Dissipation	(T _C = 25°C)	446	W	
	– Derate Above 25°C		3.57	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
ΤL	Maximum Lead Temperature for Soldering, 1/8	" from Case for 5 seconds	300	°C	

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2. $I_{AS} = 9 \text{ A}, R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$. 3. $I_{SD} \leq 32.5 \text{ A}, \text{ di/dt} \leq 200 \text{ A/}\mu\text{s}, V_{DD} \leq 400 \text{ V}, \text{ starting } T_J = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.28	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max.	40	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTH4L040N65S3F	NTH4L040N65S3F	TO-247 - 4LD	Tube	N/A	N/A	30 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

STICS							
FF CHARACTERISTICS							
ain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 1 mA, T_J = 25°C	650			V		
	V_{GS} = 0 V, I_D = 1 mA, T_J = 150°C	700			V		
eakdown Voltage Temperature pefficient	I_D = 15 mA, Referenced to 25°C		0.63		V/°C		
ro Gate Voltage Drain Current	$V_{DS} = 650 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA		
	V_{DS} = 520 V, T_{C} = 125°C		213				
ate to Body Leakage Current	V_{GS} = ±30 V, V_{DS} = 0 V			±100	nA		
e	eakdown Voltage Temperature efficient o Gate Voltage Drain Current	Gover D and Colspan="2">Gover D and Colspan="2" C	$V_{GS} = 0 \text{ V}, \text{ I}_D = 1 \text{ mA}, \text{ T}_J = 150^{\circ}\text{C}$ $V_{GS} = 0 \text{ V}, \text{ I}_D = 1 \text{ mA}, \text{ T}_J = 150^{\circ}\text{C}$ $T_D = 15 \text{ mA}, \text{ Referenced to } 25^{\circ}\text{C}$ $V_{DS} = 650 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ $V_{DS} = 520 \text{ V}, \text{ T}_C = 125^{\circ}\text{C}$	area of the definition of the definit	$\begin{tabular}{ c c c c c c c } \hline & & & & & & & & & & & & & & & & & & $		

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 2.1$ mA	3.0		5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V_{GS} = 10 V, I _D = 32.5 A		32	40	mΩ
9fs	Forward Transconductance	V_{DS} = 20 V, I _D = 32.5 A		48		S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V_{DS} = 400 V, V_{GS} = 0 V, f = 1 MHz	5940	pF
C _{oss}	Output Capacitance		140	pF
C _{oss(eff.)}	Effective Output Capacitance	$V_{DS} = 0 V$ to 400 V, $V_{GS} = 0 V$	1366	pF
C _{oss(er.)}	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	247	pF
Q _{g(tot)}	Total Gate Charge at 10V	$V_{DS} = 400 \text{ V}, \text{ I}_{D} = 32.5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$	158	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	48	nC
Q _{gd}	Gate to Drain "Miller" Charge		60	nC
ESR	Equivalent Series Resistance	f = 1 MHz	1.1	Ω

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, \text{ I}_{D} = 32.5 \text{ A},$	44	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \text{ R}_{g} = 2.2 \Omega$ (Note 4)	23	ns
t _{d(off)}	Turn-Off Delay Time		96	ns
t _f	Turn-Off Fall Time		6	ns

SOURCE-DRAIN DIODE CHARACTERISTICS

ا _S	Maximum Continuous Source to Drain Diode Forward Current			65	А
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current			162.5	А
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 32.5 A		1.3	V
t _{rr}	Reverse Recovery Time	$V_{DD} = 400 \text{ V}, \text{ I}_{SD} = 32.5 \text{ A},$	145		ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/μs	737		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

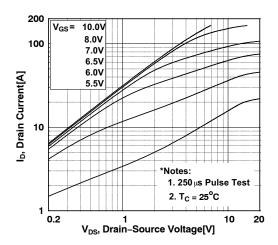
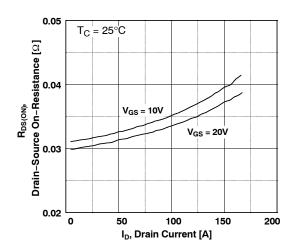
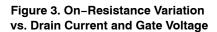


Figure 1. On-Region Characteristics





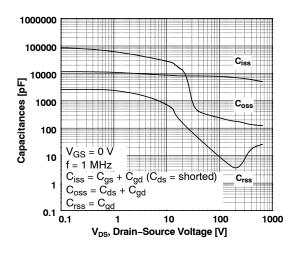


Figure 5. Capacitance Characteristics

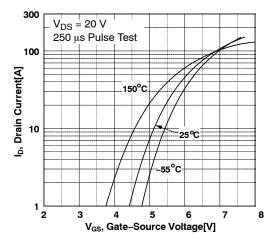


Figure 2. Transfer Characteristics

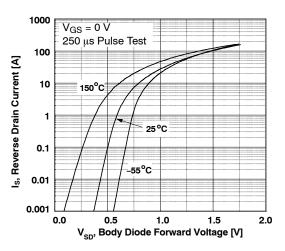


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

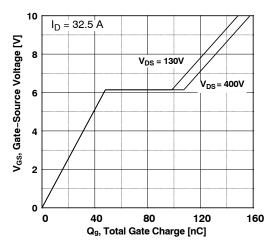
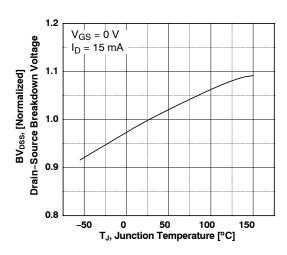
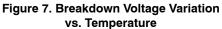


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)





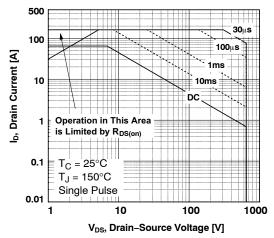


Figure 9. Maximum Safe Operating Area

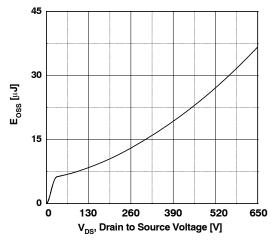


Figure 11. Eoss vs. Drain to Source Voltage

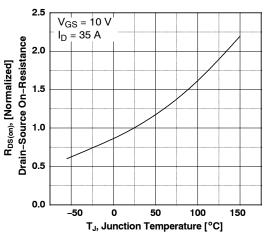


Figure 8. On–Resistance Variation vs. Temperature

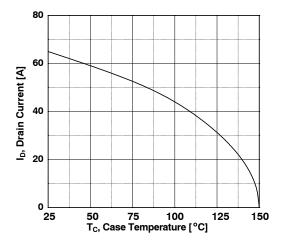


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

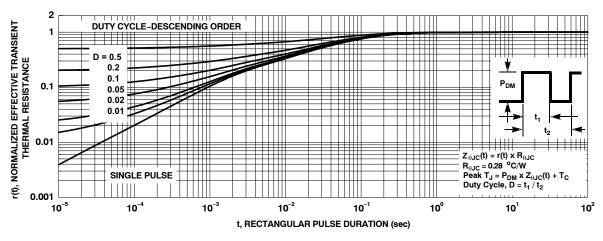


Figure 12. Transient Thermal Response Curve

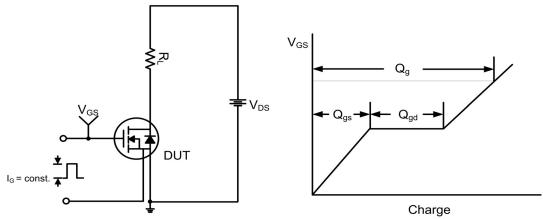


Figure 13. Gate Charge Test Circuit & Waveform

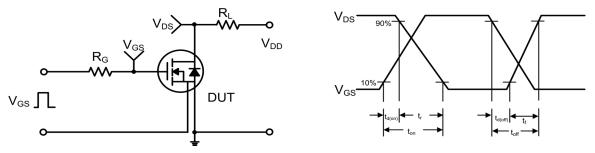
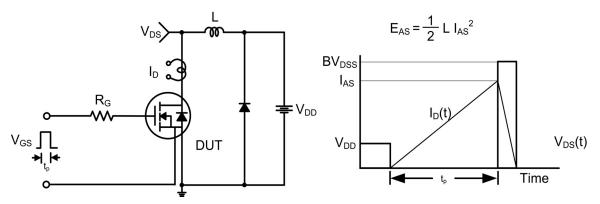


Figure 14. Resistive Switching Test Circuit & Waveforms





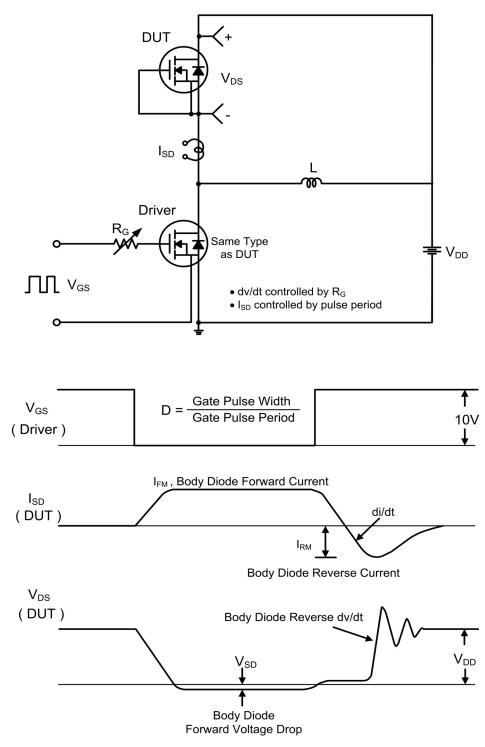


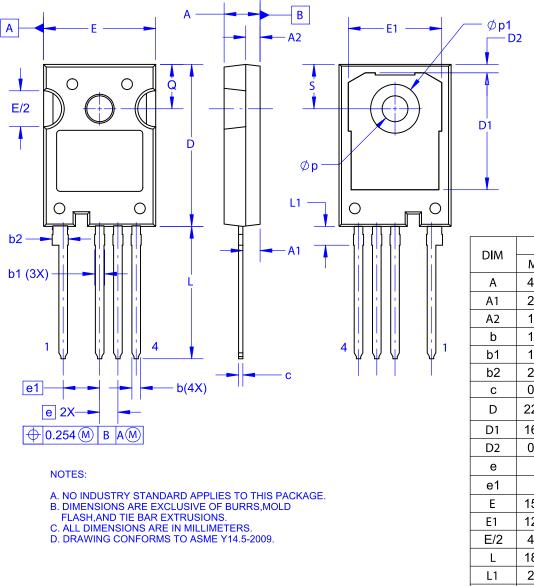
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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TO-247-4LD CASE 340CJ ISSUE A

DATE 16 SEP 2019



	MIL	LIMETER	S
DIM	MIN	NOM	MAX
А	4.80	5.00	5.20
A1	2.10	2.40	2.70
A2	1.80	2.00	2.20
b	1.07	1.20	1.33
b1	1.20	1.40	1.60
b2	2.02	2.22	2.42
С	0.50	0.60	0.70
D	22.34	22.54	22.74
D1	16.00	16.25	16.50
D2	0.97	1.17	1.37
е	2	2.54 BSC	2
e1	Ę	5.08 BSC	2
Е	15.40	15.60	15.80
E1	12.80	13.00	13.20
E/2	4.80	5.00	5.20
L	18.22	18.42	18.62
L1	2.42	2.62	2.82
р	3.40	3.60	3.80
p1	6.60	6.80	7.00
Q	5.97	6.17	6.37
S	5.97	6.17	6.37

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