MOSFET – Power, P-Channel, Schottky Diode, μCool 2x2 mm

-20 V, -4.1 A, 2.0 A

NTLJF3117P

Features

- FETKY™ Configuration with MOSFET plus Low Vf Schottky Diode
- µCOOL™ Package Provides Exposed Drain Pad for Excellent Thermal Conduction
- 2x2 mm Footprint Same as SC-88 Package Design
- Independent Pinout Provides Circuit Design Flexibility
- Low Profile (< 0.8 mm) for Easy Fit in Thin Environment
- High Current Schottky Diode: 2 A Current Rating
- This is a Pb-Free Device

Applications

- Optimized for Portable Applications like Cell Phones, Digital Cameras, Media Players, etc.
- DC-DC Buck Circuit
- Li-Ion Battery Applications
- Color Display and Camera Flash Regulators

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Param	Symbol	Value	Unit		
Drain-to-Source Voltag	е		V_{DSS}	-20	V
Gate-to-Source Voltage	•		V_{GS}	±8.0	V
Continuous Drain	Steady	T _A = 25°C	I _D	-3.3	Α
Current (Note 1)	State	T _A = 85°C		-2.4	
	t ≤ 5 s	T _A = 25°C		-4.1	
Power Dissipation (Note 1)	Steady State	T _A = 25°C	P _D	1.5	W
	t ≤ 5 s			2.3	
Continuous Drain		T _A = 25°C	I _D	-2.3	Α
Current (Note 2)	Steady	T _A = 85°C		-1.6	
Power Dissipation (Note 2)	State	T _A = 25°C	P _D	0.71	W
Pulsed Drain Current	t _p =	: 10 μs	I _{DM}	-20	Α
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to 150	°C
Source Current (Body Diode) (Note 2)			I _S	-1.9	Α
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces).
- Surface Mounted on FR4 Board using the minimum recommended pad size of 30 mm², 2 oz Cu.



ON Semiconductor®

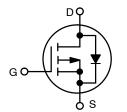
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MOSFET

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX (Note 1)
	100 mΩ @ -4.5 V	
-20 V	135 mΩ @ –2.5 V	-4.1 A
	200 mΩ @ -1.8 V	

SCHOTTKY DIODE

V _R MAX	V _F TYP	I _F MAX
30 V	0.47 V	2.0 A





P-CHANNEL MOSFET

SCHOTTKY DIODE

MARKING DIAGRAM



WDFN6 CASE 506AN



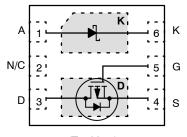
JH = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTIONS



(Top View)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

SCHOTTKY DIODE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	30	V
DC Blocking Voltage	V _R	30	V
Average Rectified Forward Current	I _F	2.0	Α

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 3)	$R_{ heta JA}$	83	
Junction–to–Ambient – $t \le 5$ s (Note 3)	$R_{ heta JA}$	54	°C/W
Junction-to-Ambient - Steady State Min Pad (Note 4)	$R_{ heta JA}$	177	

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
 Surface Mounted on FR4 Board using the minimum recommended pad size of 30 mm², 2 oz Cu.

$\textbf{MOSFET ELECTRICAL CHARACTERISTICS} \ (T_J = 25^{\circ}\text{C unless otherwise noted})$

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -25$	60 μΑ	-20			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = -250 μA, Ref to	25°C		9.95		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V 40VV 0V	T _J = 25°C			-1.0	μΑ
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$	T _J = 85°C			-10	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm$	8.0 V			±100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = -25$	50 μΑ	-0.4	-0.7	-1.0	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				2.44		mV/°C
Drain-to-Source On-Resistance	R _{DS(on)}	$V_{GS} = -4.5$, $I_D = -2.0$ A $V_{GS} = -2.5$, $I_D = -2.0$ A $V_{GS} = -1.8$, $I_D = -1.6$ A			75	100	mΩ
					101	135	1
					150	200	1
Forward Transconductance	9FS	$V_{DS} = -5.0 \text{ V}, I_D = -2.0 \text{ A}$			3.1		S
CHARGES, CAPACITANCES AND GA	ATE RESISTAN	CE					
Input Capacitance	C _{ISS}				531		pF
Output Capacitance	C _{OSS}	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ M}$ $V_{DS} = -10 \text{ V}$	ЛHz,		91		1
Reverse Transfer Capacitance	C _{RSS}	100			56		1
Total Gate Charge	Q _{G(TOT)}				5.5	6.2	nC
Threshold Gate Charge	Q _{G(TH)}				0.7		1
Gate-to-Source Charge	Q_{GS}	$V_{GS} = -4.5 \text{ V}, V_{DS} = -10 \text{ V},$ $I_{D} = -2.0 \text{ A}$			1.0		1
Gate-to-Drain Charge	Q_{GD}				1.4		1
Gate Resistance	R_{G}				8.8		Ω
SWITCHING CHARACTERISTICS (No	ote 6)						
Turn-On Delay Time	t _{d(ON)}				5.2		ns
Rise Time	t _r	V _{GS} = -4.5 V, V _{DD} = -	-5.0 V,		13.2		1
Turn-Off Delay Time	t _{d(OFF)}	$I_D = -1.0 \text{ A}, R_G = 6.0 \Omega$			13.7		1
	•	1		-	+	•	-1

5. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

Fall Time

6. Switching characteristics are independent of operating junction temperatures.

19.1

MOSFET ELECTRICAL CHARACTERISTICS ($T_J = 25$ °C unless otherwise noted)

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS	6 (Note 6)						
Turn-On Delay Time	t _{d(ON)}				5.5		ns
Rise Time	t _r	V_{GS} = -4.5 V, V_{DD} = -10 V, I_{D} = -2.0 A, R_{G} = 2.0 Ω			15		
Turn-Off Delay Time	t _{d(OFF)}				19.8		
Fall Time	t _f				21.6		
DRAIN-SOURCE DIODE CHARA	CTERISTICS						
Forward Recovery Voltage	V _{SD}	V 0V/IC 10A	T _J = 25°C		-0.75	-1.0	V
		$V_{GS} = 0 \text{ V, IS} = -1.0 \text{ A}$ $T_{J} = 125^{\circ}\text{C}$			-0.64		V
Reverse Recovery Time	t _{RR}				16.2		
Charge Time	ta	$V_{GS} = 0 \text{ V, } d_{ SD}/d_t = 100 \text{ A/}\mu\text{s,}$ $I_S = -1.0 \text{ A}$			10.6		ns
Discharge Time	t _b				5.6		

5.7

nC

5. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

Reverse Recovery Time

6. Switching characteristics are independent of operating junction temperatures.

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

 t_{b} Q_{RR}

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.1 A		0.34	0.39	V
Forward Voltage		I _F = 1.0 A		0.47	0.53	
Maximum Instantaneous	I _R	V _R = 30 V		17	20	μΑ
Reverse Current		V _R = 20 V		3.0	8.0	
		V _R = 10 V		2.0	4.5	

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 85°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.1 A		0.22	0.35	V
Forward Voltage		I _F = 1.0 A		0.40	0.50	
Maximum Instantaneous	I _R	V _R = 30 V		0.22	2.5	mA
Reverse Current		V _R = 20 V		0.11	1.6	
		V _R = 10 V		0.06	1.2	

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 125°C unless otherwise noted)

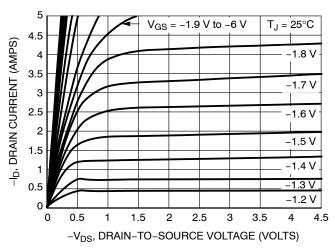
		, =	•			
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.1 A		0.2	0.29	V
Forward Voltage		I _F = 1.0 A		0.4	0.47	
Maximum Instantaneous	I _R	V _R = 30 V		2.0	20	mA
Reverse Current		V _R = 20 V		1.1	10.9	
		V _R = 10 V		0.63	8.4	

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Capacitance	С	$V_R = 5.0 \text{ V}, f = 1.0 \text{ MHz}$		38		pF

- 7. Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
- 8. Surface-mounted on FR4 board using the minimum recommended pad size of 30 mm², 2 oz cu.
- 9. Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%.
- 10. Switching characteristics are independent of operating junction temperatures.

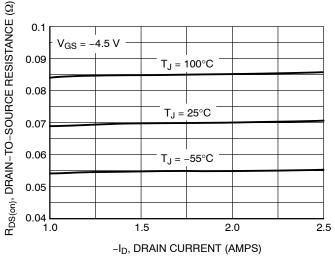
TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



5 V_{DS} ≥ 10 V 3 2 T_J = 25°C 1 T_J = 25°C T_J = -55°C 0 0.5 1 1.5 2 2.5 3 -V_{GS}, GATE-TO-SOURCE VOLTAGE (VOLTS)

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics



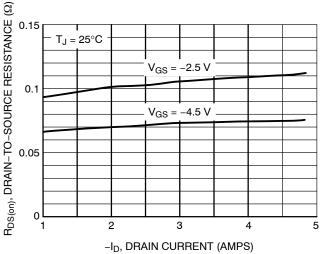
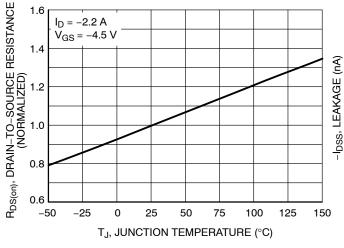


Figure 3. On-Resistance versus Drain Current

Figure 4. On-Resistance versus Drain Current and Gate Voltage



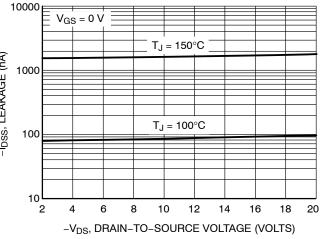
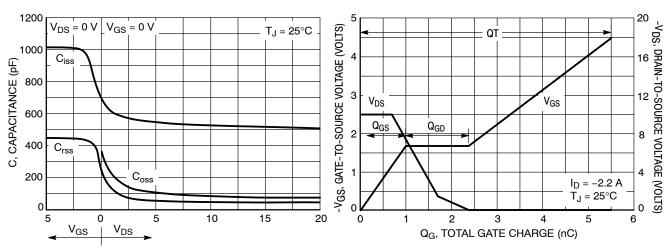


Figure 5. On–Resistance Variation with Temperature

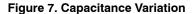
Figure 6. Drain-to-Source Leakage Current versus Voltage

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 8. Gate-To-Source and Drain-To-Source
Voltage versus Total Charge



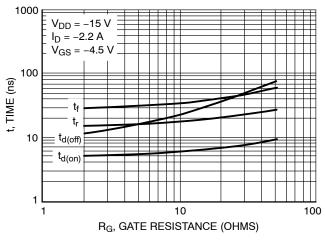


Figure 9. Resistive Switching Time Variation versus Gate Resistance

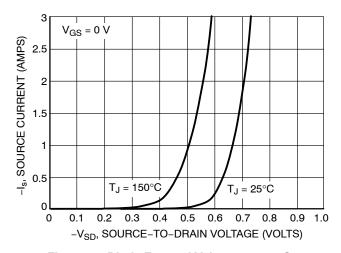


Figure 10. Diode Forward Voltage versus Current

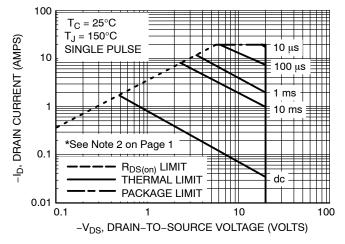


Figure 11. Maximum Rated Forward Biased Safe Operating Area

$\textbf{TYPICAL PERFORMANCE CURVES} \ \, (T_J = 25^{\circ}\text{C unless otherwise noted})$

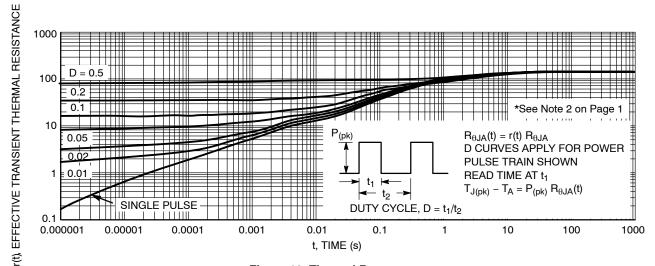
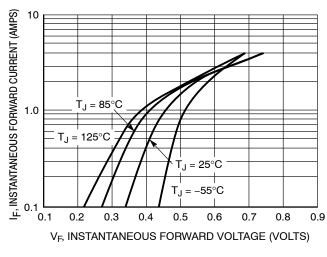


Figure 12. Thermal Response

TYPICAL SCHOTTKY PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



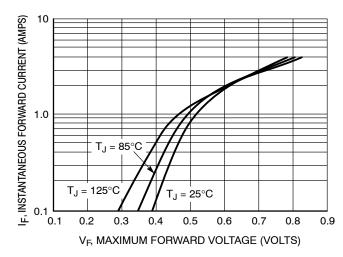


Figure 13. Typical Forward Voltage

Figure 14. Maximum Forward Voltage

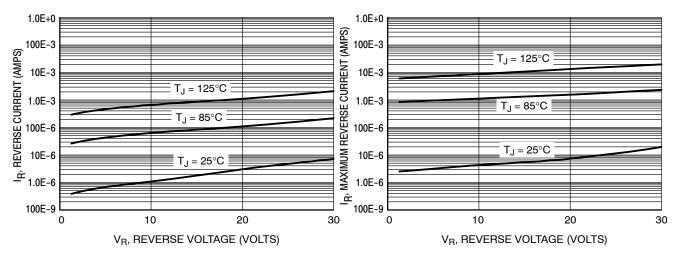


Figure 15. Typical Reverse Current

Figure 16. Maximum Reverse Current

ORDERING INFORMATION

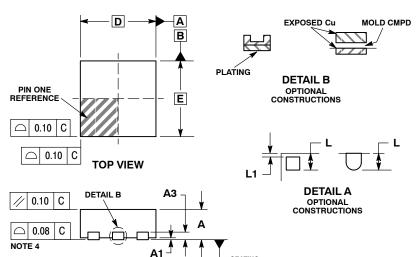
Device	Package	Shipping [†]
NTLJF3117PT1G	WDFN6 (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



WDFN6 2x2, 0.65P CASE 506AN **ISSUE G**

DATE 22 AUG 2013



SEATING PLANE C



NOTES:

PAD AS	WELL AS	THE TERM	INALS.
	MILLIM		
DIM	MIN	MAX	
Α	0.70	0.80	
A1	0.00	0.05	
A3	0.20		
b	0.25	0.35	
D	2.00		
D2	0.57	0.77	
E	2.00 BSC		
E2	0.90	1.10	
е	0.65		
F	0.95		
K	0.25		
L	0.20	0.30	
L1		0.10	

GENERIC MARKING DIAGRAM*



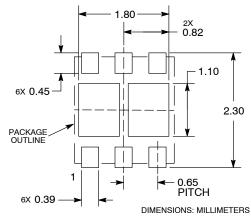
XX = Specific Device Code = Date Code Μ

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

⊕ 0.10 C A D2 D2 F DETAIL A ⊕ 0.10 C A Κ ex b 0.10 С Α В е Ф 0.05 С NOTE 3 **BOTTOM VIEW**

SIDE VIEW

SOLDERMASK DEFINED MOUNTING FOOTPRINT



STYLE 1	:
PIN 1.	SOURCE 1
2.	GATE 1
_	DDAINIO

3. DRAIN 2

SOURCE 2 5. GATE 2 6. DRAIN 1

STYLE 2: PIN 1. 2. N/C 3.

STYLE 3: ANODE SOURCE 1 GATE 1 2. DRAIN 3. SOURCE 2

SOURCE DRAIN 2 GATE 5. CATHODE 6.

GATE 2 DRAIN 1

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