## MOSFET – Power, Dual, N-Channel, μCool, UDFN6, 2.0x2.0x0.55 mm 30 V, 7.3 A

### NTLUD4C26N

#### **Features**

- UDFN Package with Exposed Drain Pads for Excellent Thermal Conduction
- Low Profile UDFN 2.0 x 2.0 x 0.55 mm for Board Space Saving
- Ultra Low R<sub>DS(on)</sub>
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Applications**

- Power Load Switch
- Wireless Charging
- DC-DC Converters

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	30	V
Gate-to-Source Vol	Gate-to-Source Voltage			±12	V
Continuous Drain	Steady	T <sub>A</sub> = 25°C	I <sub>D</sub>	7.3	Α
Current (Note 1)	State	T <sub>A</sub> = 85°C		5.3	
	t ≤ 5 s	T <sub>A</sub> = 25°C		9.1	
Power Dissipation (Note 1)	Steady State	T <sub>A</sub> = 25°C	P <sub>D</sub>	1.70	W
	t ≤ 5 s	T <sub>A</sub> = 25°C		2.63	
Continuous Drain	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	4.8	Α
Current (Note 2)	State	T <sub>A</sub> = 85°C		3.4	
Power Dissipation (Note 2) T <sub>A</sub> = 25°C			P <sub>D</sub>	0.72	W
Pulsed Drain Current $t_p = 10 \mu s$			I <sub>DM</sub>	22	Α
MOSFET Operating Junction and Storage Temperature			T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C
Source Current (Body Diode) (Note 1)			I <sub>S</sub>	3.0	Α
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
- Surface-mounted on FR4 board using the minimum recommended pad size, 2 oz. Cu.

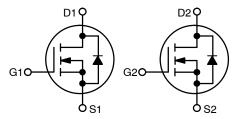


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#### **MOSFET**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX	
30 V	24 mΩ @ 4.5 V	7.3 A	
30 V	65 mΩ @ 1.8 V	7.074	



**Dual N-Channel MOSFET** 

#### MARKING DIAGRAM



UDFN6 CASE 517BF μCOOL™

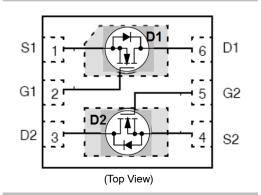


AC = Specific Device Code

M = Date Code

■ = Pb-Free Package

(Note: Microdot may be in either location)



#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 3 of this data sheet.

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient – Steady State (Note 3)	$R_{\theta JA}$	73.6	
Junction-to-Ambient – t ≤ 5 s (Note 3)	$R_{\theta JA}$	47.6	°C/W
Junction-to-Ambient – Steady State min Pad (Note 4)	$R_{\theta JA}$	174.4	

- Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
   Surface-mounted on FR4 board using the minimum recommended pad size, 2 oz. Cu.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Co	ondition	Min	Тур	Max	Units
OFF CHARACTERISTICS				•		•	•
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V,	I <sub>D</sub> = 250 μA	30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_{J}$	I <sub>D</sub> = 250 μA	A, ref to 25°C		7		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C			1	μΑ
		V <sub>DS</sub> = 24 V	T <sub>J</sub> = 125°C			10	
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{DS} = 0 V$ ,	V <sub>GS</sub> = ±12 V			±100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}$	, I <sub>D</sub> = 250 μA	0.6		1.1	V
Negative Threshold Temp. Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				2.8		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5	V, I <sub>D</sub> = 5.0 A		20	24	mΩ
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 1.0 A			40	65	1
Forward Transconductance	9FS	V <sub>DS</sub> = 1.5 V, I <sub>D</sub> = 5.0 A			23		S
CHARGES, CAPACITANCES & GATE	RESISTANCE	-					
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1 MHz, V <sub>DS</sub> = 15 V			460		pF
Output Capacitance	C <sub>OSS</sub>				225		
Reverse Transfer Capacitance	C <sub>RSS</sub>				27		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = 4.5 \text{ V}, V_{DS} = 10 \text{ V};$ $I_D = 5.0 \text{ A}$			5.0	8.0	nC
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 15 V; I <sub>D</sub> = 5.0 A			5.5	9.0	nC
Threshold Gate Charge	Q <sub>G(TH)</sub>				0.55		
Gate-to-Source Charge	Q <sub>GS</sub>				2.5		
Gate-to-Drain Charge	$Q_{GD}$				1.1		
SWITCHING CHARACTERISTICS, V <sub>GS</sub>	s = <b>4.5 V</b> (Note 6)			•			•
Turn-On Delay Time	t <sub>d(ON)</sub>				5		ns
Rise Time	t <sub>r</sub>	Vos = 45 V	/ Vpp = 15 V		15		1
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$V_{GS} = 4.5 \text{ V}, V_{DD} = 15 \text{ V},$ $I_{D} = 5.0 \text{ A}, R_{G} = 1 \Omega$			13		
Fall Time	t <sub>f</sub>				1.7		
DRAIN-SOURCE DIODE CHARACTER	ISTICS	•					
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C	1	0.7	1.0	V
		$V_{GS} = 0 \text{ V},$ $I_{S} = 2.0 \text{ A}$ $T_{J} = 125^{\circ}\text{C}$	1	0.6		1	
		I.	1 -			1	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 5. Pulse Test: pulse width  $\leq 300 \ \mu s$ , duty cycle  $\leq 2\%$ .

- 6. Switching characteristics are independent of operating junction temperatures.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

	, ,	. ,				
Parameter	Symbol	Test Condition	Min	Тур	Max	Units
DRAIN-SOURCE DIODE CHARACTERISTICS						
Reverse Recovery Time	t <sub>RR</sub>	V <sub>GS</sub> = 0 V, dls/dt = 100 A/us,		18.5		ns
Charge Time	t <sub>a</sub>			9.3		
Discharge Time	t <sub>b</sub>	$V_{GS} = 0 \text{ V, dls/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 2.0 \text{ A}$		9.1		
Reverse Recovery Charge	Q <sub>RR</sub>	1		7.8		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 5. Pulse Test: pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ .

#### **DEVICE ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTLUD4C26NTAG	UDFN6 (Pb-Free)	3000 / Tape & Reel
NTLUD4C26NTBG	UDFN6 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>6.</sup> Switching characteristics are independent of operating junction temperatures.

#### **TYPICAL CHARACTERISTICS**

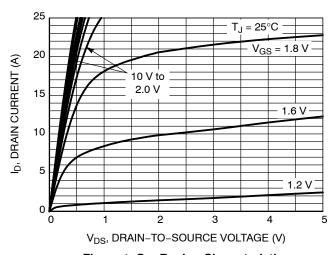


Figure 1. On-Region Characteristics

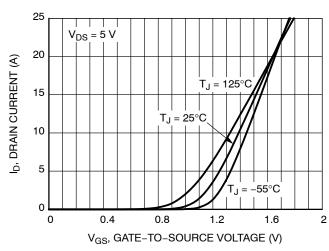


Figure 2. Transfer Characteristics

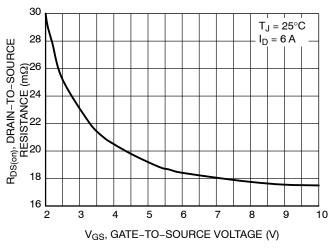


Figure 3. On-Resistance vs. Gate-to-Source Voltage

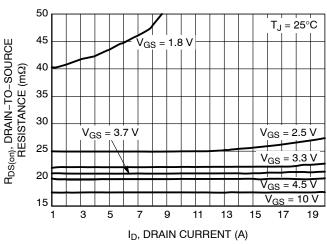


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

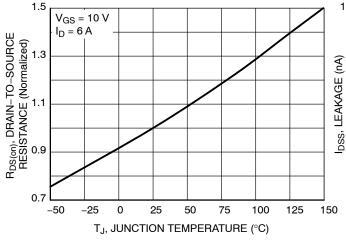


Figure 5. On–Resistance Variation with Temperature

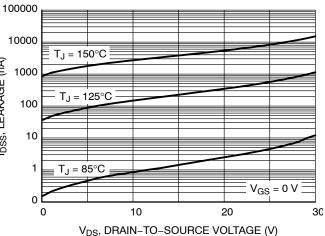


Figure 6. Drain-to-Source Leakage Current vs. Voltage

#### **TYPICAL CHARACTERISTICS**

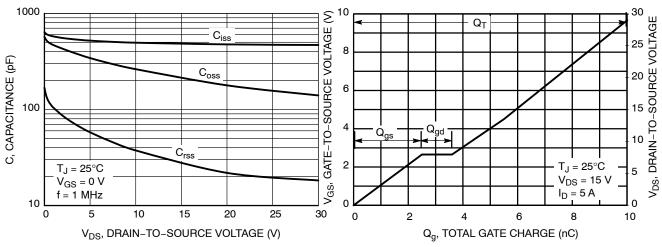


Figure 7. Capacitance Variation

Figure 8. Gate-to-Source vs. Total Charge

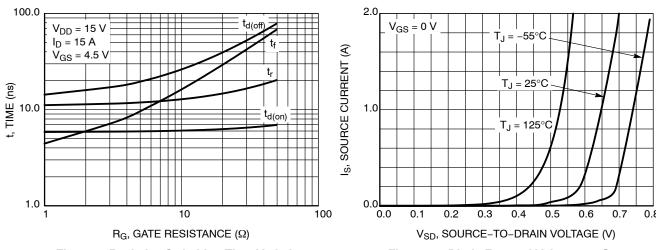


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

Figure 10. Diode Forward Voltage vs. Current

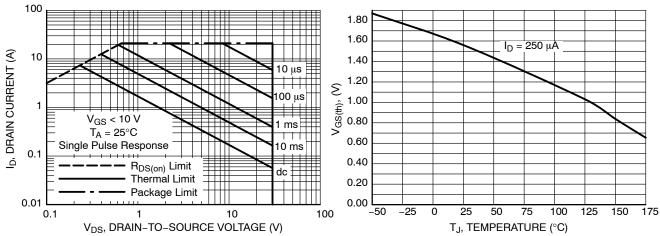


Figure 11. Maximum Rated Forward Biased Safe Operating Area

Figure 12. Threshold Voltage

#### **TYPICAL CHARACTERISTICS**

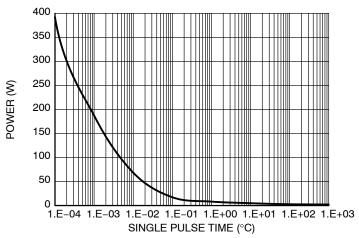
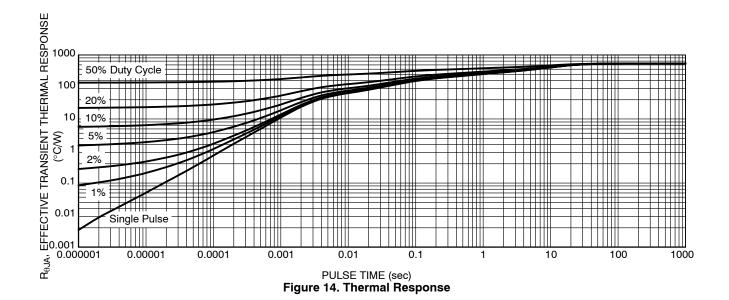


Figure 13. Single Pulse Maximum Power Dissipation



△ 0.08 C

DETAIL A

D2

е

NOTE 4

# UDFN6 2x2, 0.65P CASE 517BF ISSUE B

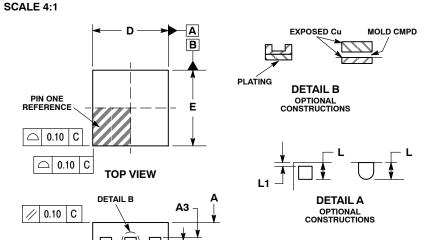
Α1

D2

SIDE VIEW

**BOTTOM VIEW** 

DATE 20 AUG 2012



⊕ 0.10 C A B

C SEATING PLANE

0.10 C A

CAB

NOTE 3

0.05 C

0.10

Ф

#### NOTES:

- DIMENSIONING AND TOLERANCING PER
   ASME V14 5M 1994
- ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN
- 0.15 AND 0.30 mm FROM THE TERMINAL TIP.
  4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS			
DIM	MIN MAX			
Α	0.45	0.55		
A1	0.00	0.05		
A3	0.13	REF		
b	0.25	0.35		
D	2.00 BSC			
D2	0.57	0.77		
E	2.00 BSC			
E2	0.90	1.10		
е	0.65 BSC			
F	0.15 BSC			
K	0.25 REF			
L	0.20	0.30		
L1	0.10			

## GENERIC MARKING DIAGRAM\*



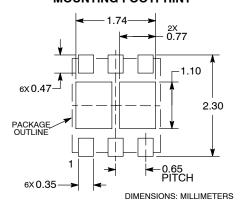
XX = Specific Device Code

M = Date Code

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present.

# RECOMMENDED MOUNTING FOOTPRINT



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