MOSFET – Power, Single, N-Channel 80 V, 1.9 m Ω , 224 A

NTMFS6H800NL

Features

- Small Footprint (5x6 mm) for Compact Design
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	80	٧
Gate-to-Source Voltage			V _{GS}	±20	٧
Continuous Drain		T _C = 25°C	I _D	224	Α
Current R _{θJC} (Notes 1, 3)	Steady State	T _C = 100°C		158	
Power Dissipation		T _C = 25°C	P_{D}	214	W
R _{θJC} (Note 1)		T _C = 100°C		107	
Continuous Drain		T _A = 25°C	I _D	30	Α
Current R _{0JA} (Notes 1, 2, 3)	Steady	T _A = 100°C		21	
Power Dissipation	State	T _A = 25°C	P_{D}	3.9	W
R _{θJA} (Notes 1, 2)		T _A = 100°C		1.9	
Pulsed Drain Current	$T_A = 25$	°C, t _p = 10 μs	I _{DM}	900	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			Is	179	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 16.2 A)			E _{AS}	601	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{\theta JC}$	0.7	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	39	

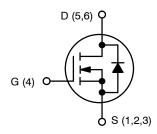
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Surface-mounted on FR4 board using a 650 mm², 2 oz. Cu pad.
- Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.



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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
80 V	1.9 mΩ @ 10 V	224 A
	2.4 mΩ @ 4.5 V	224 A

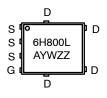


N-CHANNEL MOSFET

MARKING DIAGRAM



DFN5 (SO-8FL) CASE 506EZ



6H800L = Specific Device Code A = Assembly Location

Y = Year
W = Work Week
ZZ = Lot Traceability

ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS					•	•	•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		80			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /				36		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	T _J = 25 °C			10	μΑ
		V _{DS} = 80 V	T _J = 125°C			250	
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} = 20 V				100	nA
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 330 \mu A$		1.2		2.0	V
Threshold Temperature Coefficient	V _{GS(TH)} /T _J				-5.1		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 50 A		1.5	1.9	mΩ
		V _{GS} = 4.5 V	I _D = 50 A		1.9	2.4	
Forward Transconductance	9 _{FS}	V _{DS} =8 V, I _D = 50 A			250		S
CHARGES, CAPACITANCES & GATE RE	SISTANCE					•	
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 40 V			6900		pF
Output Capacitance	Coss				800		
Reverse Transfer Capacitance	C _{RSS}				22		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DS} = 40 V; I _D = 50 A			112		
Threshold Gate Charge	Q _{G(TH)}	V _{GS} = 4.5 V, V _{DS} = 40 V; I _D = 50 A			10		nC
Gate-to-Source Charge	Q _{GS}				19		
Gate-to-Drain Charge	Q _{GD}				17		
Plateau Voltage	V_{GP}				3.0		V
Total Gate Charge	Q _{G(TOT)}				53		nC
SWITCHING CHARACTERISTICS (Note	5)						
Turn-On Delay Time	t _{d(ON)}				20		
Rise Time	t _r	$V_{GS} = 4.5 \text{ V}, V_{I}$	_{DS} = 64 V,		153		1
Turn-Off Delay Time	t _{d(OFF)}	$V_{GS} = 4.5 \text{ V}, V_{DS} = 64 \text{ V},$ $I_{D} = 50 \text{ A}, R_{G} = 2.5 \Omega$			118		ns -
Fall Time	t _f				163		
DRAIN-SOURCE DIODE CHARACTERIS	STICS						
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V,	T _J = 25°C		0.8	1.2	.,
		I _S = 50 A	T _J = 125°C		0.7		· V
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, dIS/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 50 \text{ A}$			77		ns
Charge Time	t _a				40		
Discharge Time	t _b				38		
Reverse Recovery Charge	Q _{RR}				110		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$.

5. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS

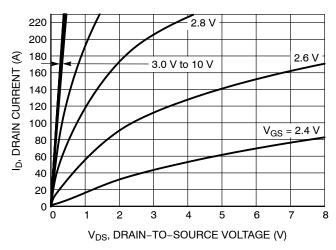


Figure 1. On-Region Characteristics

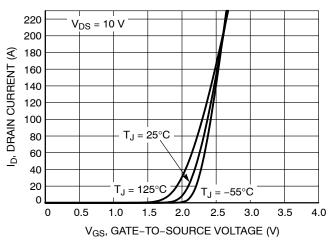


Figure 2. Transfer Characteristics

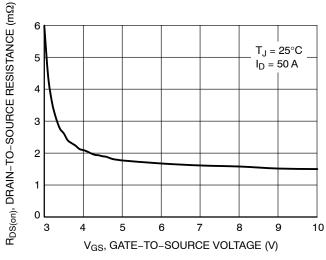


Figure 3. On-Resistance vs. Gate-to-Source Voltage

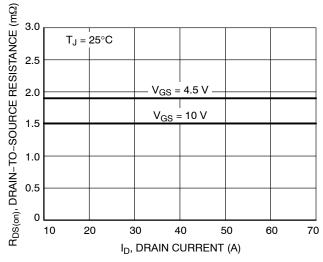


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

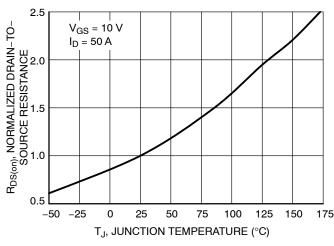


Figure 5. On–Resistance Variation with Temperature

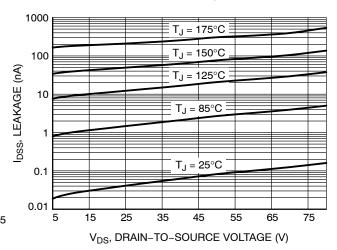


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS

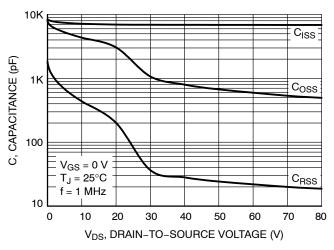


Figure 7. Capacitance Variation

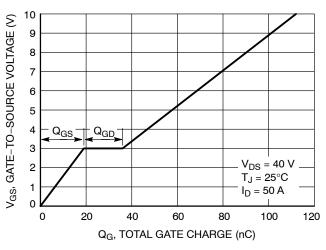


Figure 8. Gate-to-Source vs. Total Charge

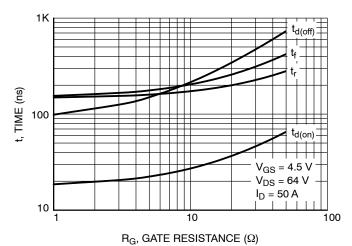


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

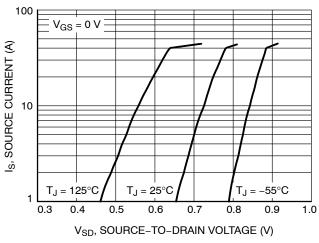


Figure 10. Diode Forward Voltage vs. Current

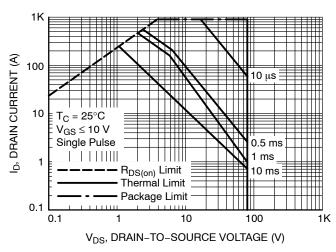


Figure 11. Maximum Rated Forward Biased Safe Operating Area

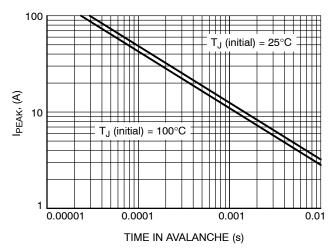


Figure 12. Maximum Drain Current vs. Time in Avalanche

TYPICAL CHARACTERISTICS

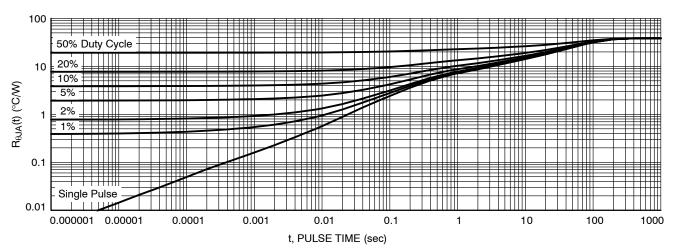


Figure 13. Thermal Response

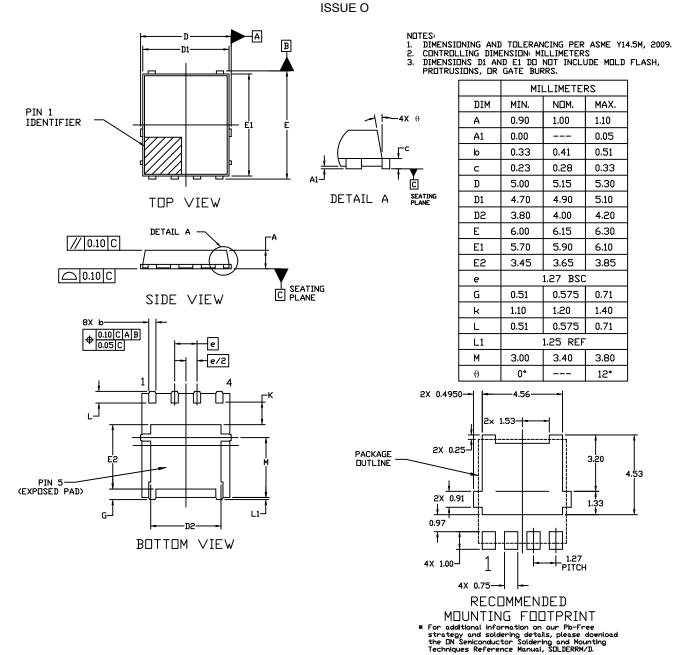
DEVICE ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
NTMFS6H800NLT1G	6H800L	DFN5 (Pb-Free)	1500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

DFN5 5x6, 1.27P (SO-8FL)CASE 506EZ



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