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MOSFET - Power, N-Channel, SUPERFET® III, FAST

650 V, 64 mΩ, 40 A

NTMT064N65S3H

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate. Consequently, SUPERFET III FAST MOSFET is suitable for various AC/DC power conversion for system miniaturization and higher efficiency.

The Power88 package is an ultra-slim surface-mount package (1 mm high) with a low profile and small footprint (8 x 8 mm²). SUPERFET III MOSFET in a Power88 package offers excellent switching performance due to lower parasitic source inductance and separated power and drive sources. Power88 offers Moisture Sensitivity Level 1 (MSL 1).

Features

- 700 V @ T_J = 150°C
- Typ. $R_{DS(on)} = 52 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 82 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 750 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

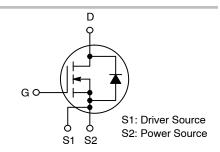
- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar



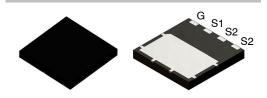
ON Semiconductor®

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V _{DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	64 mΩ @ 10 V	40 A



POWER MOSFET



TDFN4 8X8 2P CASE 520AB

MARKING DIAGRAM

o NTMT064 N65S3H AWLYWW

NTMT064N65S3H = Specific Device Code A = Assembly Location

WL = Wafer Lot Y = Year WW = Work Week

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Paramete	Value	Unit		
V _{DSS}	Drain to Source Voltage	650	V		
V _{GSS}	Gate to Source Voltage	- DC	±30	V	
		- AC (f > 1 Hz)	±30		
I _D	Drain Current	- Continuous (T _C = 25°C)	40	Α	
		- Continuous (T _C = 100°C)	25		
I _{DM}	Drain Current	- Pulsed (Note 1)	112	А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		422	mJ	
I _{AS}	Avalanche Current (Note 2)		6.5	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		2.60	mJ	
dv/dt	MOSFET dv/dt		120	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		20		
P _D	Power Dissipation	(T _C = 25°C)	260	W	
		- Derate Above 25°C	2.08	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. $I_{AS} = 6.5 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$. 3. $I_{SD} \le 20 \text{ A}$, $di/dt \le 200 \text{ A/}\mu\text{s}$, $V_{DD} \le 400 \text{ V}$, starting $T_J = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.48	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient, Max. (Note 4)	45	

^{4.} Device on 1 in² pad 2 oz copper pad on 1.5 x 1.5 in. board of FR-4 material.

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Reel Size	Tape Width	Shipping Quantity [†]
NTMT064N65S3H	NTMT064N65S3H	TDFN4	13″	13.3 mm	3000 Units / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

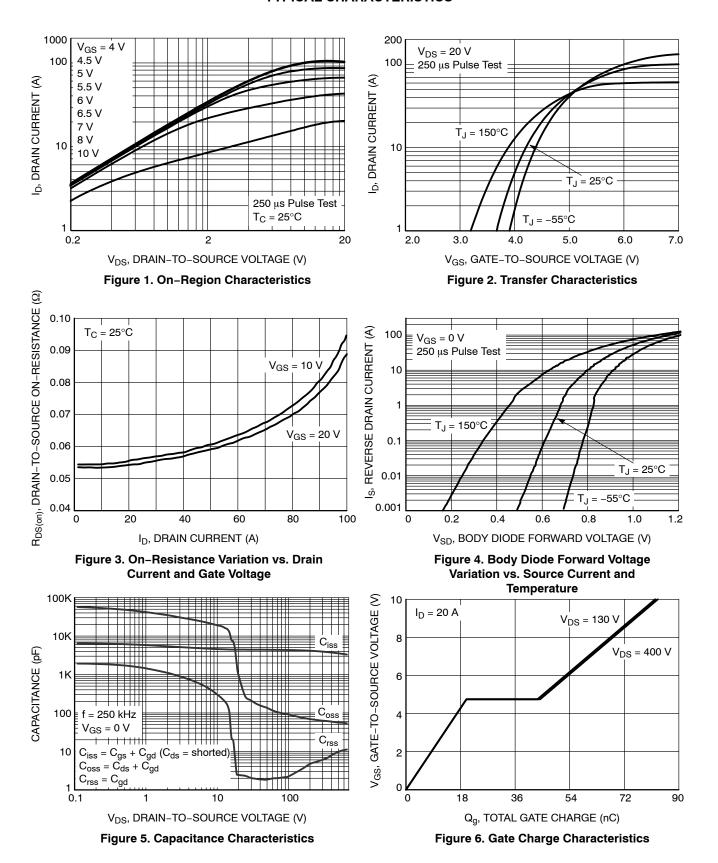
ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS		•	•	•	
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$	650			V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700			V
$\Delta BV_{DSS} \! / \! \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, Referenced to 25°C	0.63			V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	10		10	μΑ
		V _{DS} = 520 V, T _C = 125°C		3.2		
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±30 V, V _{DS} = 0 V			±100	nA
ON CHARACTE	RISTICS		•	•	•	
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 3.9 \text{ mA}$	2.4		4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 20 A		52	64	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 20 A		44		S
DYNAMIC CHA	RACTERISTICS		ı			
C _{iss}	Input Capacitance			3745		pF
C _{oss}	Output Capacitance	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, f = 250 \text{ kHz}$		58		pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		750		pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		100		pF
Q _{g(tot)}	Total Gate Charge at 10 V			82		nC
Q _{gs}	Gate to Source Gate Charge	$V_{DS} = 400 \text{ V}, I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}$ (Note 5)		20		nC
Q_{gd}	Gate to Drain "Miller" Charge	(110.00)		23		nC
ESR	Equivalent Series Resistance	f = 1 MHz		0.7		Ω
SWITCHING CH	IARACTERISTICS					
t _{d(on)}	Turn-On Delay Time			29		ns
t _r	Turn-On Rise Time	$V_{DD} = 400 \text{ V}, I_D = 20 \text{ A},$		16		ns
t _{d(off)}	Turn-Off Delay Time	$V_{DD} = 400 \text{ V, } I_{D} = 20 \text{ A,} \ V_{GS} = 10 \text{ V, } R_{g} = 4.7 \Omega \ (\text{Note 5})$		85		ns
t _f	Turn-Off Fall Time			3.1		ns
SOURCE-DRAI	N DIODE CHARACTERISTICS		•	•	•	
Is	Maximum Continuous Source to Drain Diode Forward Current				40	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current				112	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 20 A			1.2	V
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 20 A,		367		ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt = 100 A/μs		6.3		μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. Essentially independent of operating temperature typical characteristics.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

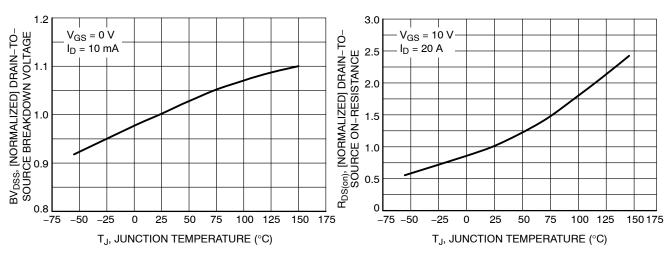


Figure 7. Breakdown Voltage Variation vs. Temperature

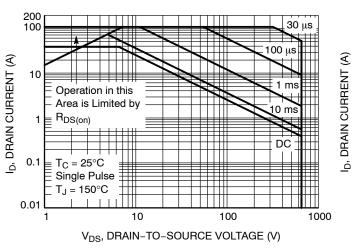


Figure 9. Maximum Safe Operating Area

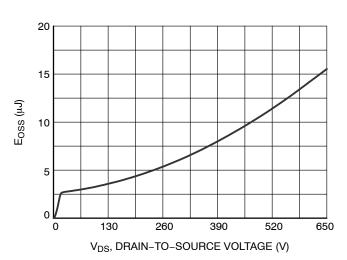


Figure 11. E_{OSS} vs. Drain-to-Source Voltage

Figure 8. On–Resistance Variation vs. Temperature

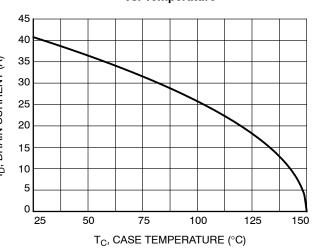


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL CHARACTERISTICS

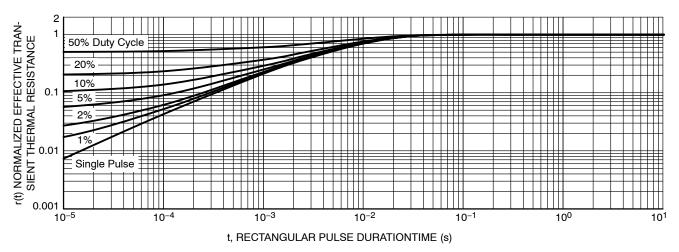


Figure 12. Transient Thermal Response Curve

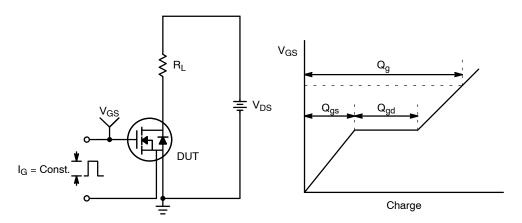


Figure 13. Gate Charge Test Circuit & Waveform

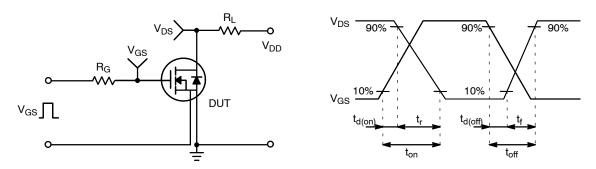


Figure 14. Resistive Switching Test Circuit & Waveforms

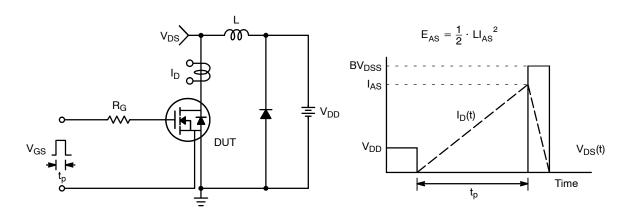


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

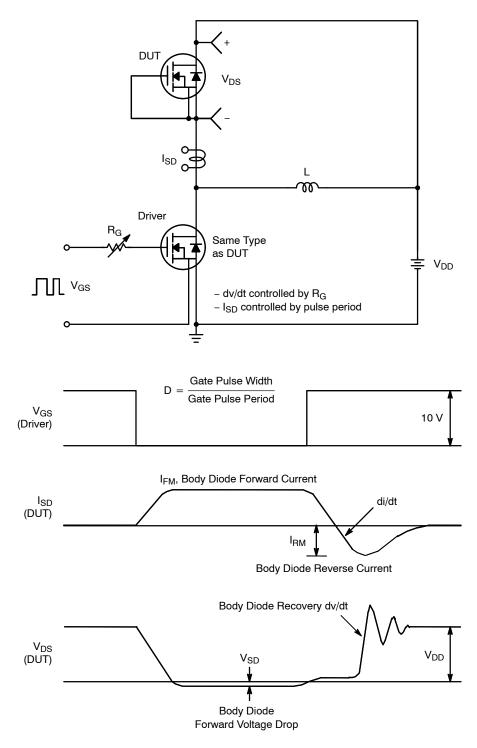
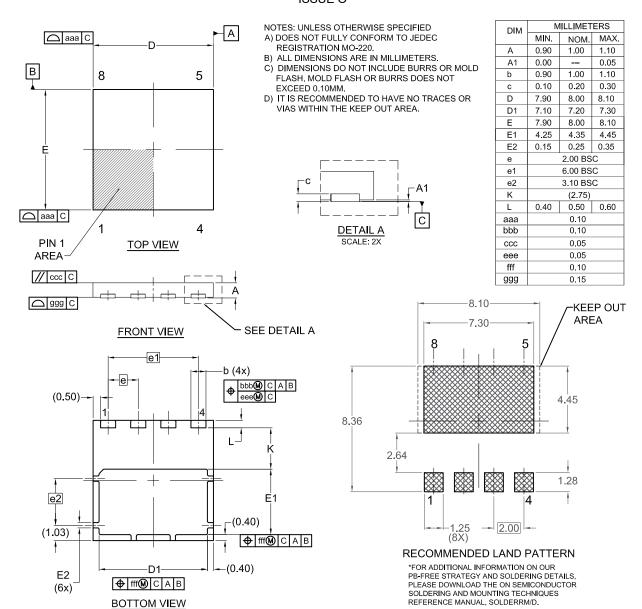


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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PACKAGE DIMENSIONS

TDFN4 8x8, 2P CASE 520AB ISSUE O



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