MOSFET – Power, Single P-Channel, SOT-23 -20 V, -3.6 A

Features

- Leading -20 V Trench for Low R_{DS(on)}
- -1.8 V Rated for Low Voltage Gate Drive
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

• Power Load Switch

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Parameter			Symbol	Value	Unit	
Drain-to-Source Voltage			V _{DSS}	-20	V	
Gate-to-Source Voltage			V _{GS}	±8	V	
Continuous Drain Current	Steady	$T_A = 25^{\circ}C$	Ι _D	-3.3	А	
(Note 1)	State	$T_A = 70^{\circ}C$		-2.6		
	t≤5s	$T_A = 25^{\circ}C$		-3.6		
		$T_A = 70^{\circ}C$		-2.9		
Power Dissipation (Note 1)	Steady State	$T_A = 25^{\circ}C$	PD	0.72	W	
	t≤5 s			0.86		
Continuous Drain Current	Steady	$T_A = 25^{\circ}C$	Ι _D	-2.5	А	
(Note 2)	State	$T_A = 70^{\circ}C$		-2.0		
Power Dissipation (Note 2)		$T_A = 25^{\circ}C$	P _D	0.42	W	
Pulsed Drain Current $t_p = 10 \ \mu s$			I _{DM}	-13	А	
Operating Junction and Storage Temperature		T _J , T _{STG}	–55 to 150	°C		
Source Current (Body Diode)			۱ _S	-1.3	А	
Lead Temperature for Soldering Purposes (1/8 in from case for 10 s)			ΤL	260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Мах	Unit
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	174	°C/W
Junction-to-Ambient – t \leq 5 s (Note 1)	$R_{\theta JA}$	145	
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	300	

1. Surface-mounted on FR4 board using 1 in sq. pad size (Cu area = 727 mm sq., 1 oz).

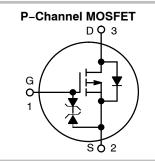
 Surface-mounted on FR4 board using minimum pad size (Cu area = 3.8 mm sq., 1 oz).

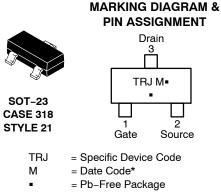


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V _{(BR)DSS}	V _{(BR)DSS} R _{DS(on)} Max	
-20 V	47 mΩ @ −4.5 V	
	63 mΩ @ –2.5 V	-3.6 A
	100 mΩ @ –1.8 V	





 (Note: Microdot may be in either location)
 *Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
NTR3A052PZT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T, I = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				•		
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = -250 μ A		-20			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	$I_D = -250 \ \mu\text{A}$, ref to 25°C			16		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = -20 V	$T_J = 25^{\circ}C$			-1	μΑ
			T _J = 125°C			-100	μA
Gate-to-Source Leakage Current	I _{GSS}	V_{DS} = 0 V, V_{GS}	= ±8 V			±10	μA
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	V_{GS} = V_{DS} , I_D = -250 μ A		-0.4		-1.0	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				3.3		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V_{GS} = -4.5 V	I _D = -3.5 A		33	47	mΩ
		V _{GS} = -2.5 V	I _D = -3.0 A		41	63	
		V _{GS} = -1.8 V	I _D = -2.0 A		54	100	
		V _{GS} = -1.5 V	I _D = -0.5 A		69		
Forward Transconductance	9FS	$V_{DS} = -5 \text{ V}, \text{ I}_{D} = -3.5 \text{ A}$			16		S
CHARGES AND CAPACITANCES							
Input Capacitance	C _{iss}	V_{GS} = 0 V, f = 1.0 MHz, V_{DS} = -4 V			1243		pF
Output Capacitance	C _{oss}				194		1
Reverse Transfer Capacitance	C _{rss}				158		
Total Gate Charge	Q _{G(TOT)}				11.9		nC
Threshold Gate Charge	Q _{G(TH)}	V_{GS} = -4.5 V, V_{DS} = -4 V, I _D = -3.5 A			0.7		1
Gate-to-Source Charge	Q _{GS}				1.7		
Gate-to-Drain Charge	Q _{GD}				2.6		
SWITCHING CHARACTERISTICS (Note	4)						
Turn-On Delay Time	t _{d(on)}	V_{GS} = -4.5 V, V_{DS} = -4 V, I_{D} = -1.2 A, R_{G} = 6.0 Ω			8.0		ns
Rise Time	t _r				15		
Turn-Off Delay Time	t _{d(off)}				38		
Fall Time	t _f				42		
DRAIN-SOURCE DIODE CHARACTER	STICS						
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V,	$T_J = 25^{\circ}C$		-0.7	-1.2	V
		$I_{\rm S} = -1.2 \rm{A}$	T _J = 125°C		-0.6		
Reverse Recovery Time	t _{RR}	V _{GS} = 0 V, dI _{SD} /dt = 100 A/μs, I _S = -1.2 A			18		ns
Charge Time	ta				8.0		
Discharge Time	t _b				10		1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
Pulse Test: pulse width ≤ 300 ms, duty cycle ≤ 2%.
Switching characteristics are independent of operating junction temperatures.

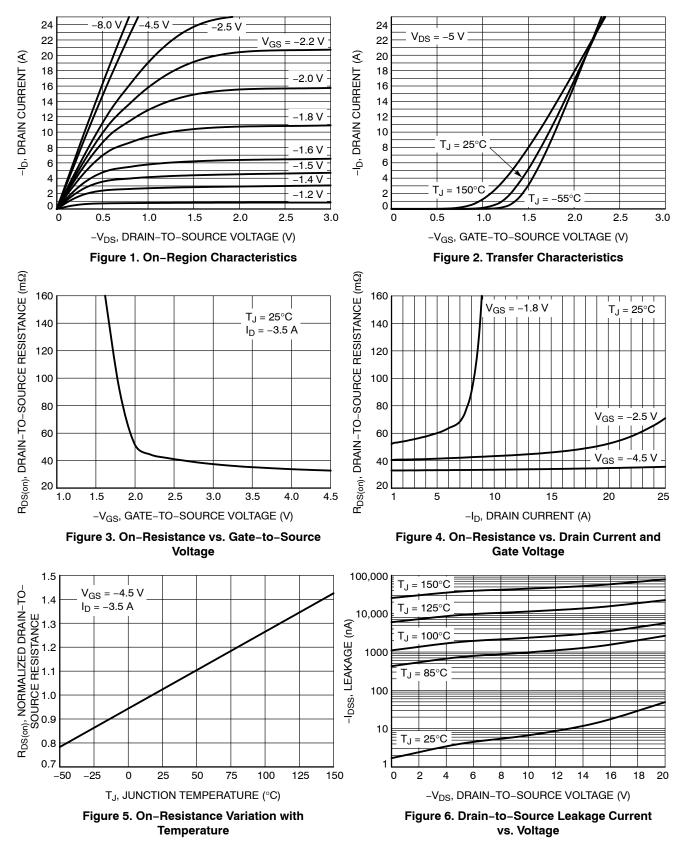
6.9

nC

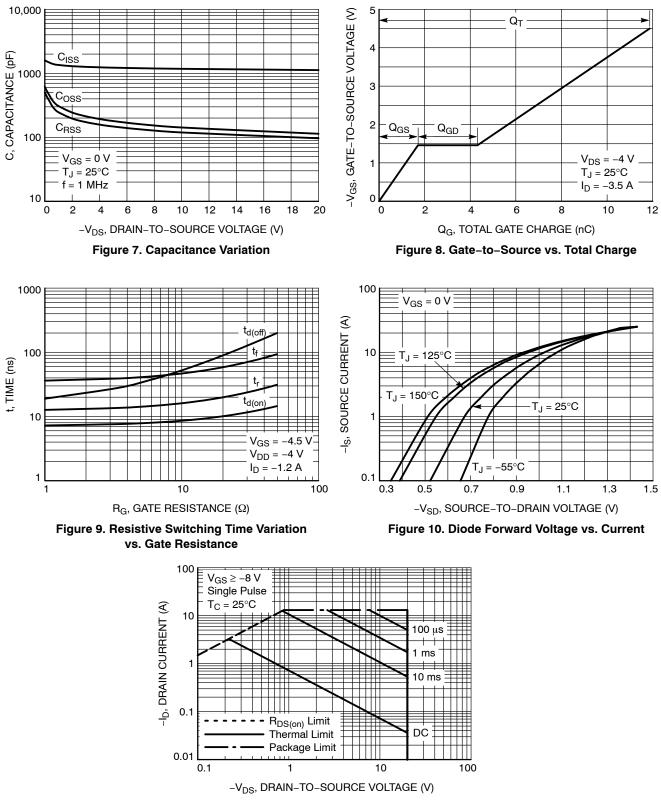
Reverse Recovery Charge

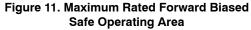
Q_{RR}

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS





TYPICAL CHARACTERISTICS

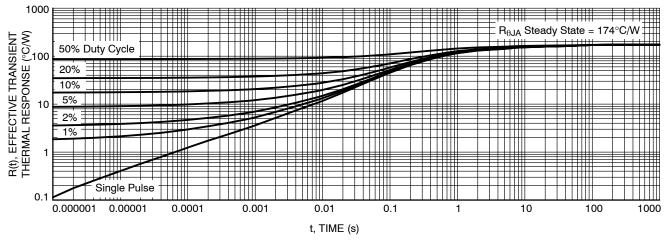


Figure 12. Thermal Impedance (Junction-to-Ambient)





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