MOSFET – Power, Single, N-Channel, SOT-23

20 V, 3.2 A

Features

- Leading Planar Technology for Low Gate Charge / Fast Switching
- 2.5 V Rated for Low Voltage Gate Drive
- SOT-23 Surface Mount for Small Footprint
- NVR Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Load/Power Switch for Portables
- Load/Power Switch for Computing
- DC-DC Conversion

MAXIMUM RATINGS (T_J= 25°C unless otherwise stated)

Parameter			Symbol	Value	Unit		
Drain-to-Source Voltage			V _{DSS}	20	V		
Gate-to-Source Voltage			V _{GS}	±12	V		
Continuous Drain Current (Note 1)	Steady State	$T_A = 25^{\circ}C$	۱ _D	3.2	А		
		T _A = 85°C		2.4	А		
Steady State Power Dissipation (Note 1)	Steady State		Steady State		PD	1.25	W
Pulsed Drain Current	t _p = 10 μs		I _{DM}	10.0	А		
Operating Junction and Storage Temperature			T _J , T _{stg}	–55 to 150	°C		
Continuous Source Current (Body Diode)			I _S	1.6	А		
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)				260	°C		

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient (Note 1)	$R_{\theta JA}$	100	°C/W
Junction-to-Ambient (Note 2)	$R_{\theta JA}$	300	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Surface-mounted on FR4 board using 1 in sq pad size
- (Cu area = 1.127 in sq [1 oz] including traces).
- 2. Surface-mounted on FR4 board using the minimum recommended pad size.

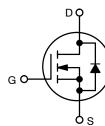


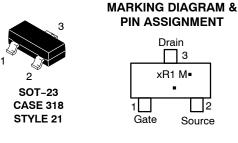
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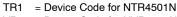
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V _{(BR)DSS}	R _{DS(on)} Typ	I_D Max (Note 1)
20 V	70 mΩ @ 4.5 V	3.6 A
	88 mΩ @ 2.5 V	3.1 A









- VR1 = Device Code for NVR4501N Μ
 - = Date Code*
 - = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

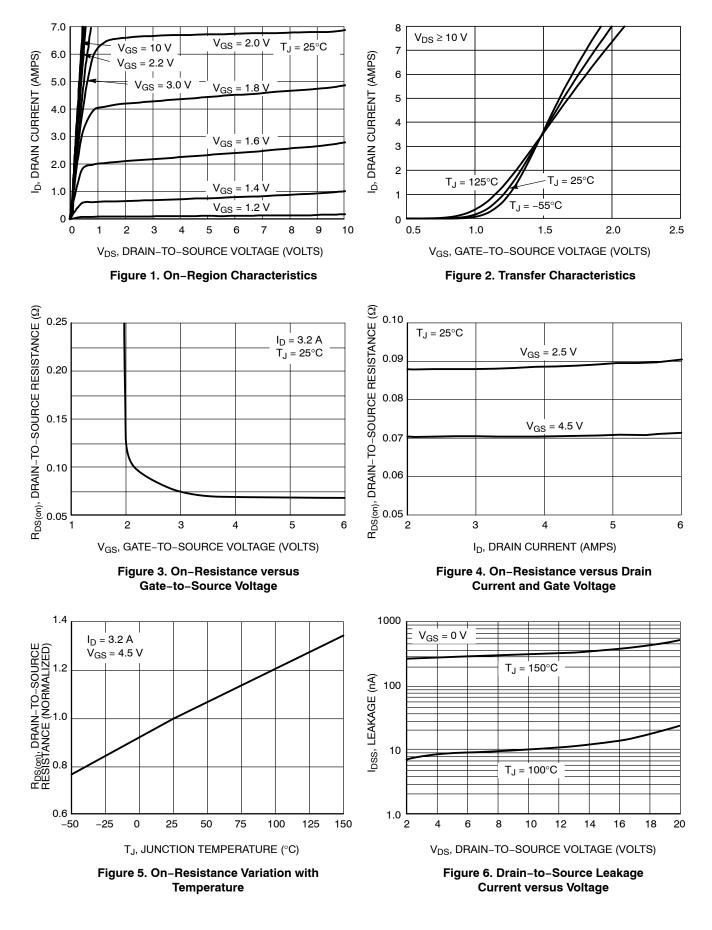
Device	Package	Shipping†
NTR4501NT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
NVR4501NT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

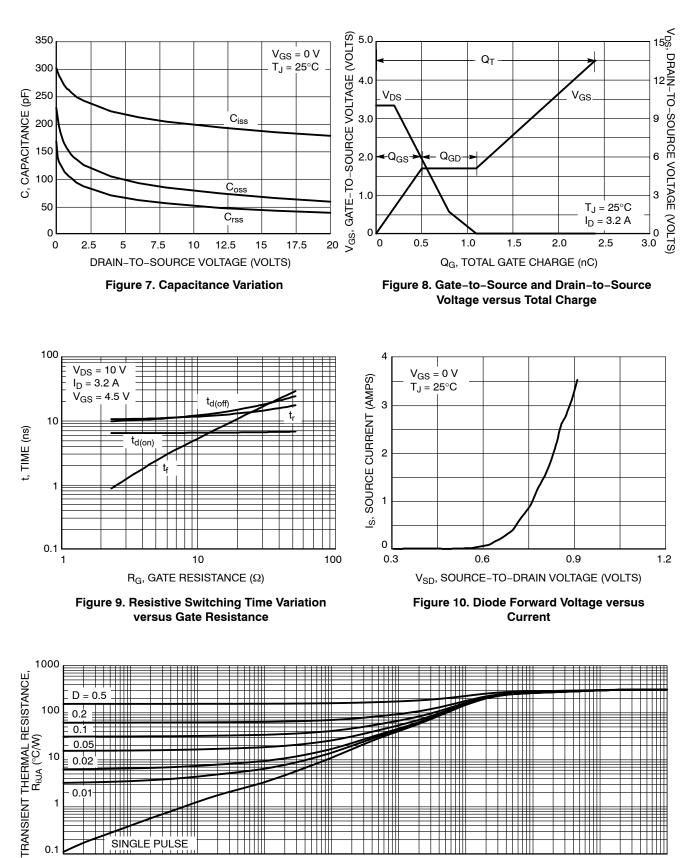
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Electrical Characteristics ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Con	dition	Min	Тур	Max	Units
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage (Note 3)	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA		20	24.5		V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				22		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	$T_J = 25^{\circ}C$			1.5	μA
		V _{DS} = 16 V	$T_J = 85^{\circ}C$			10	μA
Gate-to-Source Leakage Current	I _{GSS}	V_{DS} = 0 V, V_{GS} = ±12 V				±100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage (Note 3)	V _{GS(TH)}	V _{GS} = V _{DS} , I _I	_D = 250 μA	0.65		1.2	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				-2.3		mV/°C
Drain-to-Source On Resistance	_	V_{GS} = 4.5 V, I _D = 3.6 A			70	80	mΩ
	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 3.1 A			88	105	
Forward Transconductance	9fs	$V_{DS} = 5.0 \text{ V}, \text{ I}_{D} = 3.6 \text{ A}$			9		S
CHARGES AND CAPACITANCES	•				•		•
Input Capacitance	C _{iss}	V _{GS} = 0 V, f = 1.0 MHz, V _{DS} = 10 V			200		pF
Output Capacitance	C _{oss}				80		
Reverse Transfer Capacitance	C _{rss}				50		
Total Gate Charge	Q _{G(TOT)}	V_{GS} = 4.5 V, V_{DS} = 10 V, I _D = 3.6 A			2.4	6.0	nC
Gate-to-Source Gate Charge	Q _{GS}				0.5		
Gate-to-Drain Charge	Q _{GD}				0.6		
SWITCHING CHARACTERISTICS (Note 4)	•				•		
Turn-On Delay Time	t _{d(on)}				6.5	13	ns
Rise Time	t _r	V _{GS} = 4.5 V, V	/ns = 10 V.		12	24	
Turn-Off Delay Time	t _{d(off)}	I _D = 3.6 A, R	_G = 6.0 Ω		12	24	
Fall Time	t _f	1			3	6	1
SOURCE-DRAIN DIODE CHARACTERISTICS	5				•	•	•
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V, Is	_{SD} = 1.6 A		0.8	1.2	V
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V}, \\ d_{IS}/d_t = 100 \text{ A}/\mu\text{s}, \\ I_S = 1.6 \text{ A}$			7.1		
Charge Time	t _a				5		ns
Discharge Time	t _b				1.9		
Reverse Recovery Charge	Q _{RR}				3.0		nC

Pulse Test: Pulse width ≤ 300 μs, duty cycle ≤ 2%.
Switching characteristics are independent of operating junction temperatures.





0.1

1

0.01

PULSE TIME, tp (s) Figure 11. Thermal Response

0.001

0.0001

SINGLE PULSE

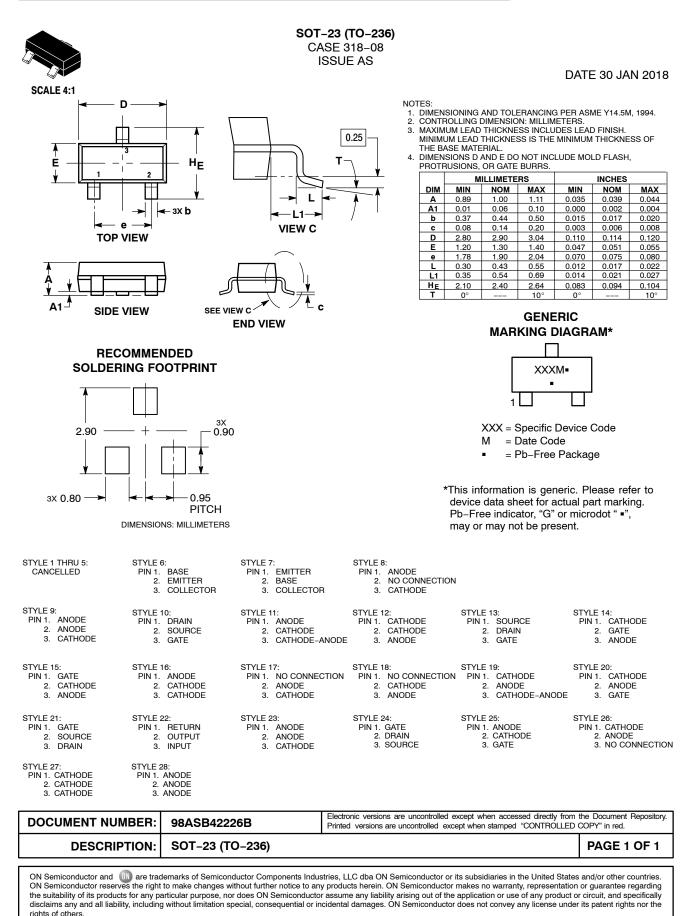
0.00001

0.1 0.000001 10

100

1000





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