MOSFET - Power

-60 V, -20 A, 52 m Ω

Features

- Low R_{DS(on)}
- Fast Switching
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Load Switches
- DC Motor Control
- DC-DC Conversion

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Param	Symbol	Value	Unit		
Drain-to-Source Voltage	V_{DSS}	-60	V		
Gate-to-Source Voltage			V_{GS}	±20	٧
Continuous Drain		T _A = 25°C	I _D	-5.7	Α
Current R _{θJA} (Note 1)		T _A = 100°C		-4.0	
Power Dissipation R _{θJA}	T _A = 25°C		P_{D}	3.2	W
(Note 1)	Steady	T _A = 100°C		1.6	
Continuous Drain	State	T _C = 25°C	I _D	-20	Α
Current R _{θJC} (Note 1)		T _C = 100°C		-14	
Power Dissipation		T _C = 25°C	P_{D}	40	W
R _{θJC} (Note 1)		$T_C = 100^{\circ}C$		20	
Pulsed Drain Current	t _p = 10 μs	i	I _{DM}	-76	Α
Operating Junction and S	T _J , T _{stg}	–55 to +175	°C		
Source Current (Body Did	I _S	-20	Α		
Single Pulse Drain-to-So	E _{AS}	45	mJ		
lanche Energy		I _{AS}	30	Α	
Lead Temperature for So (1/8" from case for 10 s)	TL	260	°C		

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ hetaJC}$	3.8	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{ hetaJA}$	47	

 Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces.

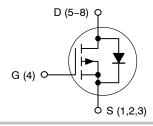


ON Semiconductor®

www.onsemi.com

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX	
-60 V	52 mΩ @ –10 V	-20 A	
	72 mΩ @ -4.5 V	-20 A	

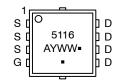
P-Channel MOSFET





CASE 511AB

MARKING DIAGRAM



5116 = Specific Device Code A = Assembly Location

Y = Year WW = Work Week ■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NTTFS5116PLTAG	WDFN8 (Pb-Free)	1500/Tape & Reel
NTTFS5116PLTWG	WDFN8 (Pb-Free)	5000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS			•		•		•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$		-60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				69.7		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C			-1.0	μΑ
		$V_{DS} = -60 \text{ V}$	T _J = 125°C			-100	1
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} :	= ±20 V			±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = -$	-250 μΑ	-1		-3	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				-6.2		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = −10 V	I _D = -6 A		37	52	mΩ
		V _{GS} = -4.5 V	$I_D = -4.4 \text{ A}$		51	72	
Forward Transconductance	9FS	$V_{DS} = -15 \text{ V}, I_D = -6 \text{ A}$			11		S
CHARGES, CAPACITANCES AND GA	ATE RESISTAN	NCE	•		•		•
Input Capacitance	C _{iss}	V _{GS} = 0 V, f = 1.0 MHz, V _{DS} = -30 V			1258		pF
Output Capacitance	C _{oss}				127		1
Reverse Transfer Capacitance	C _{rss}			84		1	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -10 \text{ V}, V_{DS} = -48 \text{ V}, I_D = -5 \text{ A}$ $V_{GS} = -4.5 \text{ V}, V_{DS} = -48 \text{ V}, I_D = -5 \text{ A}$			25		nC
					14		
Threshold Gate Charge	Q _{G(TH)}				1		nC
Gate-to-Source Charge	Q _{GS}	1.,			4		1
Gate-to-Drain Charge	Q_{GD}	$V_{GS} = -4.5 \text{ V}, V_{DS} = -48 \text{ V}, I_{D} = -5 \text{ A}$			7		
Plateau Voltage	V _{GP}				3.1		V
Gate Resistance	R_{G}				5.3		Ω
SWITCHING CHARACTERISTICS (No	ote 3)				•	•	
Turn-On Delay Time	t _{d(on)}				15		ns
Rise Time	t _r	V _{GS} = -4.5 V, V _{DS}	= -48 V.		58		1
Turn-Off Delay Time	t _{d(off)}	I _D = -5 A, R _G	= 6 Ω		30		1
Fall Time	t _f				37		
DRAIN-SOURCE DIODE CHARACTE	RISTICS		•		•		•
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V, I _S = -5 A	$T_J = 25^{\circ}C$		-0.79	-1.2	V
			T _J = 125°C		-0.64		
Reverse Recovery Time	t _{RR}	-			20		ns
Charge Time	ta	$V_{GS} = 0 \text{ V. dis/d}_{+} $	–100 A/us.		15		1
Discharge Time	t _b	V_{GS} = 0 V, d_{IS}/d_t = -100 A/ μ s, I_S = -5 A			5		1
Reverse Recovery Charge	Q _{RR}				19		nC

^{2.} Pulse Test: pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$. 3. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS

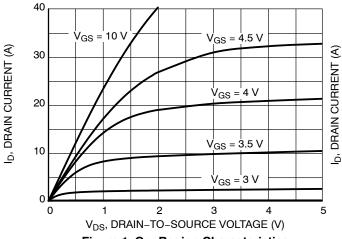


Figure 1. On-Region Characteristics

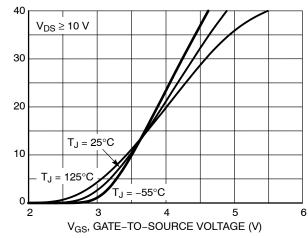


Figure 2. Transfer Characteristics

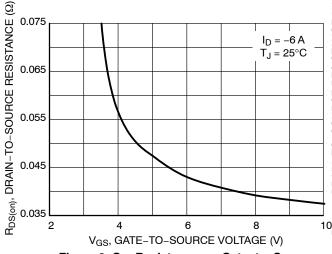


Figure 3. On-Resistance vs. Gate-to-Source Voltage

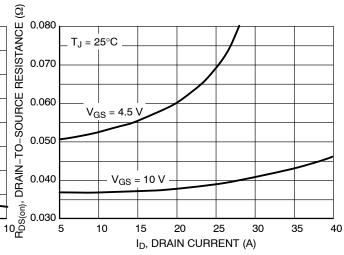


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

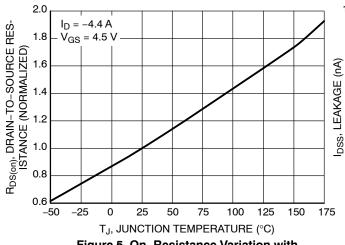


Figure 5. On–Resistance Variation with Temperature

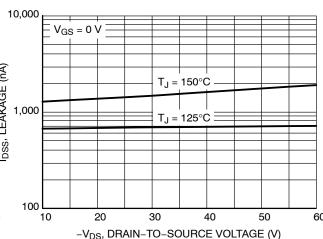
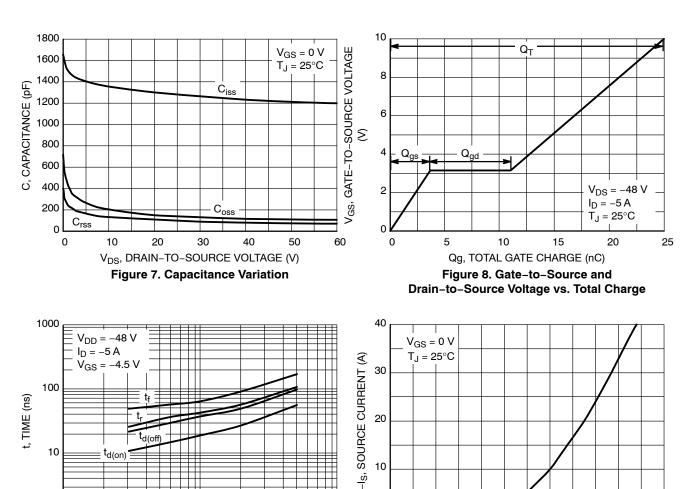


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS



10

100

0.5

 R_G , GATE RESISTANCE (Ω) Figure 9. Resistive Switching Time Variation vs. Gate Resistance

10

10

100

10

0.1

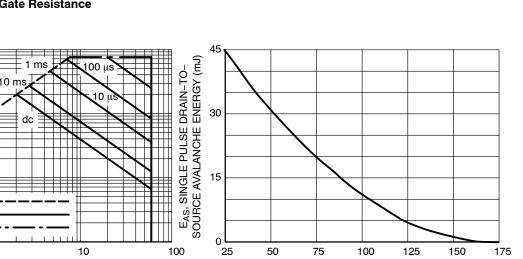
-I_D, DRAIN CURRENT (A)

t_{d(on)}

 $V_{GS} = -10 \text{ V}$

Single Pulse

R_{DS(on)} Limit Thermal Limit Package Limit



-V_{DS}, DRAIN-TO-SOURCE VOLTAGE (V) Figure 11. Maximum Rated Forward Biased Safe Operating Area

T_J, STARTING JUNCTION TEMPERATURE (°C) Figure 12. Maximum Avalanche Energy vs. **Starting Junction Temperature**

0.8

-V_{SD}, SOURCE-TO-DRAIN VOLTAGE (V)

Figure 10. Diode Forward Voltage vs. Current

0.9

TYPICAL CHARACTERISTICS

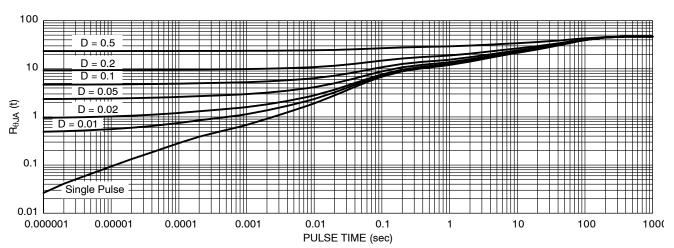
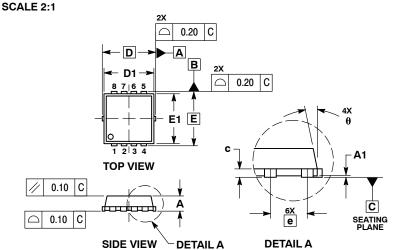


Figure 13. Thermal Response



WDFN8 3.3x3.3, 0.65P CASE 511AB ISSUE D

DATE 23 APR 2012



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSION D1 AND E1 DO NOT INCLUDE MOLD FLASH
 PROTRUSIONS OR GATE BURRS.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.70	0.75	0.80	0.028	0.030	0.031	
A1	0.00		0.05	0.000		0.002	
b	0.23	0.30	0.40	0.009	0.012	0.016	
С	0.15	0.20	0.25	0.006	0.008	0.010	
D		3.30 BSC		0	0.130 BSC		
D1	2.95	3.05	3.15	0.116	0.120	0.124	
D2	1.98	2.11	2.24	0.078	0.083	0.088	
E	3.30 BSC			0.130 BSC			
E1	2.95	3.05	3.15	0.116	0.120	0.124	
E2	1.47	1.60	1.73	0.058	0.063	0.068	
E3	0.23	0.30	0.40	0.009	0.012	0.016	
е	0.65 BSC			0.026 BSC		0	
G	0.30	0.41	0.51	0.012	0.016	0.020	
K	0.65	0.80	0.95	0.026	0.032	0.037	
L	0.30	0.43	0.56	0.012	0.017	0.022	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
М	1.40	1.50	1.60	0.055	0.059	0.063	
θ	0 °		12 °	0 °		12 °	



GENERIC MARKING DIAGRAM*

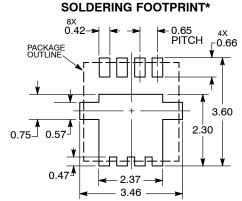


XXXXX = Specific Device Code = Assembly Location

= Year WW = Work Week = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.



DIMENSION: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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