# **<u>MOSFET</u>** – Power, Single, N-Channel 40 V, 4.8 mΩ, 74 A

#### Features

- Small Footprint (3.3 x 3.3 mm) for Compact Design
- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Capacitance to Minimize Driver Losses
- These Devices are Pb-Free and are RoHS Compliant

#### **MAXIMUM RATINGS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

Parar	Symbol	Value	Unit		
Drain-to-Source Voltag	V <sub>DSS</sub>	40	V		
Gate-to-Source Voltage	V <sub>GS</sub>	±20	V		
Continuous Drain		T <sub>C</sub> = 25°C	I <sub>D</sub>	74	А
Current R <sub>θJC</sub> (Notes 1, 2, 3, 4)	Steady	T <sub>C</sub> = 100°C		42	
Power Dissipation	State	$T_{C} = 25^{\circ}C$	PD	50	W
$R_{\theta JC}$ (Notes 1, 2, 3)		$T_{C} = 100^{\circ}C$		16	
Continuous Drain		$T_A = 25^{\circ}C$	Ι <sub>D</sub>	19	А
Current R <sub>θJA</sub> (Notes 1, 3, 4)	Steady State	T <sub>A</sub> = 100°C		13	
Power Dissipation		T <sub>A</sub> = 25°C	PD	3.1	W
$R_{\theta JA}$ (Notes 1, 3)		$T_A = 100^{\circ}C$		1.6	
Pulsed Drain Current	$T_A = 25^{\circ}C, t_p = 10 \ \mu s$		I <sub>DM</sub>	321	А
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	–55 to +175	°C
Source Current (Body Diode)			I <sub>S</sub>	42	А
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 4.6 A)			E <sub>AS</sub>	104	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			ΤL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS (Note 1)

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 3)	$R_{\theta JC}$	3.0	°C/W
Junction-to-Ambient - Steady State (Note 3)	$R_{\theta JA}$	47.7	

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

2. Psi  $(\Psi)$  is used as required per JESD51–12 for packages in which substantially less than 100% of the heat flows to single case surface.

3. Surface-mounted on FR4 board using a 650 mm<sup>2</sup>, 2 oz. Cu pad.

4. Continuous DC current rating. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

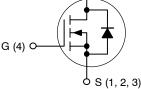


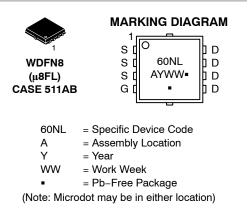
## **ON Semiconductor®**

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V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX
40 V	4.8 mΩ @ 10 V	74 A
40 V	7.6 mΩ @ 4.5 V	748







#### **ORDERING INFORMATION**

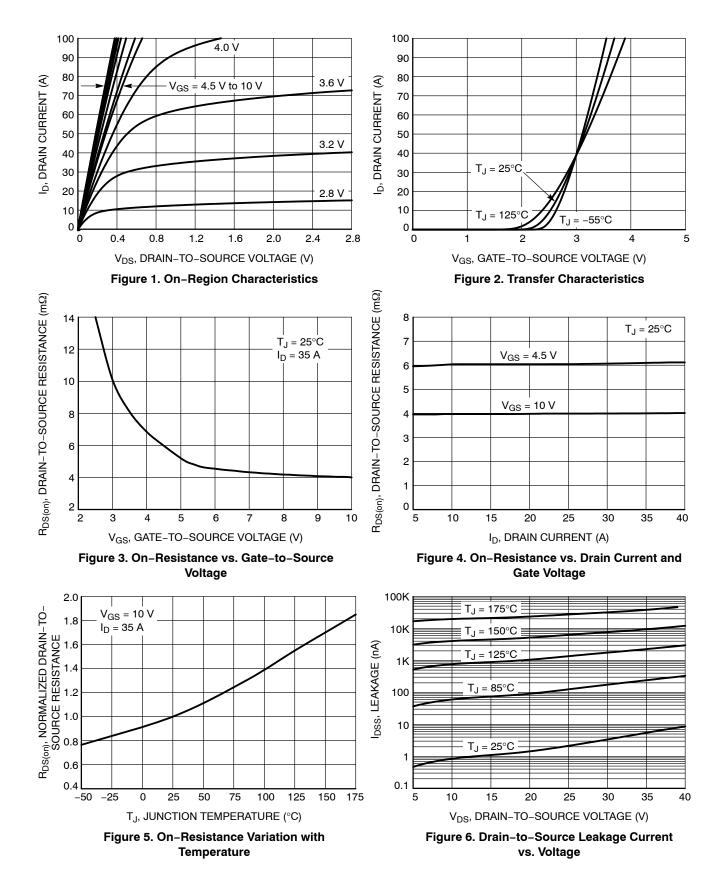
See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

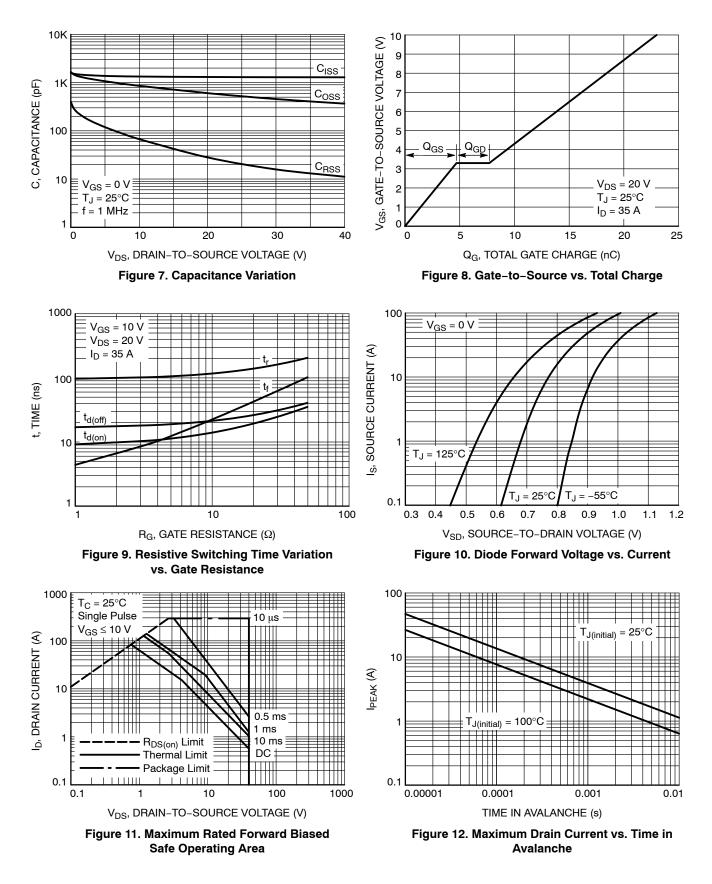
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS		•			•		-
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, I <sub>D</sub> = 250 $\mu$ A		40			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 40 V	$T_J = 25^{\circ}C$			10	μA
		$V_{DS} = 40 V$	T <sub>J</sub> = 125°C			250	1
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>0</sub>	<sub>GS</sub> = 20 V			100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS}$ = $V_{DS}$ , $I_D$ = 40 $\mu$ A		1.2		2.0	V
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V,	I <sub>D</sub> = 35 A		4	4.8	mΩ
		V <sub>GS</sub> = 4.5 V,	I <sub>D</sub> = 35 A		6.1	7.6	1
Forward Transconductance	9 <sub>FS</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 35 A			72		S
CHARGES AND CAPACITANCES							
Input Capacitance	C <sub>iss</sub>				1300		pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, f = V <sub>DS</sub> = 2	1.0 MHz, 25 V		530		
Reverse Transfer Capacitance	C <sub>rss</sub>				22		
Threshold Gate Charge	Q <sub>G(TH)</sub>				2.5		nC
Gate-to-Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 20 V, I <sub>D</sub> = 35 A			4.7		1
Gate-to-Drain Charge	$Q_{GD}$				3		1
Total Gate Charge	Q <sub>G(TOT)</sub>	$V_{GS}$ = 10 V, $V_{DS}$ =	20 V, I <sub>D</sub> = 35 A		11		nC
SWITCHING CHARACTERISTICS (No	te 6)						
Turn-On Delay Time	t <sub>d(on)</sub>				9.2		ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10 V, V	<sub>DS</sub> = 20 V,		97		1
Turn-Off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> = 10 V, V <sub>I</sub> I <sub>D</sub> = 35	5Ă		17		1
Fall Time	t <sub>f</sub>	1			4.4		1
DRAIN-SOURCE DIODE CHARACTER	ISTICS						
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V,	$T_J = 25^{\circ}C$		0.86	1.2	V
		$I_{\rm S} = 35 \rm A$	T <sub>J</sub> = 125°C		0.75		1
Reverse Recovery Time	t <sub>RR</sub>		•		29		ns
Charge Time	t <sub>a</sub>	V <sub>GS</sub> = 0 V, dl <sub>S</sub> /dt = 100 A/µs,			14		1
Discharge Time	t <sub>b</sub>	$I_{\rm S} = 35$	5A ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		14		1
Reverse Recovery Charge	Q <sub>RR</sub>	1			12		nC

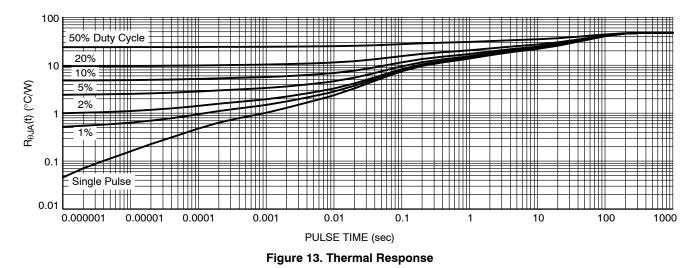
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 5. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2%. 6. Switching characteristics are independent of operating junction temperatures.

#### **TYPICAL CHARACTERISTICS**



#### **TYPICAL CHARACTERISTICS**





#### **DEVICE ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>
NTTFS5C460NLTAG	60NL	WDFN8 (Pb–Free)	1500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

DATE 23 APR 2012





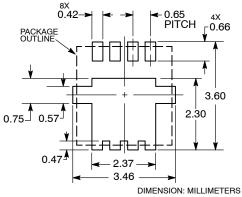
Pb-Free indicator, "G" or microdot " .", may or may not be present.

NOTES: LES: DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. DIMENSION D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS. 1.

2. 3.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.70	0.75	0.80	0.028	0.030	0.031	
A1	0.00		0.05	0.000		0.002	
b	0.23	0.30	0.40	0.009	0.012	0.016	
с	0.15	0.20	0.25	0.006	0.008	0.010	
D		3.30 BSC		0.130 BSC			
D1	2.95	3.05	3.15	0.116	0.120	0.124	
D2	1.98	2.11	2.24	0.078	0.083	0.088	
E	3.30 BSC			0.130 BSC			
E1	2.95	3.05	3.15	0.116	0.120	0.124	
E2	1.47	1.60	1.73	0.058	0.063	0.068	
E3	0.23	0.30	0.40	0.009	0.012	0.016	
е		0.65 BSC	;	(	0.026 BSC		
G	0.30	0.41	0.51	0.012	0.016	0.020	
к	0.65	0.80	0.95	0.026	0.032	0.037	
L	0.30	0.43	0.56	0.012	0.017	0.022	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
м	1.40	1.50	1.60	0.055	0.059	0.063	
θ	0 °		12 °	0 °		12 °	

SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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