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### LED FRONTLIGHTING SOLUTIONS APG

NCV78763 LDM A – REFERENCE DESIGN K QUICK START GUIDE & SYSTEM OVERVIEW

Document version 1.6-29th of Aug 2016

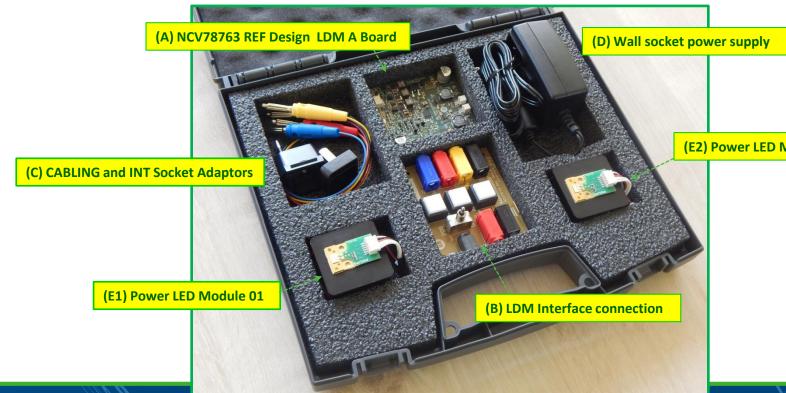
Christiam Gasparini
System Application Engineer
Automotive APG/AAB BU - Belgium





#### NCV78763 REF DES LDM A - KIT OVERVIEW

 The NCV78763 REF Design LDM A KIT v1.1 is meant for Autom Front Lighting Advanced Applications for Day Time Running Li Position Light (PL) and Turn Indicator (TURN), aimed for equivalent system replacement and migration to Power LEDS.

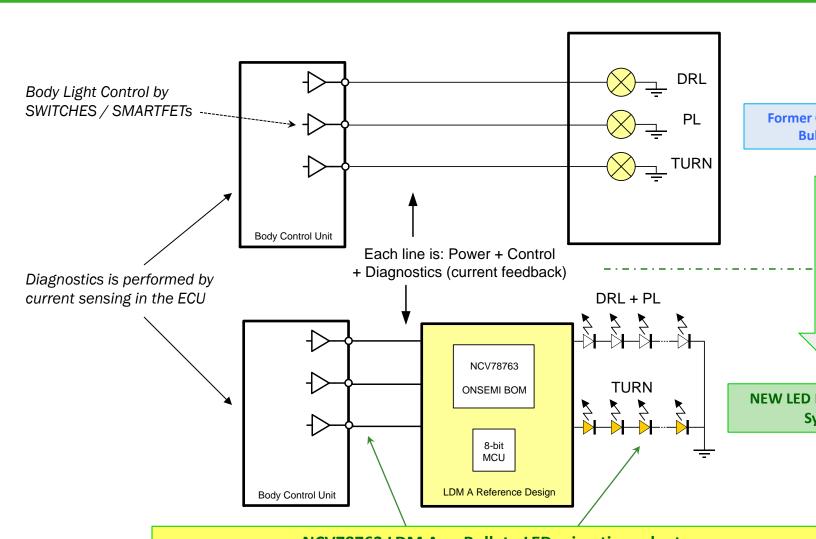


#### NCV78763 REF DES LDM A - Main Features sheet

- The NCV78763 REF Design LDM A system solution is based on NCV78763 Power LED Driver for Front Lighting (APG/AAB BU), v ONSEMI BOM portfolio for external power devices.
- The module features failure diagnostics as open load detection, shor protection and thermal protection lock out. A low pin count 8-bit micro is embedded on board for such monitoring and control.
- The LED strings (DRL/PL) are driven according to advanced dimming techniques including logarithmic ramps analog and digital modulatio enhanced human eye sensitivity smoothness perception. The implem fully exploits the SPI bus and LED Driver settings.
- Other key characteristics of the NCV78763 LDM A include: con electronics, compatibility target with automotive bulb architection.
   ONSEMI Platform design approach for:
  - a) Compatibility with a wide range of LED strings configuration without B
  - b) Easily customizable to the specific system requirements.
  - c) Wide supply VBAT Operating range from 5V to 20V or higher.



### NCV78763 REF DES LDM A & Bulb Systems Compati

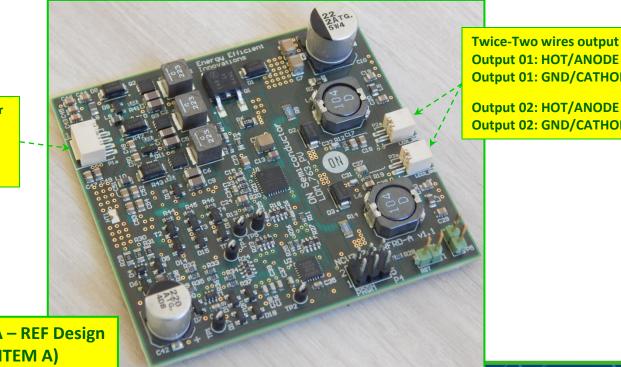


NCV78763 LDM A as Bulb to LED migration adaptor The module inputs to outputs functionality is configurable via the embedded MCU



#### NCV78763 LDM A REF DES KIT: Main board LDM

- The **NCV78763 LDM A Board**, core of the system, is shown hereby. It sized to allow the outputs to drive LED strings for a total power of all and up to 60V voltage.
- LED Chan 01 is dedicated to DRL and PL, controlled via logarithmic slopes ramp, while channel 02 performs the turn indicator (blinker) f



Four ways input connector

Input 01: DRL Input 02: PL Input 03: TURN Input 04: GND

> NCV78763 LDM A – REF Design Board (KIT ITEM A)

#### NCV78763 LDM A REF DES KIT: the LDM Interface

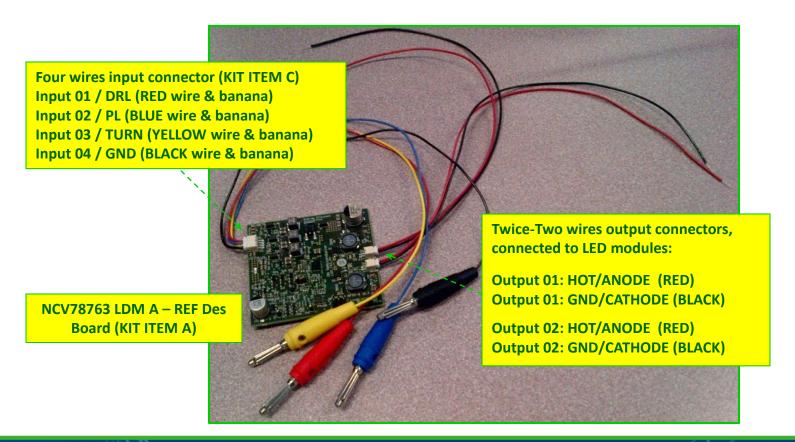
The LDM REF Design A Interface (ITEM B) grants additional for use of an wall socket power supply and allows straigh commands by the user via buttons and switch.

**BUTTONS & spy LEDS** 1: LDM INPUT 02 / POSITION 2: LDM INPUT 01 / DRL / F SWITCH - PL/ DRL/OPEN 3: LDM INPUT 03 / TURN / Wall socket - Light power supply **Plugs towards** Semiconductor LAB Bench power supply GND bar for oscilloscope probe(s) **General "power available" LED** 

wire input inte

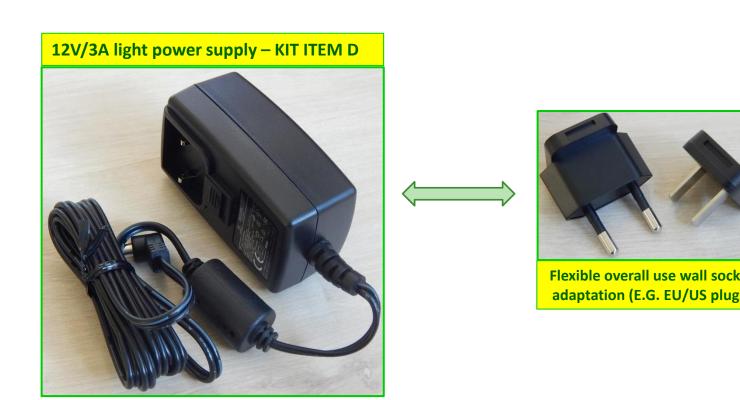
### NCV78763 LDM A REF DES KIT: Cabling & connection

 The NCV78763 LDM A KIT modules come equipped with call ITEM C) for easy interface towards a standard bench power standard bench power standard LDM REF Design A interface (KIT ITEM B):



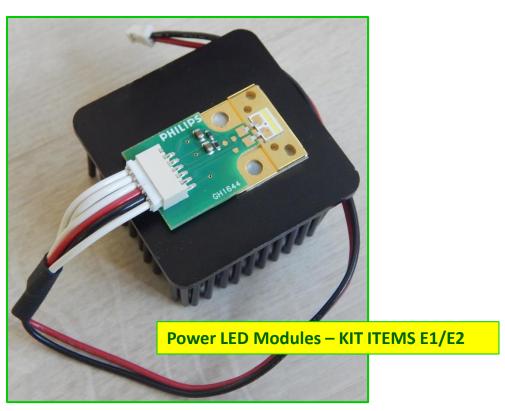
#### NCV78763 LDM A REF DES KIT: Power Supply and plug a

 The provided power supply can be directly plugged in the LDM into absence of a bench power supply, or for highly portable ready-to-go d



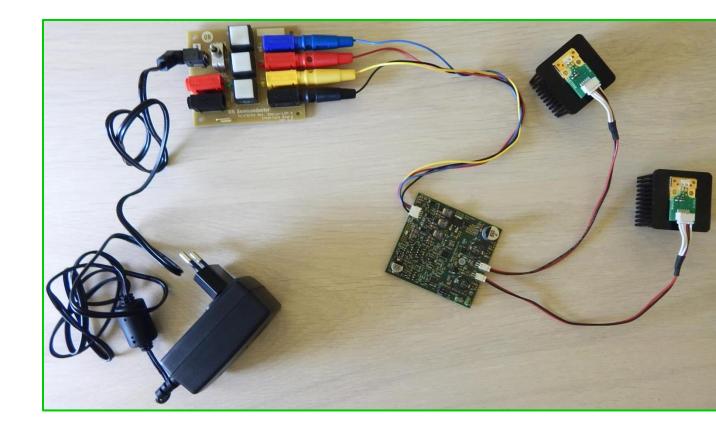
#### NCV78763 LDM A REF DES KIT: Power LED Modules

For reference and immediate use, the KIT contains commercial Au LED Modules (Philips - GH1644). Two samples are available per KIT plates are equipped a customized compact heatsink and cabli plugged to the NCV78763 LDM A.



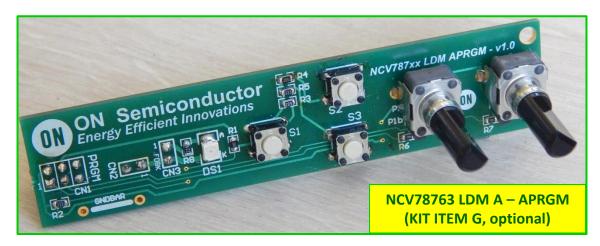
### NCV78763 LDM A REF DES KIT: the entire setup system

The connections of all previous elements brings to the followin



#### NCV78763 LDM A REF DES KIT: The APRGM (optional)

- The standard **factory firmware** is set to drive the default KIT LED mo 10W power in steady state (around 580mA DC current). Using a differential string below 22V, the same current will be applied.
- The system embeds enhanced feature, allowing to adapt parare completely different string and/or headlight per means of the prosphere shown (APRGM). The current levels for channels 01 and 02 are adjusted the two respective trimmers. The microcontroller takes care of safe items such as power level and voltage adjustments, current limits EEPROM memory save.



#### NCV78763 LDM A REF DES KIT: APRGM - Setup

In order to allow the current and LED strings programming proce APRGM board, when the system is in **power off**, must first be plugge three **LDM A main board** connectors "P4" "S1" and "P5" (respection CN2 and CN3 in APRGM).



NCV78763 LDM A – APRGM: LED current programming procedure setup

#### NCV78763 LDM A REF DES KIT: APRGM - Procedure #1

- After setup, the LED current programming procedure is actually enter powering up the system, the button S3 is kept pressed. Details on the procedure reported below.
- 1. When entering programming procedure, the LED DS1 will start blinking button S3. Before driving current into the LED strings, the microcontroller wi the user brings the trimmers P1 and P2 (current setting knobs) to the position (counter-clock-wise) to preselect the minimum current level: whe position is attained, the LDM will drive currents in both LED strings. At any sequence can be exited without save by pressing "S2";
- Adjust the current in the LEDs. The right setting can be found by hun perception such as lighting of a dark room (never look directly in the LED potential avoid health hazard or dazzling!), light intensity measurement, measurement probe, or series multimeter in current sensing mode;
- 3. Once the desired current is found, new settings are save by pressing "S1";
- 4. As an alternative, original factory settings can always be restored with the "S3

Please note that any LED string between 2V and 60V can be used. The firmware the current so that 10W maximum per channel can be delivered.



### NCV78763 LDM A REF DES KIT: APRGM - Procedure #2

 Please refer to the following picture for a graphical explanation of current setting programming procedure.



#### **Button S1:**

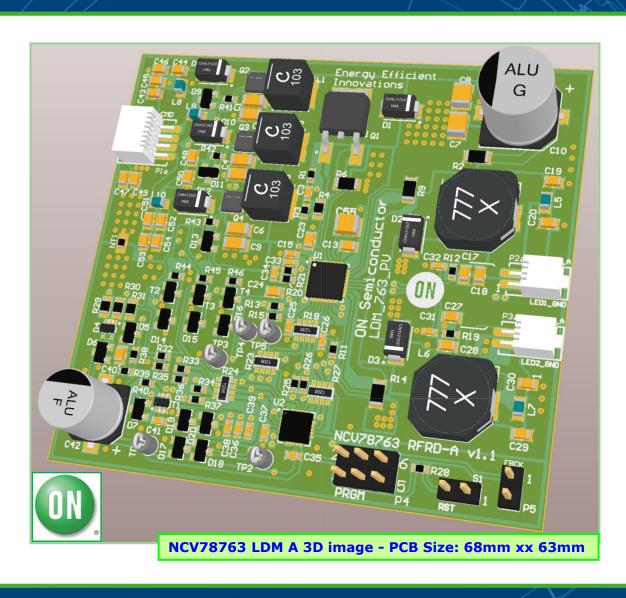
- Exit programming feature and Save new user settings

#### **Button S3:**

- Enter procedure while powering up
- Save factory settings when programming procedure is r

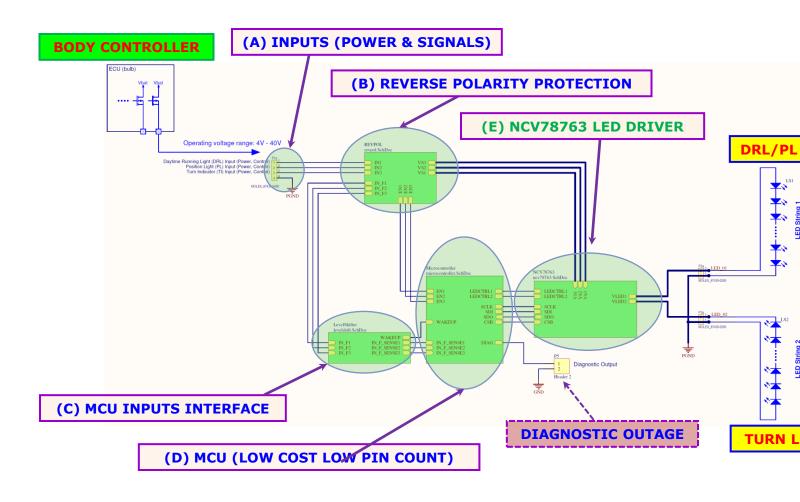


### NCV78763 LDM A REF DES: PCB Board 3D Overview

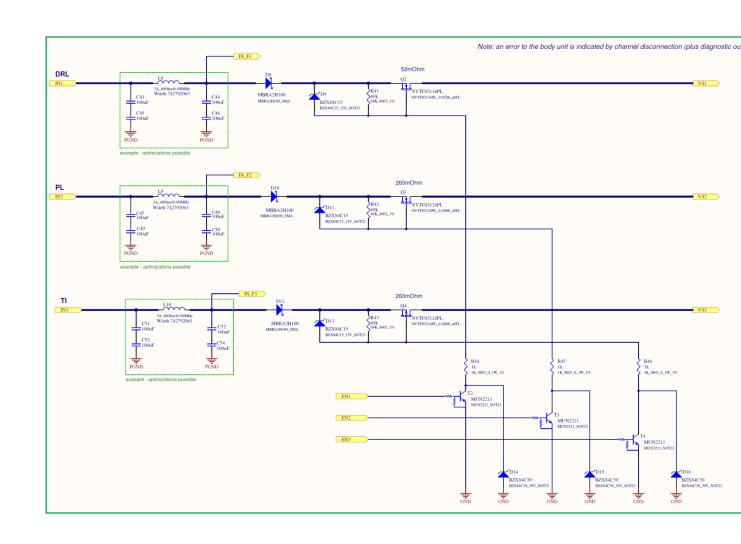


#### NCV78763 LDM A REF DES: Schematics (top-level view

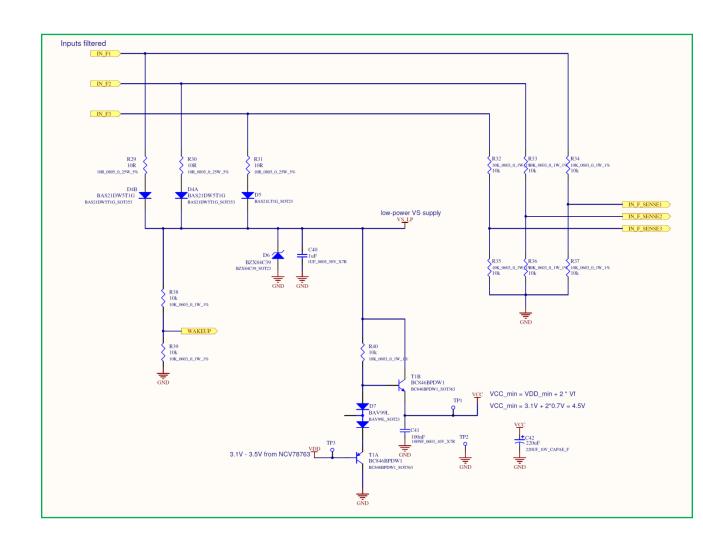
The LDM A REF DES v1.1 top level block schematics view is shown be



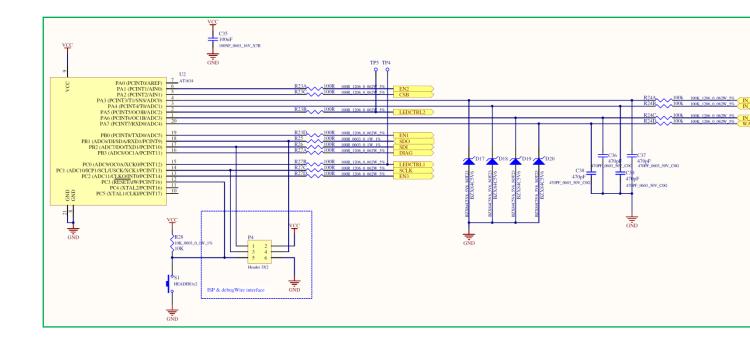
#### NCV78763 LDM A REF DES: sub-circuit #B: reverse b



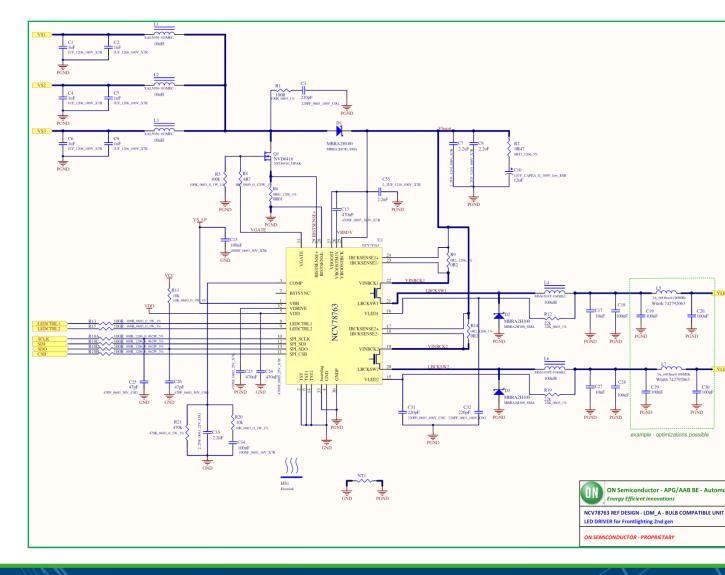
#### NCV78763 LDM A REF DES: sub-circuit #C: MCU inte



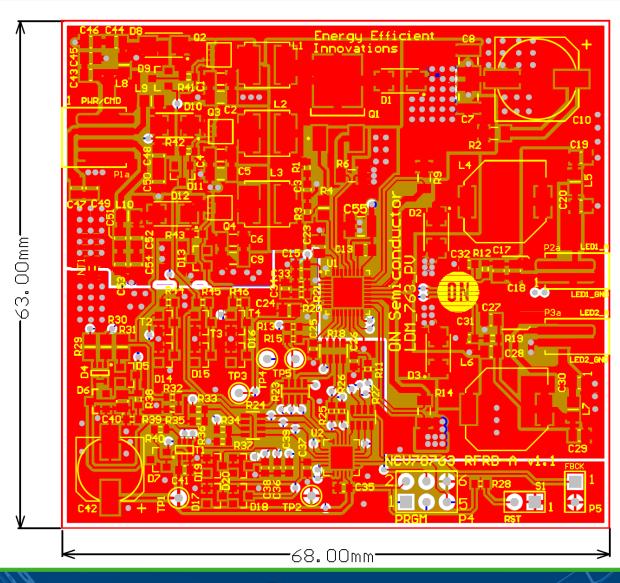
#### NCV78763 LDM A REF DES: sub-circuit #D: MCU



### NCV78763 LDM A REF DES: sub-circuit #E: NCV7876



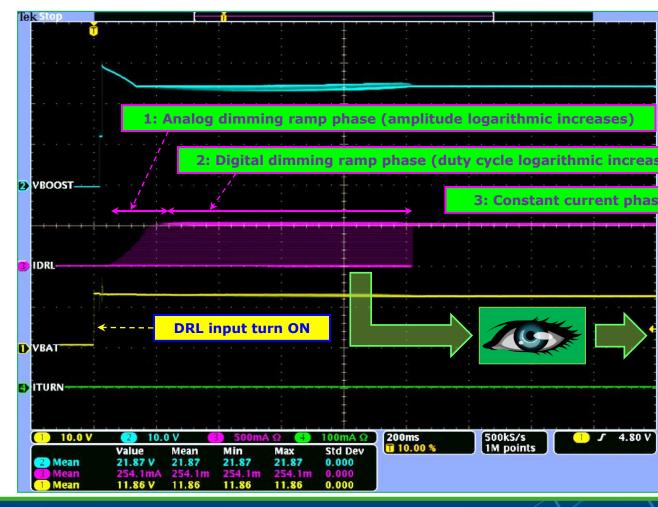
# NCV78763 LDM A REF DES: PCB (top side)





# Measurement report: DRL ramp turn-o

 At power on (line #1 = DRL input), the DRL led string is ramped to the nomin value by combining analog and digital dimming (logarithmic mode is shown).



# System steady state stability (low VBAT = 7



# System steady state stability (nom VBAT =



# System steady state stability (nom VBAT =



# PSRR stability (VBAT = 10V >>> 20V)



# PSRR stability (VBAT = 20V >>> 10V)



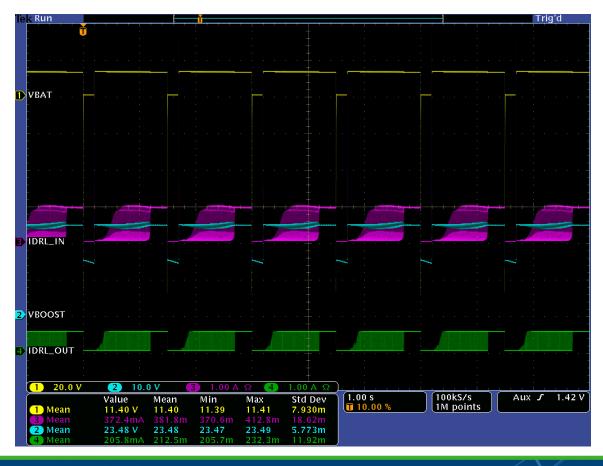
### Diagnostic & specific input turn-off

- In this diagnostic detection example, the output VLED is shorted to G
- The module turns off the input to flag to the body controller the failu



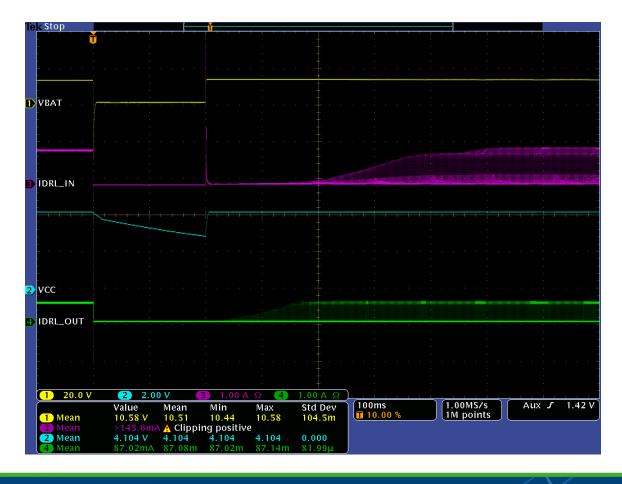
## **ISO 7637 pulses (type 1)**

The LDM A module is validated through the standard ISO 7637 pulses concerns pulse type 1, it is shown how it correctly restarts normal opeach pulse iteration.



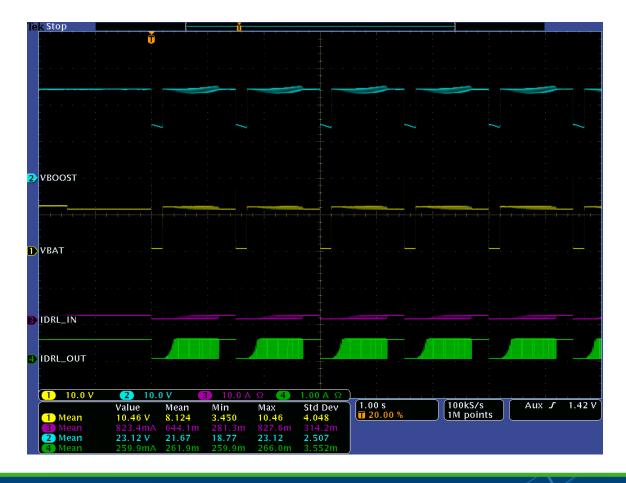
### ISO 7637 pulses (type 1 - ZOOM)

• The module correctly restarts normal operation at each pulse <a href="Note">Note</a>: No reset from the MCU.



# ISO 7637 pulses (type 2a)

 Also in this case, the module correctly restarts normal operation at pulse iteration.



# ISO 7637 pulses (type 3a)

• The highest amplitude disturbance (-150V) does not minimally affermedule operation.



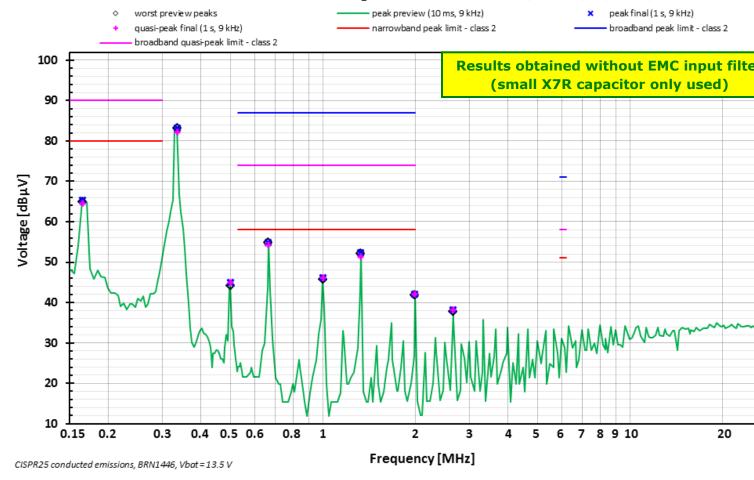
# ISO 7637 pulses (type 3b)

• The highest disturbance (+150V) does not affect at all the module of



# EMC conducted emission CISPR25

#### NCV78763 reference design board RFRD-A v1.1 (DRL & TI inputs powered)









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# LED FRONTLIGHTING SOLUTIONS AAE NCV78763 LDM A – QUICK START GUIDE - OVER

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