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NVATS68301PZ

Power MOSFET -100 V, 75 m Ω , -31 A, P-Channel

The NVATS68301PZ is a power MOSFET designed for compact size and high efficiency which can achieve high thermal performance. AEC-Q101 qualified MOSFET and PPAP capable suitable for automotive applications.

Features

- Low On-Resistance
- High Current Capability
- 100% Avalanche Tested
- AEC-Q101 qualified and PPAP capable
- ATPAK package is pin-compatible with DPAK (TO-252)
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

- Reverse Battery Protection
- Load Switch
- Automotive Front Lighting
- Automotive Body Controllers

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	-100	V
Gate to Source Voltage	VGSS	±20	V
Drain Current (DC)	ID	-31	Α
Drain Current (Pulse) PW ≤ 10 μs, duty cycle ≤ 1%	IDP	-124	Α
Power Dissipation Tc=25°C	PD	84	W
Operating Junction and Storage Temperature	Tj, Tstg	-55 to +175	°C
Avalanche Energy (Single Pulse) (Note 2)	EAS	54	mJ
Avalanche Current (Note 3)	IAV	-28	Α

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 2 : V_{DD} = -30 V, L = 100 μ H, IAV = -28 A (Fig.1)
- 3 : L ≤ 100 μH, Single pulse

THERMAL RESISTANCE RATINGS

THERMAE REGISTANSE RATINGS						
Parameter	Symbol	Value	Unit			
Junction to Case Steady State (Tc = 25°C)	R_{θ} JC	1.78	°C/W			
Junction to Ambient (Note 4)	$R_{\theta JA}$	79.3	°C/W			

Note 4 : Surface mounted on FR4 board using a 130 mm², 1 oz. Cu pad

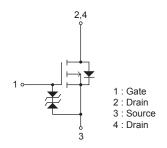


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VDSS	R _{DS} (on) Max	ID Max
-100 V	75 mΩ @ –10 V	-31 A

ELECTRICAL CONNECTION P-Channel





MARKING



ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 5)

Davamatan	Comple ed	Conditions	Value			I Imit
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	$I_D = -1 \text{ mA}, V_{GS} = 0 \text{ V}$	-100			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} = -100 V, V _{GS} = 0 V			-1	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} = ±16 V, V _{DS} = 0 V			±10	μΑ
Gate Threshold Voltage	V _G S(th)	V _{DS} = -10 V, I _D = -1 mA	-2.0		-3.5	V
Forward Transconductance	gFS	V _{DS} = -10 V, I _D = -14 A		29		S
Static Drain to Source On-State Resistance	R _{DS} (on)	I _D = -14 A, V _{GS} = -10 V		57	75	mΩ
Input Capacitance	Ciss	V _{DS} = -20 V, f = 1 MHz		2,850		pF
Output Capacitance	Coss			270		pF
Reverse Transfer Capacitance	Crss			125		pF
Turn-ON Delay Time	t _d (on)	0. 5: 0		26		ns
Rise Time	t _r			150		ns
Turn-OFF Delay Time	t _d (off)	See Fig.2		250		ns
Fall Time	tf			170		ns
Total Gate Charge	Qg			55		nC
Gate to Source Charge	Qgs	$V_{DS} = -60 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -28 \text{ A}$		10		nC
Gate to Drain "Miller" Charge	Qgd			17		nC
Forward Diode Voltage	V _{SD}	I _S = -28 A, V _{GS} = 0 V		-0.88	-1.5	V

Note 5 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Fig.1 Unclamped Inductive Switching Test Circuit

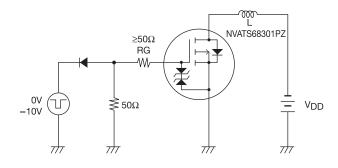
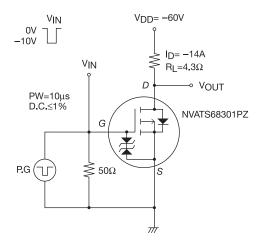
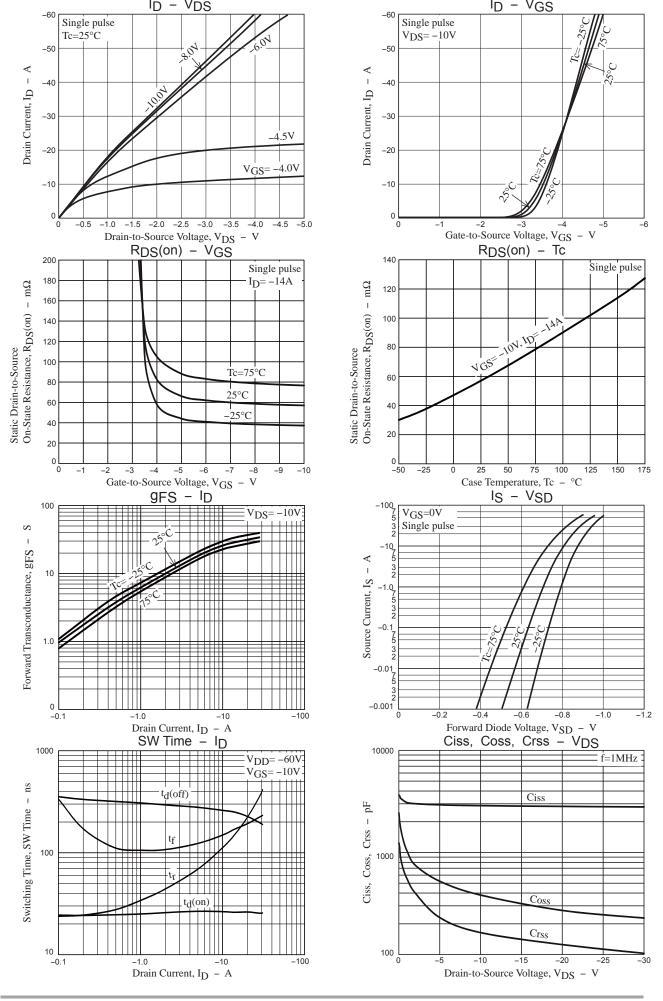
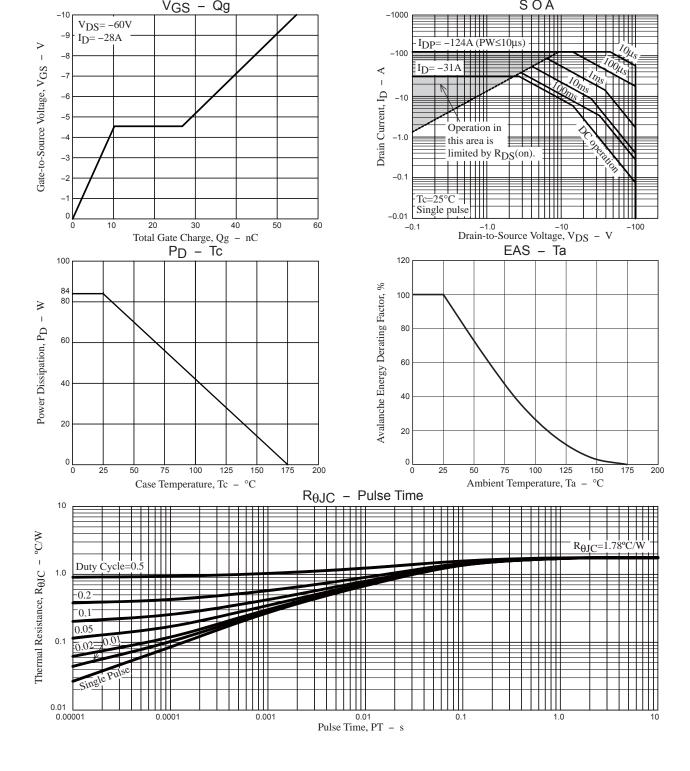


Fig.2 Switching Time Test Circuit





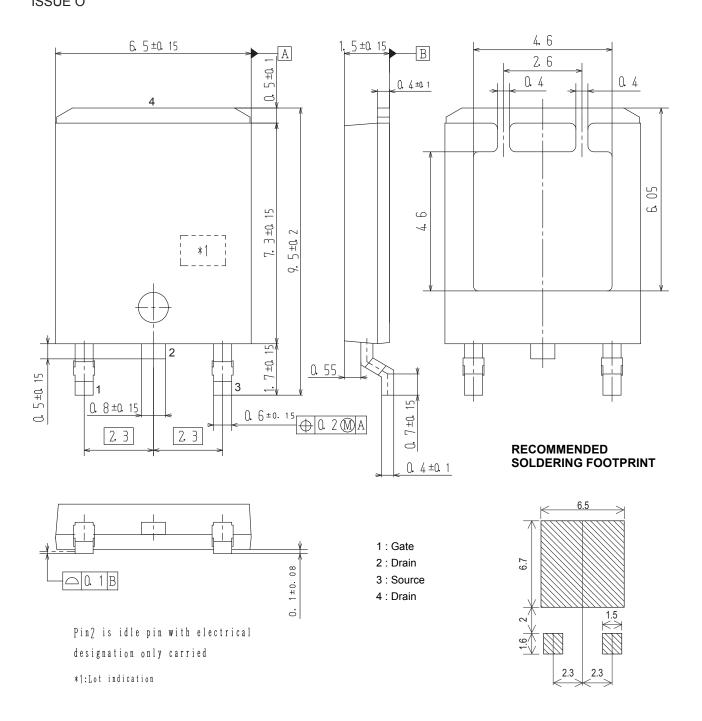


PACKAGE DIMENSIONS

unit: mm

DPAK (Single Gauge) / ATPAK

CASE 369AM ISSUE O



ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
NVATS68301PZT4G	ATP301	DPAK(Single Gauge) / ATPAK (Pb-Free / Halogen Free)	3,000 / Tape & Reel

[†] For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub link/Collateral/BRD8011-D.PDF

Note on usage: Since the NVATS68301PZ is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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