

# Silicon Carbide (SiC) MOSFET - 56 mohm, 650 V, M2, D2PAK-7L NVBG075N065SC1

## Features

- Typ.  $R_{DS(on)} = 56\text{ m}\Omega @ V_{GS} = 18\text{ V}$   
Typ.  $R_{DS(on)} = 75\text{ m}\Omega @ V_{GS} = 15\text{ V}$
- Ultra Low Gate Charge ( $Q_{G(tot)} = 59\text{ nC}$ )
- Low Output Capacitance ( $C_{oss} = 109\text{ pF}$ )
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- RoHS Compliant

## Typical Applications

- Automotive On Board Charger
- Automotive DC/DC Converter for EV/HEV

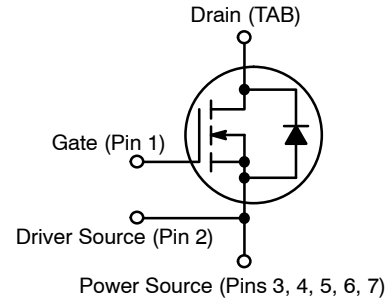
## MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage		$V_{DSS}$	650	V	
Gate-to-Source Voltage		$V_{GS}$	-8/+22	V	
Recommended Operation Values of Gate - Source Voltage		$T_C < 175^\circ\text{C}$ $V_{GSop}$	-5/+18	V	
Continuous Drain Current (Note 2)	Steady State	$T_C = 25^\circ\text{C}$	$I_D$	37	A
			$P_D$	139	W
Continuous Drain Current (Notes 1, 2)	Steady State	$T_C = 100^\circ\text{C}$	$I_D$	26	A
			$P_D$	69	W
Pulsed Drain Current (Note 3)		$T_C = 25^\circ\text{C}$	$I_{DM}$	101	A
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to +175	$^\circ\text{C}$	
Source Current (Body Diode)		$I_S$	32	A	
Single Pulse Drain-to-Source Avalanche Energy ( $I_L = 12.9\text{ A}_{pk}$ , $L = 1\text{ mH}$ ) (Note 4)		$E_{AS}$	83	mJ	
Maximum Lead Temperature for Soldering, 1/8" from Case for 10 Seconds		$T_L$	260	$^\circ\text{C}$	

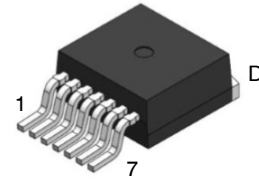
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface mounted on a FR-4 board using 1 in 2 pad of 2 oz copper.
2. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
3. Repetitive rating, limited by max junction temperature.
4.  $E_{AS}$  of 83 mJ is based on starting  $T_J = 25^\circ\text{C}$ ;  $L = 1\text{ mH}$ ,  $I_{AS} = 12.9\text{ A}$ ,  $V_{DD} = 50\text{ V}$ ,  $V_{GS} = 18\text{ V}$ .

$V_{(BR)DSS}$	$R_{DS(ON)}\text{ MAX}$	$I_D\text{ MAX}$
650 V	85 m $\Omega$ @ 18 V	37 A

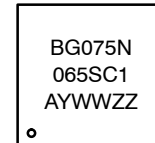


N-CHANNEL MOSFET



D2PAK-7L  
CASE 418BJ

## MARKING DIAGRAM



BG075N065SC1 = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
ZZ = Lot Traceability

## ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

# NVBG075N065SC1

## THERMAL CHARACTERISTICS

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Case (Note 2)	$R_{\theta JC}$	1.08	-	$^{\circ}C/W$
Thermal Resistance Junction-to-Ambient (Notes 1, 2)	$R_{\theta JA}$	-	40	$^{\circ}C/W$

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}C$ unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 V, I_D = 1 mA$	650			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_J$	$I_D = 20 mA$ , refer to $25^{\circ}C$ (Note 5)		0.12		$V/^{\circ}C$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{GS} = 0 V$ $V_{DS} = 650 V$	$T_J = 25^{\circ}C$		10	$\mu A$
			$T_J = 175^{\circ}C$ (Note 5)		1	mA
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS} = +18/-5 V, V_{DS} = 0 V$			250	nA

## ON CHARACTERISTICS

Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 5 mA$	1.8	2.8	4.3	V
Recommended Gate Voltage	$V_{GOP}$		-5		+18	V
Drain-to-Source On Resistance	$R_{DS(on)}$	$V_{GS} = 15 V, I_D = 15 A, T_J = 25^{\circ}C$		75		m $\Omega$
		$V_{GS} = 18 V, I_D = 15 A, T_J = 25^{\circ}C$		56	85	
		$V_{GS} = 18 V, I_D = 15 A, T_J = 175^{\circ}C$ (Note 5)		70		
Forward Transconductance	$g_{FS}$	$V_{DS} = 10 V, I_D = 15 A$ (Note 5)		8		S

## CHARGES, CAPACITANCES & GATE RESISTANCE

Input Capacitance	$C_{ISS}$	$V_{GS} = 0 V, f = 1 MHz,$ $V_{DS} = 325 V$ (Note 5)		1191		pF
Output Capacitance	$C_{OSS}$			109		
Reverse Transfer Capacitance	$C_{RSS}$			11		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = -5/18 V, V_{DS} = 520 V,$ $I_D = 15 A$ (Note 5)		59		nC
Gate-to-Source Charge	$Q_{GS}$			17		
Gate-to-Drain Charge	$Q_{GD}$			20		
Gate-Resistance	$R_G$	$f = 1 MHz$		5.6		$\Omega$

## SWITCHING CHARACTERISTICS

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = -5/18 V, V_{DS} = 400 V,$ $I_D = 15 A, R_G = 2.2 \Omega,$ Inductive Load (Note 5)		9		ns
Rise Time	$t_r$			12		
Turn-Off Delay Time	$t_{d(OFF)}$			20		
Fall Time	$t_f$			8		
Turn-On Switching Loss	$E_{ON}$			35		$\mu J$
Turn-Off Switching Loss	$E_{OFF}$			12		
Total Switching Loss	$E_{TOT}$			47		

## SOURCE-DRAIN DIODE CHARACTERISTICS

Continuous Source-Drain Diode Forward Current	$I_{SD}$	$V_{GS} = -5 V, T_J = 25^{\circ}C$ (Note 5)			32	A
Pulsed Source-Drain Diode Forward Current (Note 3)	$I_{SDM}$	$V_{GS} = -5 V, T_J = 25^{\circ}C$ (Note 5)			101	A
Forward Diode Voltage	$V_{SD}$	$V_{GS} = -5 V, I_{SD} = 15 A, T_J = 25^{\circ}C$		4.4		V

# NVBG075N065SC1

## ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
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### SOURCE-DRAIN DIODE CHARACTERISTICS

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Recovery Time	t <sub>RR</sub>	V <sub>GS</sub> = -5/18 V, I <sub>SD</sub> = 15 A, dI <sub>S</sub> /dt = 1000 A/μs (Note 5)		16		ns
Reverse Recovery Charge	Q <sub>RR</sub>			66		nC
Reverse Recovery Energy	E <sub>REC</sub>			2.6		μJ
Peak Reverse Recovery Current	I <sub>RRM</sub>			8.4		A
Charge time	T <sub>a</sub>			8.6		ns
Discharge time	T <sub>b</sub>			7.1		ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. Define by design, not subject to production test.

TYPICAL CHARACTERISTICS

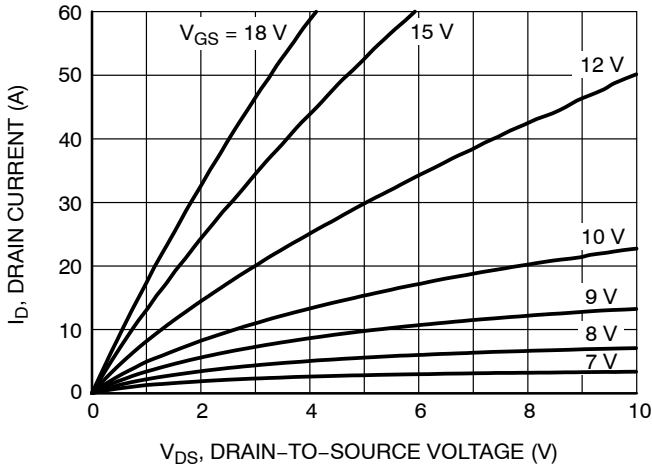


Figure 1. On-Region Characteristics

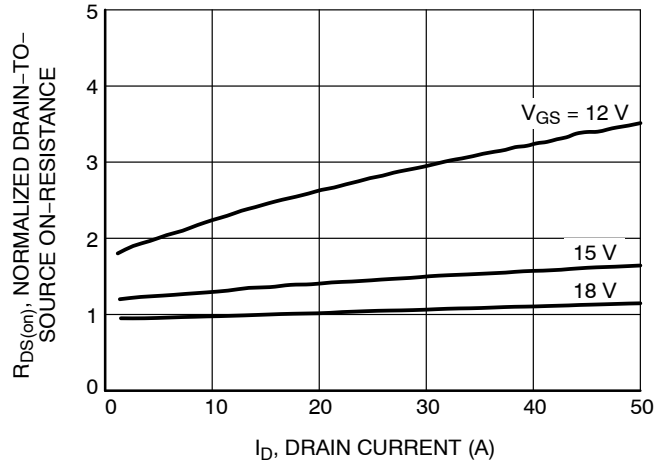


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

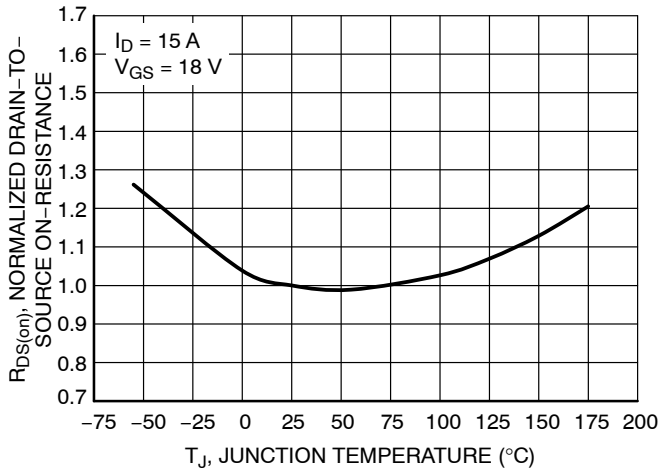


Figure 3. On-Resistance Variation with Temperature

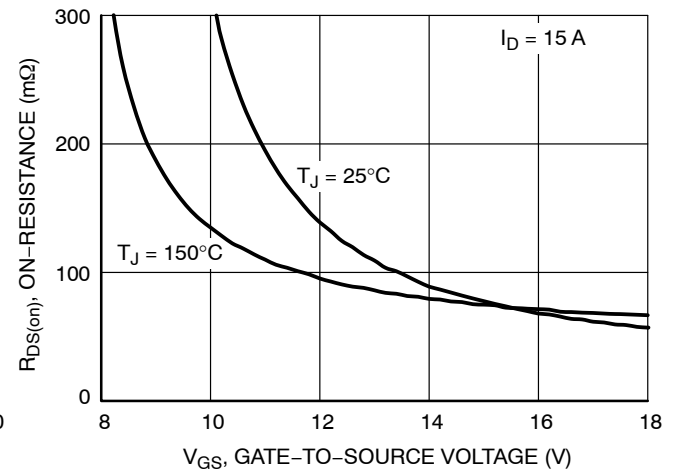


Figure 4. On-Resistance vs. Gate-to-Source Voltage

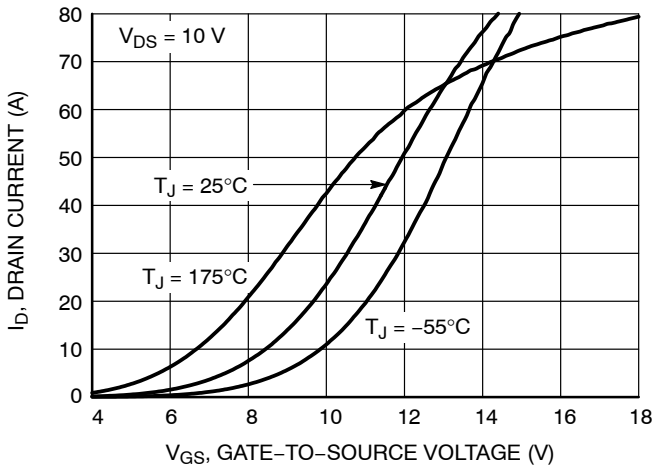


Figure 5. Transfer Characteristics

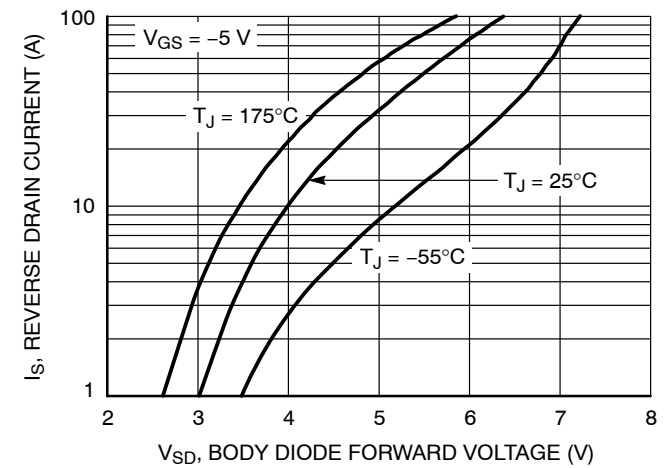
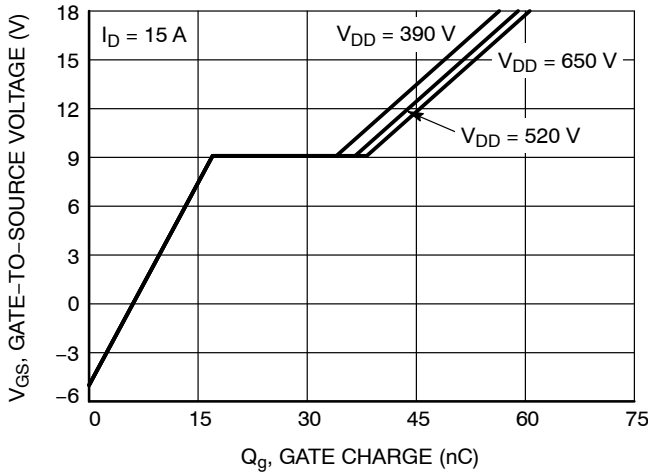


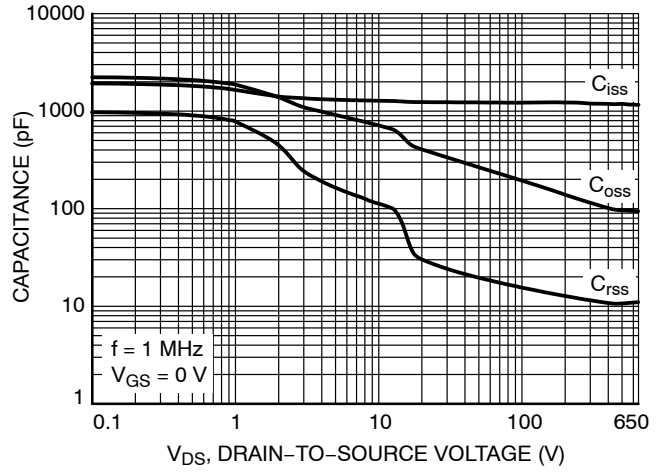
Figure 6. Diode Forward Voltage vs. Current

# NVBG075N065SC1

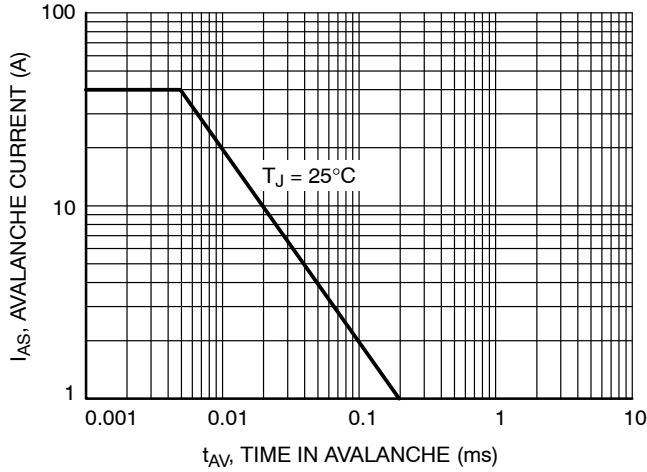
## TYPICAL CHARACTERISTICS



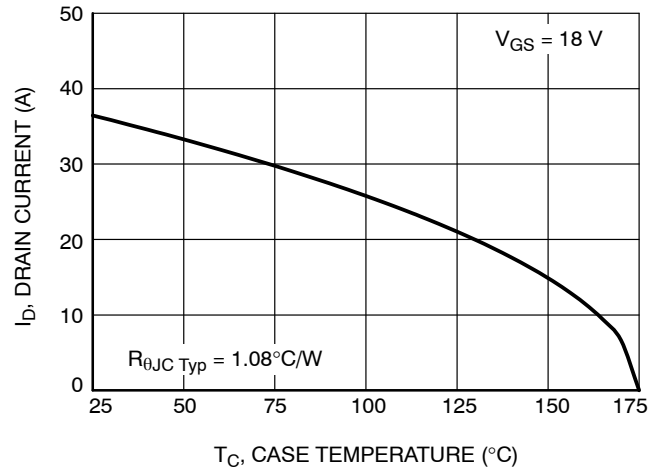
**Figure 7. Gate-to-Source Voltage vs. Total Charge**



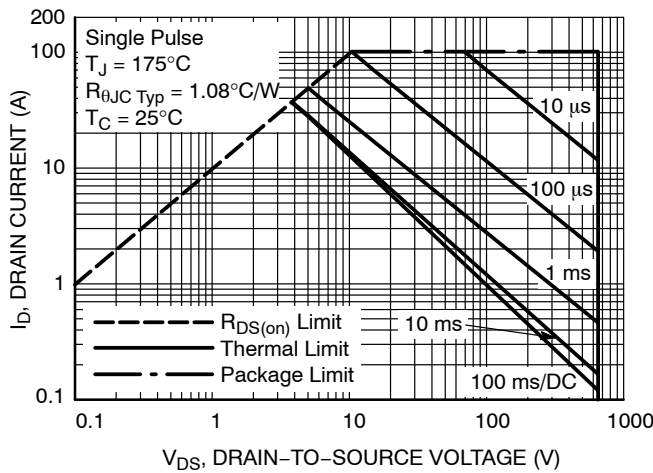
**Figure 8. Capacitance vs. Drain-to-Source Voltage**



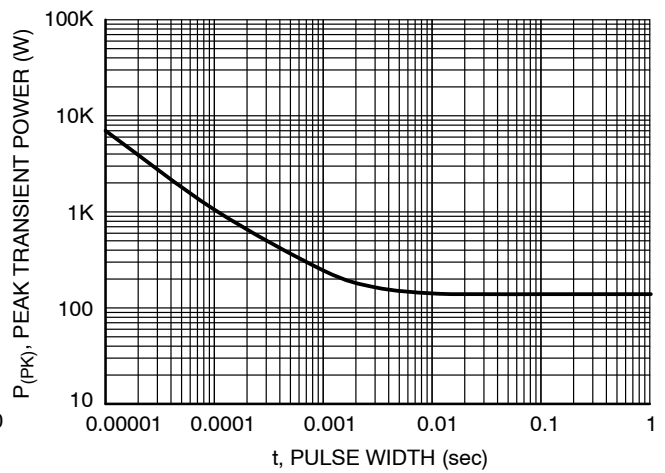
**Figure 9. Unclamped Inductive Switching Capability**



**Figure 10. Maximum Continuous Drain Current vs. Case Temperature**



**Figure 11. Safe Operating Area**



**Figure 12. Single Pulse Maximum Power Dissipation**

# NVBG075N065SC1

## TYPICAL CHARACTERISTICS

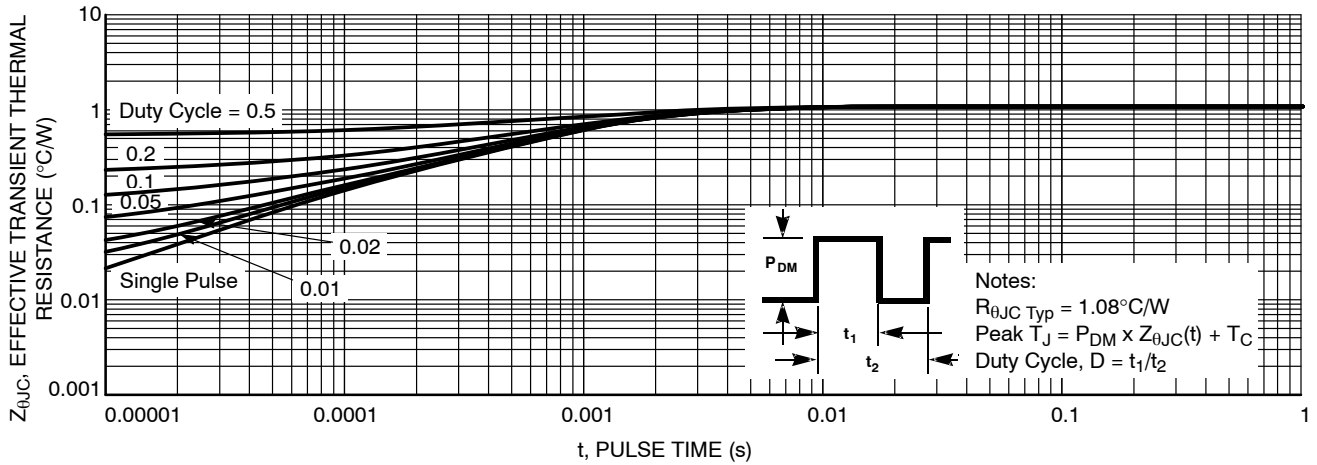


Figure 13. Junction-to-Case Transient Thermal Response

### DEVICE ORDERING INFORMATION

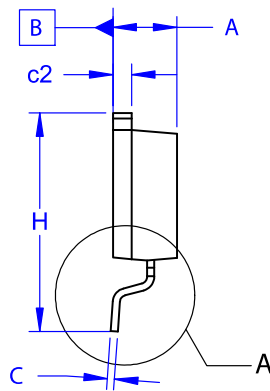
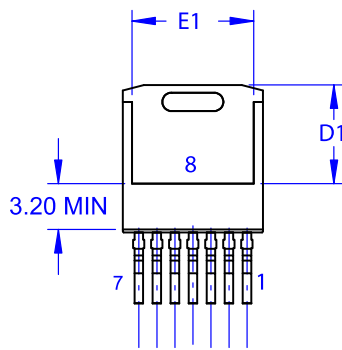
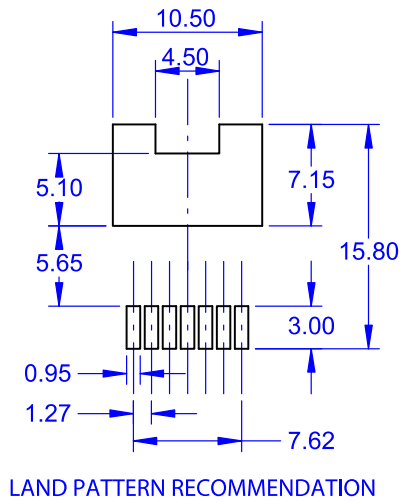
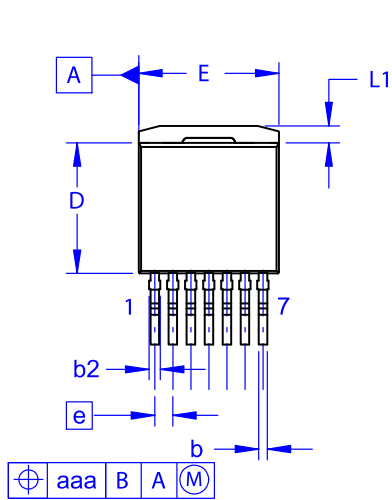
Device	Package	Shipping <sup>†</sup>
NVBG075N065SC1	D2PAK-7L	800 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NVBG075N065SC1

## PACKAGE DIMENSIONS

**D<sup>2</sup>PAK7 (TO-263-7L HV)**  
CASE 418BJ  
ISSUE B



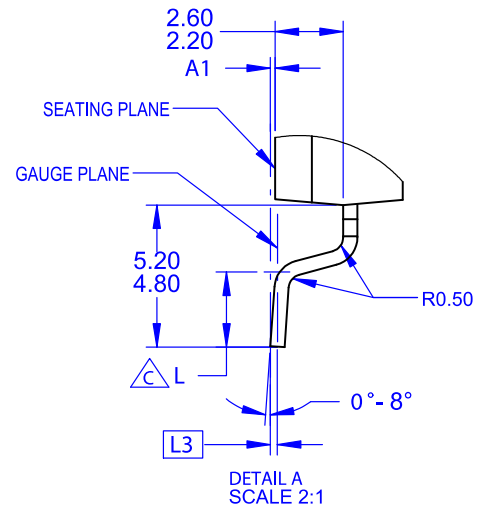
**NOTES:**

- A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.

$\triangle$  OUT OF JEDEC STANDARD VALUE.  
D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.

- E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.30	4.50	4.70
A1	0.00	0.10	0.20
b2	0.60	0.70	0.80
b	0.51	0.60	0.70
c	0.40	0.50	0.60
c2	1.20	1.30	1.40
D	9.00	9.20	9.40
D1	6.15	6.80	7.15
E	9.70	9.90	10.20
E1	7.15	7.65	8.15
e	~	1.27	~
H	15.10	15.40	15.70
L	2.44	2.64	2.84
L1	1.00	1.20	1.40
L3	~	0.25	~
aaa	~	~	0.25



# NVBG075N065SC1

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