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MOSFET - SiC Power, Single N-Channel, TO247-4L 1200 V, 22 mΩ, 68 A

NVH4L022N120M3S

Features

- Typ. $R_{DS(on)} = 22 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 151 \text{ nC})$
- High Speed Switching with Low Capacitance (Coss = 244 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- These Devices are RoHS Compliant

Typical Applications

- Automotive On Board Charger
- Automotive DC/DC Converter for EV/HEV
- Automotive Traction Inverter

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	1200	V
Gate-to-Source Voltage			V_{GS}	-10/+22	٧
	Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-3/+18	٧
Continuous Drain Current (Note 1)	Steady T _C = 25°C State		I _D	68	Α
Power Dissipation (Note 1)			P _D	352	W
Continuous Drain Current (Note 1)	Steady State T _C = 100°C		I _D	48	Α
Power Dissipation (Note 1)			P _D	176	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	246	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode) T _C = 25°C V _{GS} = -3 V			Is	72	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 23.1 A, L = 1 mH) (Note 3)			E _{AS}	267	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			TL	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

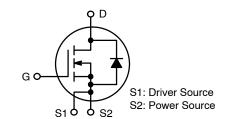
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. E_{AS} of 267 mJ is based on starting $T_J = 25^{\circ}\text{C}$; L = 1 mH, $I_{AS} = 23.1$ A, $V_{DD} = 100$ V, $V_{GS} = 18$ V.



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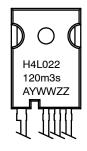
V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX	
1200 V	30 mΩ @ 18 V	68 A	



N-CHANNEL MOSFET



MARKING DIAGRAM



H4L022120M3S = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NVH4L022N120M3S	TO247-4L	30 Units / Tube

THERMAL CHARACTERISTICS

Parameter		Max	Unit
Junction-to-Case - Steady State (Note 1)		0.43	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	40	

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit	
OFF-STATE CHARACTERISTICS								
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA		1200	_	_	V	
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 1 mA, reference	I _D = 1 mA, referenced to 25°C		0.3	-	V/°C	
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 1200 V	T _J = 25°C	-	-	100	μΑ	
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +22/-10 \text{ V}, \text{ V}$	V _{DS} = 0 V	-	-	±1	μΑ	
ON-STATE CHARACTERISTICS (Note 2)	•					•		
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D =$	20 mA	2.04	2.72	4.4	V	
Recommended Gate Voltage	V_{GOP}			-3	_	+18	V	
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 18 V, I _D = 40 A	A, T _J = 25°C	-	22	30	mΩ	
		V _{GS} = 18 V, I _D = 40 A	, T _J = 175°C	_	47	_		
Forward Transconductance	9FS	V _{DS} = 10 V, I _D	= 40 A	-	34	_	S	
CHARGES, CAPACITANCES & GATE RESISTANCE								
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 800 V		_	3175	_	pF	
Output Capacitance	C _{OSS}				146	-		
Reverse Transfer Capacitance	C _{RSS}			_	12	-		
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$ $I_{D} = 40 \text{ A}$		_	151	-	nC	
Threshold Gate Charge	Q _{G(TH)}			-	20	-		
Gate-to-Source Charge	Q _{GS}			-	34	_		
Gate-to-Drain Charge	Q_{GD}			_	40	-		
Gate-Resistance	R_{G}	f = 1 MHz		-	1.5	_	Ω	
SWITCHING CHARACTERISTICS	•							
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -3/18$	3 V,	-	18	-	ns	
Rise Time	t _r	$V_{DS} = 800$ $I_{D} = 40 A$	V, ,	-	24	_		
Turn-Off Delay Time	t _{d(OFF)}	$R_G = 4.5 \Omega$	2	-	48	_		
Fall Time	t _f	Inductive Load (Note 4)		-	13	_		
Turn-On Switching Loss	E _{ON}			-	490	_	μJ	
Turn-Off Switching Loss	E _{OFF}			-	221	_		
Total Switching Loss	E _{tot}			-	771	_		
SOURCE-DRAIN DIODE CHARACTERISTICS								
Continuous Source-Drain Diode Forward Current	I _{SD}	$V_{GS} = -3 \text{ V}, T_C$	= 25°C	-	-	72	Α	
Pulsed Source-Drain Diode Forward Current (Note 2)	I _{SDM}			-	-	246		
Forward Diode Voltage	V _{SD}	V _{GS} = -3 V, I _{SD} = 40 A	A, T _J = 25°C	-	4.5	_	V	
	1							

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit		
SOURCE-DRAIN DIODE CHARACTERI	SOURCE-DRAIN DIODE CHARACTERISTICS							
Reverse Recovery Time	t _{RR}	$V_{GS} = -3/18 \text{ V}, I_{SD} = 40 \text{ A},$ $dI_{S}/dt = 1000 \text{ A}/\mu\text{s}, V_{DS} = 800 \text{ V}$	_	22	_	ns		
Reverse Recovery Charge	Q _{RR}	αι _S /ατ = 1000 A/μs, ν _{DS} = 800 ν	_	138	_	nC		
Reverse Recovery Energy	E _{REC}		_	5	_	μJ		
Peak Reverse Recovery Current	I _{RRM}		_	13	_	Α		
Charge time	t _A		_	13	_	ns		
Discharge time	t _B]	_	9	_	ns		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. E_{ON}/E_{OFF} result is with body diode

TYPICAL CHARACTERISTICS

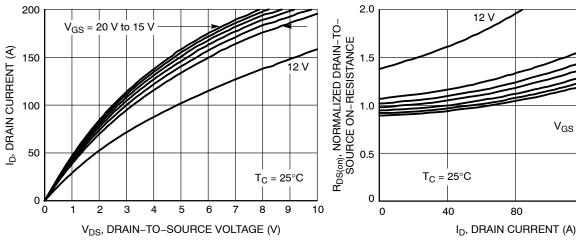


Figure 1. On-Region Characteristics

Figure 2. Normalized On–Resistance vs. Drain Current and Gate Voltage

 $V_{GS} = 20 \text{ V to } 15 \text{ V}$

120

160

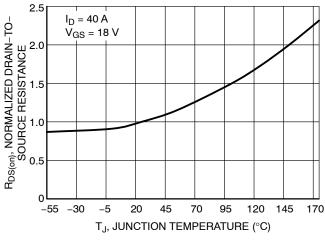


Figure 3. On–Resistance Variation with Temperature

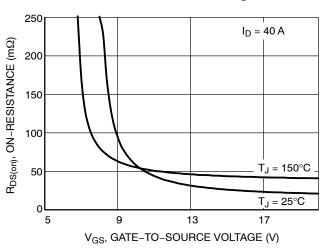


Figure 4. On-Resistance vs. Gate-to-Source Voltage

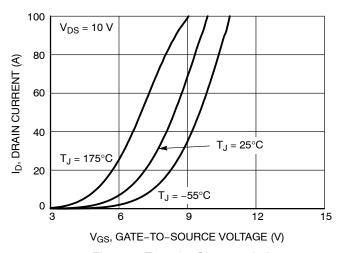


Figure 5. Transfer Characteristics

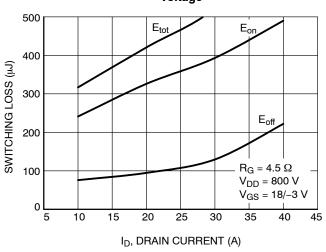
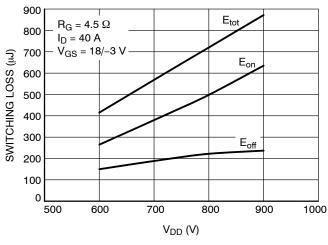


Figure 6. Switching Loss vs. Drain Current

TYPICAL CHARACTERISTICS

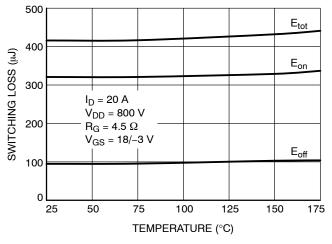
700



 $I_{D} = 20 \text{ A}$ $\mathsf{E}_{\mathsf{tot}}$ $V_{DD} = 800 \text{ V}$ 600 $V_{GS} = 18/-3 \text{ V}$ SWITCHING LOSS (µJ) 500 E_{on} 400 300 200 $\mathsf{E}_{\mathsf{off}}$ 100 0 2 6 10 R_{G} , GATE RESISTANCE (Ω)

Figure 7. Switching Loss vs. Drain Voltage

Figure 8. Switching Loss vs. Gate Resistance



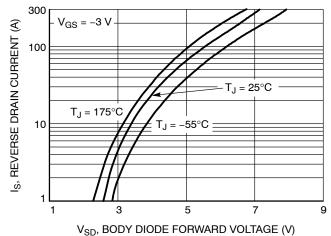


Figure 9. Switching Loss vs. Temperature

Figure 10. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS

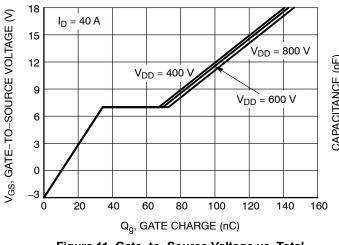


Figure 11. Gate-to-Source Voltage vs. Total Charge

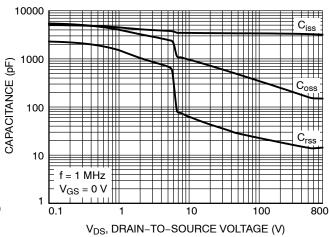


Figure 12. Capacitance vs. Drain-to-Source Voltage

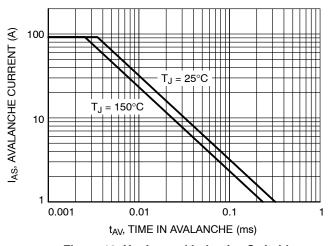


Figure 13. Unclamped Inductive Switching Capability

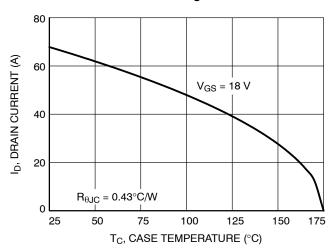


Figure 14. Maximum Continuous Drain Current vs. Case Temperature

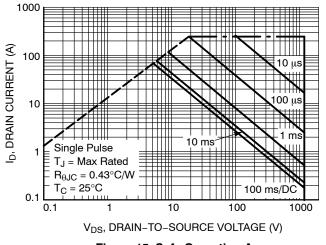


Figure 15. Safe Operating Area

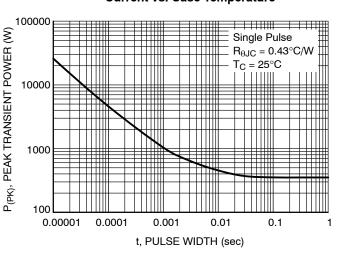


Figure 16. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS

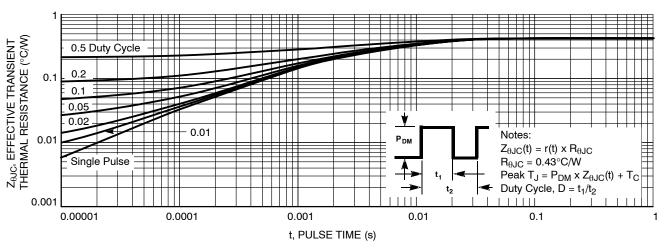
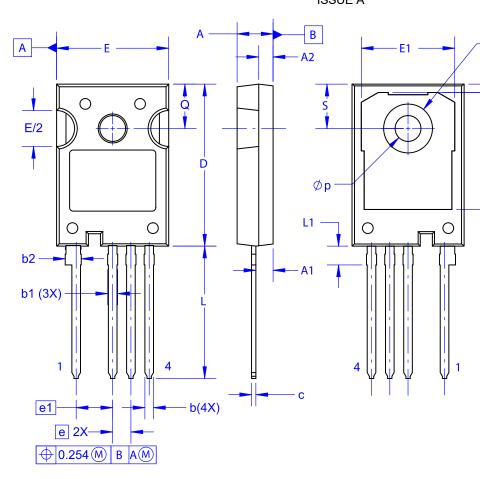


Figure 17. Junction-to-Case Transient Thermal Response

PACKAGE DIMENSIONS

TO-247-4LD CASE 340CJ ISSUE A



NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
 B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
 FLASH, AND TIE BAR EXTRUSIONS.
 C. ALL DIMENSIONS ARE IN MILLIMETERS.
 D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MIL	LIMETER	S		
DIM	MIN	NOM	MAX		
Α	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	1.07	1.20	1.33		
b1	1.20	1.40	1.60		
b2	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.25	16.50		
D2	0.97	1.17	1.37		
е	2.54 BSC				
e1	5.08 BSC				
Е	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E/2	4.80	5.00	5.20		
L	18.22	18.42	18.62		
L1	2.42	2.62	2.82		
р	3.40	3.60	3.80		
p1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97	6.17	6.37		

Ø**p1**

D1

- D2

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