

# MOSFET - SiC Power, Single N-Channel, TO247-3L 650 V, 19 mΩ, 99 A

### **NVHL025N065SC1**

#### **Features**

- Typ.  $R_{DS(on)} = 19 \text{ m}\Omega$  @  $V_{GS} = 18 \text{ V}$ Typ.  $R_{DS(on)} = 25 \text{ m}\Omega$  @  $V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge (Q<sub>G(tot)</sub> = 164 nC)
- Low Capacitance (C<sub>oss</sub> = 278 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Pb-Free and is RoHS Compliant

#### **Typical Applications**

- Automotive On Board Charger
- Automotive DC/DC Converter for EV/HEV

#### **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage		$V_{DSS}$	650	V	
Gate-to-Source Voltage			V <sub>GS</sub> -8/+22		V
Recommended Operation Values of Gate-to-Source Voltage		T <sub>C</sub> < 175°C	$V_{GSop}$	-5/+18	V
Continuous Drain Current (Note 1)	Steady State T <sub>C</sub> = 25°C		I <sub>D</sub>	99	Α
Power Dissipation (Note 1)			P <sub>D</sub>	348	W
Continuous Drain Current (Note 1)	Steady State	T <sub>C</sub> = 100°C	I <sub>D</sub>	70	Α
Power Dissipation (Note 1)			P <sub>D</sub>	174	W
Pulsed Drain Current (Note 2)	T <sub>C</sub> = 25°C		I <sub>DM</sub>	323	Α
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	
Source Current (Body Diode)			I <sub>S</sub>	75	Α
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 11.2 A, L = 1 mH) (Note 3)		E <sub>AS</sub>	62	mJ	
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)		TL	260	°C	

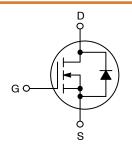
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

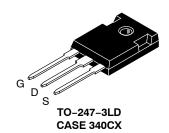
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- 2. Repetitive rating, limited by max junction temperature.
- 3. EAS of 62 mJ is based on starting  $T_J = 25^{\circ}C$ ; L = 1 mH,  $I_{AS} = 11.2$  A,  $V_{DD} = 50$  V,  $V_{GS} = 18$  V.

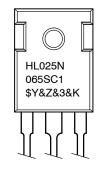
V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
650 V	28.5 mΩ @ 18 V	99 A



**N-CHANNEL MOSFET** 



#### **MARKING DIAGRAM**



H4L025065SC1 = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

#### **ORDERING INFORMATION**

Device	Package	Shipping
NVHL025N065SC1	TO247-3L	30 Units / Tube

#### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ heta JC}$	0.43	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{ heta JA}$	40	

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA		650	_	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	I <sub>D</sub> = 20 mA, referenced to 25°C		-	0.15	-	V/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, \qquad T_{J} = 25^{\circ}\text{C}$		-	_	10	μΑ
		V <sub>DS</sub> = 650 V	-	-	1	mA	
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = +18/-5 \text{ V}, \text{ V}$	V <sub>DS</sub> = 0 V	-	_	250	nA
ON CHARACTERISTICS (Note 2)	•					•	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D =$	15.5 mA	1.8	2.8	4.3	V
Recommended Gate Voltage	V <sub>GOP</sub>			-5	-	+18	V
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 15 V, I <sub>D</sub> = 45 A	A, T <sub>J</sub> = 25°C	-	25	_	mΩ
		V <sub>GS</sub> = 18 V, I <sub>D</sub> = 45 A	A, T <sub>J</sub> = 25°C	-	19	28.5	
		V <sub>GS</sub> = 18 V, I <sub>D</sub> = 45 A, T <sub>J</sub> = 175°C		-	24	_	
Forward Transconductance	9FS	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 45 A		-	27	_	S
CHARGES, CAPACITANCES & GATE RES	SISTANCE					•	
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1 MHz, V <sub>DS</sub> = 325 V		-	3480	_	pF
Output Capacitance	C <sub>OSS</sub>			-	278	_	
Reverse Transfer Capacitance	C <sub>RSS</sub>			-	25	-	
Total Gate Charge	Q <sub>G(TOT)</sub>	$V_{GS} = -5/18 \text{ V}, V_{DS} = 520 \text{ V},$ $I_{D} = 45 \text{ A}$		-	164	-	nC
Gate-to-Source Charge	$Q_{GS}$			-	48	-	
Gate-to-Drain Charge	$Q_{GD}$			-	48	-	
Gate-Resistance	$R_{G}$	f = 1 MHz		-	1.5	_	Ω
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS} = -5/18 \text{ V},$ $V_{DS} = 400 \text{ V},$ $I_{D} = 45 \text{ A},$ $R_{G} = 2.2 \Omega$ inductive load		-	18	_	ns
Rise Time	t <sub>r</sub>			-	51	_	
Turn-Off Delay Time	t <sub>d(OFF)</sub>			-	34	-	
Fall Time	t <sub>f</sub>			-	9	-	
Turn-On Switching Loss	E <sub>ON</sub>			-	560	-	μJ
Turn-Off Switching Loss	E <sub>OFF</sub>			-	112	-	
Total Switching Loss	E <sub>tot</sub>			-	672	_	
DRAIN-SOURCE DIODE CHARACTERIST	ics						
Continuous Drain-Source Diode Forward Current	I <sub>SD</sub>	V <sub>GS</sub> = -5 V, T <sub>J</sub>	= 25°C	-	-	75	Α
Pulsed Drain-Source Diode Forward Current (Note 2)	I <sub>SDM</sub>			-	-	323	
Forward Diode Voltage	$V_{SD}$	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 45 A, T <sub>J</sub> = 25°C		-	4.7	-	V

#### **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit		
DRAIN-SOURCE DIODE CHARACTERISTICS								
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = -5/18 \text{ V, } I_{SD} = 45 \text{ A,}$ $dI_{S}/dt = 1000 \text{ A/}\mu\text{s}$	-	25	-	ns		
Reverse Recovery Charge	Q <sub>RR</sub>		-	165	-	nC		
Reverse Recovery Energy	E <sub>REC</sub>		_	18.8	_	μJ		
Peak Reverse Recovery Current	I <sub>RRM</sub>		-	13	-	Α		
Charge time	Ta		-	15	-	ns		
Discharge time	Tb	]	-	10.3	_	ns		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **TYPICAL CHARACTERISTICS**

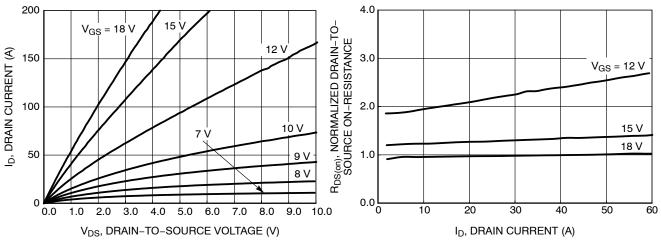


Figure 1. On-Region Characteristics

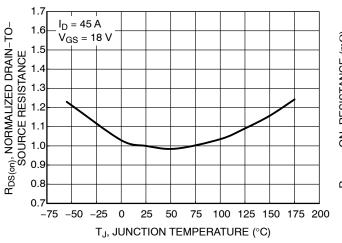


Figure 3. On–Resistance Variation with Temperature

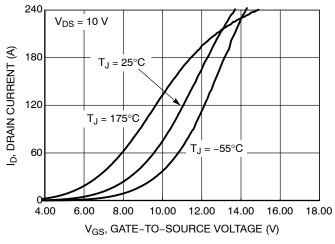


Figure 5. Transfer Characteristics

Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

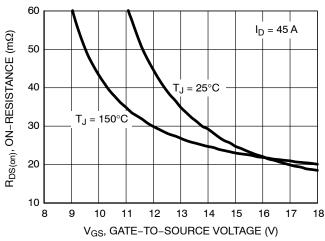


Figure 4. On-Resistance vs. Gate-to-Source Voltage

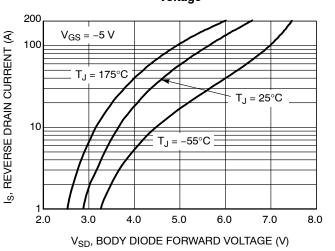


Figure 6. Diode Forward Voltage vs. Current

#### **TYPICAL CHARACTERISTICS**

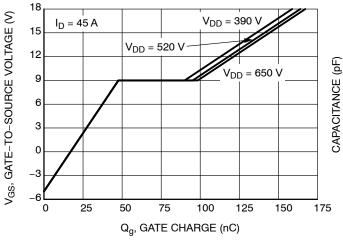


Figure 7. Gate-to-Source Voltage vs. Total Charge

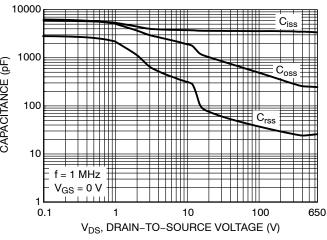


Figure 8. Capacitance vs. Drain-to-Source Voltage

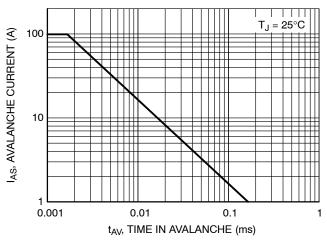


Figure 9. Unclamped Inductive Switching Capability

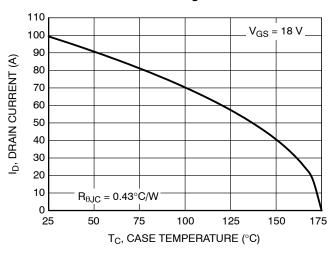


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

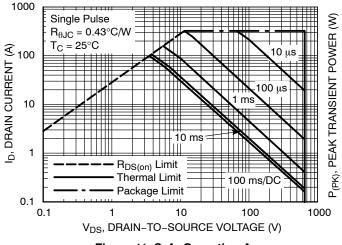


Figure 11. Safe Operating Area

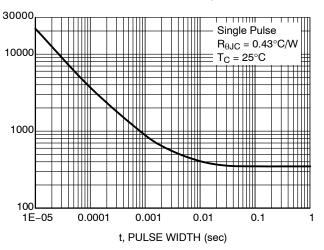


Figure 12. Single Pulse Maximum Power Dissipation

#### **TYPICAL CHARACTERISTICS**

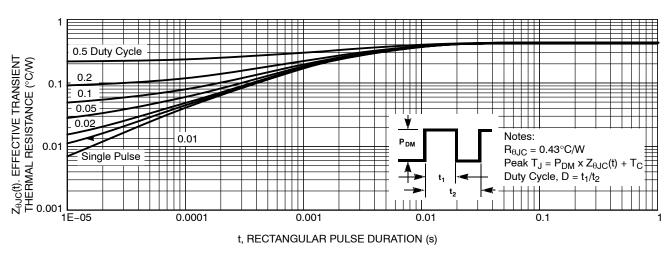


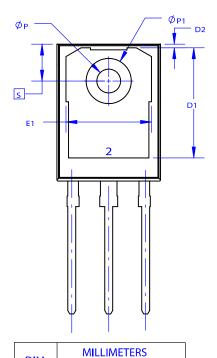
Figure 13. Junction-to-Case Thermal Response

#### PACKAGE DIMENSIONS

В

TO-247-3LD
CASE 340CX
ISSUE A

**DATE 06 JUL 2020** 



DIM MAXMIN MOM Α 4.58 4.70 4.82 **A1** 2.20 2.40 2.60 A2 1.40 1.50 1.60 20.32 20.82 D 20.57 Ε 15.37 15.62 15.87 E2 4.96 5.08 5.20 5.56 е 19.75 20.00 20.25 L L1 3.69 3.81 3.93 ØΡ 3.51 3.58 3.65 Q 5.34 5.46 5.58 S 5.34 5.46 5.58 1.17 1.26 1.35 h b2 1.53 1.65 1.77 b4 2.42 2.54 2.66 0.51 0.61 0.71 С D1 13.08 D2 0.51 0.93 1.35 12.81 E1 ØP1 6.60 6.80 7.00

#### NOTES: UNLESS OTHERWISE SPECIFIED.

L1

(2X) b2

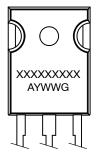
(2X) e

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.

 $\oplus$  0.25 (M)

E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

# GENERIC MARKING DIAGRAM\*



XXXXX = Specific Device Code A = Assembly Location

Y = Year
 WW = Work Week
 G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present. Some products may not follow the Generic Marking.

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C3M0045065K E3M0120090J C3M0065090J-TR C3M0120100J C3M0075120J DMWS120H100SM4 DMWSH120H28SM4
DMWSH120H90SM4 DMWSH120H90SM4Q DMWSH120H28SM4Q DMWSH120H90SCT7Q DMWSH120H28SM3
DMWSH120H43SM3 DMWSH120H90SM3 DMWSH120H28SM3Q DMWSH120H90SM3Q DIF120SIC053-AQ DIW120SIC059-AQ
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G3R20MT12K G3R20MT12N G3R20MT17K G3R20MT17N G3R30MT12J-TR G3R30MT12K G3R350MT12D G3R40MT12D
G3R40MT12J