# MOSFET - Power, N-Channel, SUPERFET® III, FRFET®

# 650 V, 65 A, 40 m $\Omega$

#### **Description**

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

#### **Features**

- $700 \text{ V} @ \text{T}_{\text{I}} = 150^{\circ}\text{C}$
- Typ.  $R_{DS(on)} = 33.8 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 153 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 1333 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable

### **Applications**

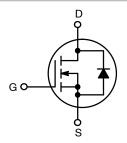
- Automotive On Board Charger HEV-EV
- Automotive DC/DC converter for HEV-EV



#### ON Semiconductor®

#### www.onsemi.com

V <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
650 V	40 mΩ @ 10 V	65 A



**POWER MOSFET** 



TO-247 LONG LEADS CASE 340CX

#### **MARKING DIAGRAM**



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week)

&K = Lot

NVHL040N65S3F = Specific Device Code

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

## **ABSOLUTE MAXIMUM RATINGS** ( $T_C = 25^{\circ}C$ , Unless otherwise noted)

Symbol	Parameter	NVHL040N65S3F	Unit	
V <sub>DSS</sub>	Drain to Source Voltage		650	V
V <sub>GSS</sub>	Gate to Source Voltage	- DC	±30	V
		- AC (f > 1 Hz)	±30	
I <sub>D</sub>	Drain Current	– Continuous (T <sub>C</sub> = 25°C)	65	А
		- Continuous (T <sub>C</sub> = 100°C)	45	
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	162.5	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	1009	mJ	
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		4.46	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)		50	
$P_{D}$	Power Dissipation	(T <sub>C</sub> = 25°C)	446	W
		- Derate Above 25°C	3.57	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse–width limited by maximum junction temperature.

2.  $I_{AS} = 9 \text{ A}$ ,  $R_G = 25 \Omega$ , starting  $T_J = 25^{\circ}C$ .

3.  $I_{SD} \le 32.5 \text{ A}$ ,  $di/dt \le 200 \text{ A/µs}$ ,  $V_{DD} \le 400 \text{ V}$ , starting  $T_J = 25^{\circ}C$ .

#### THERMAL CHARACTERISTICS

Symbol	Parameter	NVHL040N65S3F	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.28	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

#### PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NVHL040N65S3F	NVHL040N65S3F	TO-247	Tube	N/A	N/A	30 Units

## **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 25°C	650	-	_	V
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 10 mA, T <sub>J</sub> = 150°C	700	-	_	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, Referenced to 25°C	-	0.64	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V	-	-	10	μΑ
		V <sub>DS</sub> = 520 V, T <sub>C</sub> = 125°C	-	103	_	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	-	_	±100	nA
ON CHARACTE	RISTICS			•		
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 2.1 \text{ mA}$	3.0	-	5.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 32.5 A	-	33.8	40	mΩ
9FS	Forward Transconductance	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 32.5 A	_	40	-	S
OYNAMIC CHAI	RACTERISTICS					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	5875	_	pF
C <sub>oss</sub>	Output Capacitance	1	_	140	-	pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	V <sub>DS</sub> = 0 V to 400 V, V <sub>GS</sub> = 0 V	-	1333	_	pF
C <sub>oss(er.)</sub>	Energy Related Output Capacitance	V <sub>DS</sub> = 0 V to 400 V, V <sub>GS</sub> = 0 V	-	241	_	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V	$V_{DS} = 400 \text{ V}, I_D = 32.5 \text{ A}, V_{GS} = 10 \text{ V}$	-	153	-	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	(Note 4)	-	51	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge		-	61	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	1.9	_	Ω
SWITCHING CH	ARACTERISTICS					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, I_D = 32.5 \text{ A}, V_{GS} = 10 \text{ V}$	-	41	_	ns
t <sub>r</sub>	Turn-On Rise Time	$R_g = 2.2 \Omega$ (Note 4)	-	53	_	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	,	-	96	_	ns
t <sub>f</sub>	Turn-Off Fall Time		-	28	_	ns
SOURCE-DRAII	N DIODE CHARACTERISTICS			•		<u> </u>
I <sub>S</sub>	Maximum Continuous Source to Drain Diode Forward Current			-	65	Α
I <sub>SM</sub>	Maximum Pulsed Source to Drain Diode Forward Current		-	-	162.5	Α
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 32.5 A	-	-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 32.5 A,	-	159	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> /dt = 100 A/μs	_	840	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

#### **TYPICAL CHARACTERISTICS**

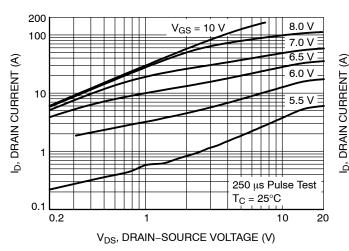


Figure 1. On-Region Characteristics

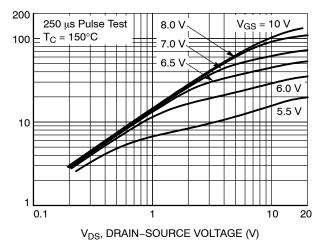


Figure 2. On-Region Characteristics

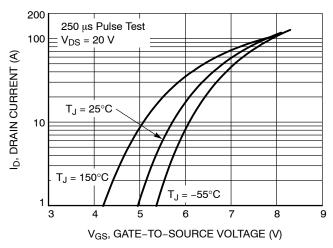


Figure 3. Transfer Characteristics

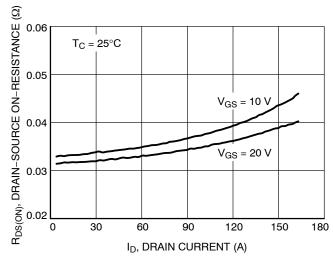


Figure 4. On-Resistance Variation vs. Drain Current and Gate Voltage

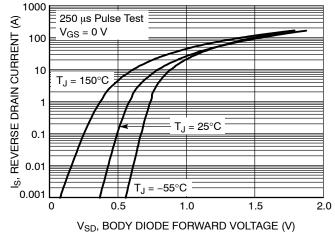


Figure 5. Body Diode Forward Voltage Variation vs. Source Current and Temperature

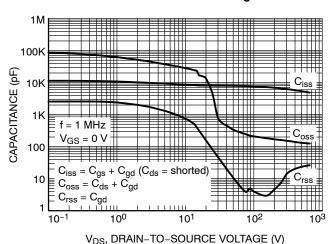
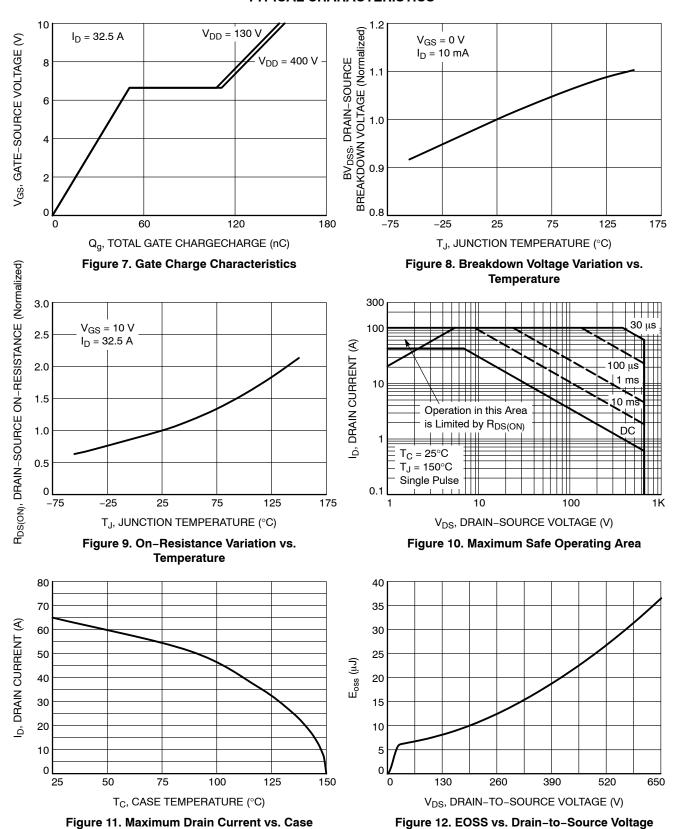


Figure 6. Capacitance Characteristics

#### **TYPICAL CHARACTERISTICS**



**Temperature** 

#### **TYPICAL CHARACTERISTICS**

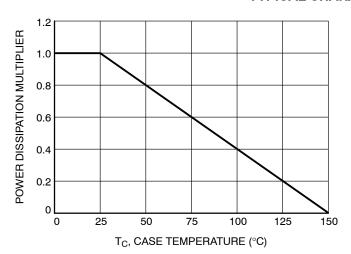


Figure 13. Normalized Power Dissipation vs.

Case Temperature

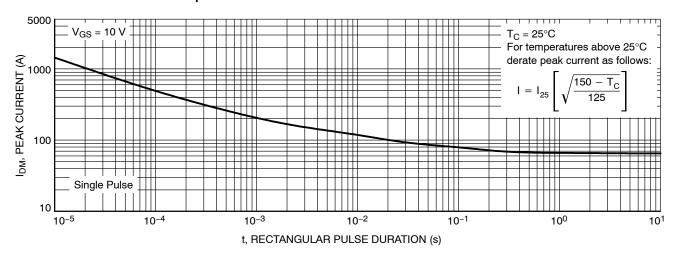


Figure 14. Peak Current Capability

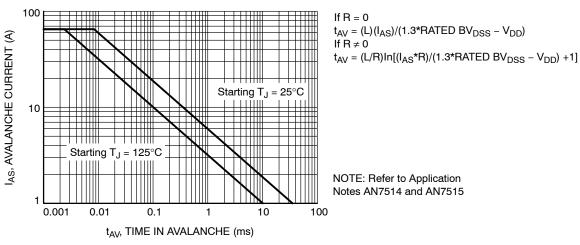


Figure 15. Unclamped Inductive Switching Capability

#### **TYPICAL CHARACTERISTICS**

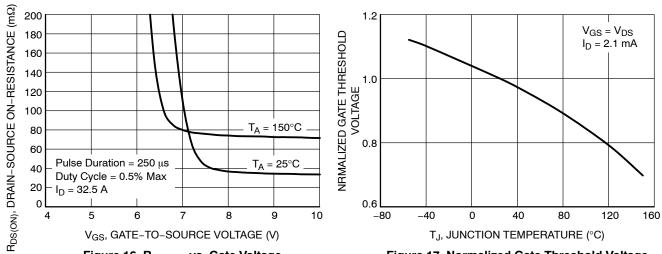


Figure 16. R<sub>DS(ON)</sub> vs. Gate Voltage

Figure 17. Normalized Gate Threshold Voltage vs. Temperature

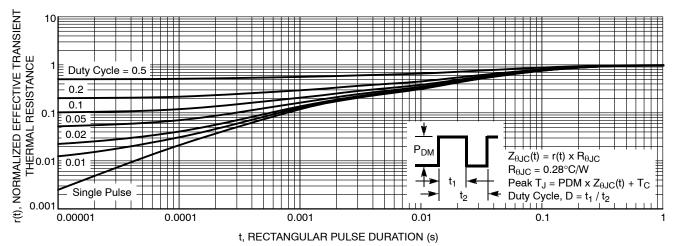


Figure 18. Transient Thermal Response Curve

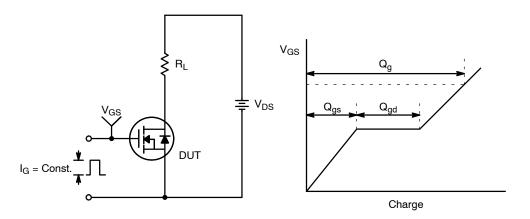


Figure 19. Gate Charge Test Circuit & Waveform

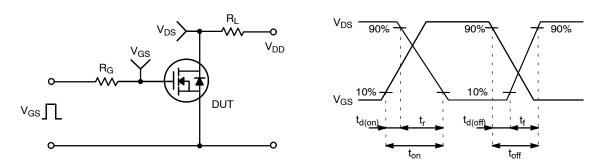


Figure 20. Resistive Switching Test Circuit & Waveforms

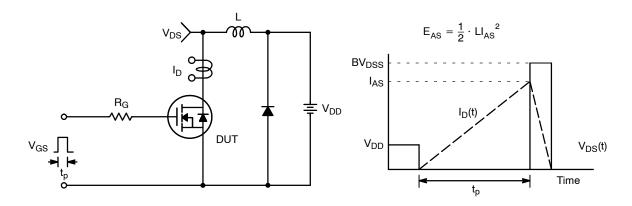


Figure 21. Unclamped Inductive Switching Test Circuit & Waveforms

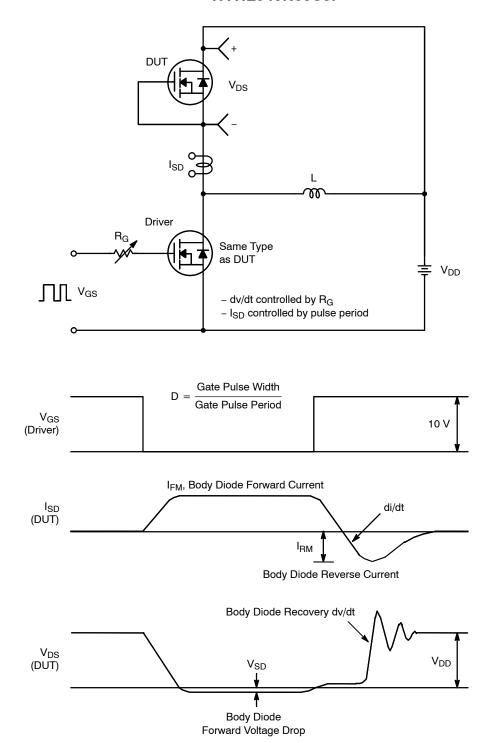
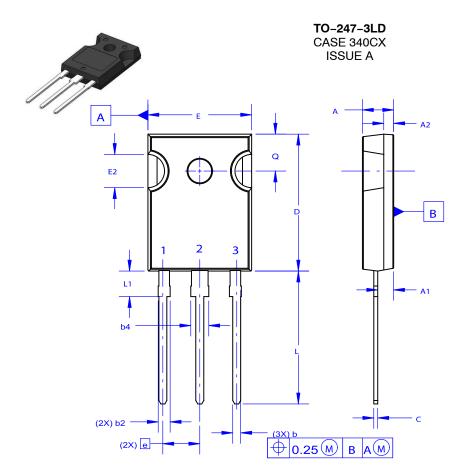
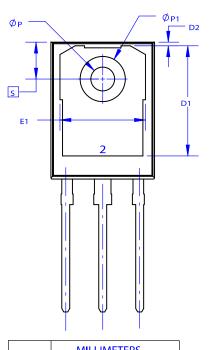


Figure 22. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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**DATE 06 JUL 2020** 

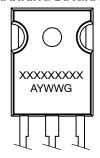


#### NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

  B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

# **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code = Assembly Location

= Year WW = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " =", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.58	4.70	4.82		
<b>A</b> 1	2.20	2.40	2.60		
A2	1.40	1.50	1.60		
D	20.32	20.57	20.82		
Е	15.37	15.62	15.87		
E2	4.96	5.08	5.20		
е	~	5.56	~		
L	19.75	20.00	20.25		
L1	3.69	3.81	3.93		
ØΡ	3.51	3.58	3.65		
Q	5.34	5.46	5.58		
S	5.34	5.46	5.58		
b	1.17	1.26	1.35		
b2	1.53	1.65	1.77		
b4	2.42	2.54	2.66		
С	0.51	0.61	0.71		
D1	13.08	~	~		
D2	0.51	0.93	1.35		
E1	12.81	~	~		
Ø <b>P</b> 1	6.60	6.80	7.00		

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