

MOSFET - Power, N-Channel, SUPERFET[®] III, Automotive, Easy-drive 650 V, 72 mΩ, 44 A NVHL072N65S3

Description

SuperFET III MOSFET is **onsemi**'s brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss provide superior switching performance, and withstand extreme dv/dt rate. Consequently, SuperFET III MOSFET Easy-drive series helps manage EMI issues and allows for easier design implementation.

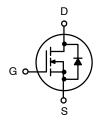
Features

- AEC-Q101 Qualified
- Max Junction Temperature 150°C
- Typ. $R_{DS}(on) = 61 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_G = 82 nC)
- Low Effective Output Capacitance (Typ. C_{OSS}(eff.) = 724 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Typical Applications

- Automotive PHEV-BEV DC-DC Converter
- Automotive Onboard Charger for PHEV-BEV

BV _{DSS}	R _{DS(on)} MAX	I _D MAX
650 V	72 m Ω @ 10 V	44 A

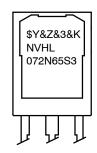


N-Channel MOSFET



TO-247-3LD CASE 340CX

MARKING DIAGRAM



\$Y = onsemi Logo &Z = Assembly Plant Code &3 = Numeric Date Code &K = Lot Code

NVHL072N65S3 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

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ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise specified)

Symbol		Value	Unit	
V _{DSS}	Drain to Source Voltage	to Source Voltage		V
V _{GSS}	Gate to Source Voltage	DC	±30	V
		AC (f > 1 Hz)	±30	V
I _D	Drain Current	Continuous (T _C = 25°C)	44	Α
		Continuous (T _C = 100°C)	28	Α
I _{DM}	Pulsed Drain Current	Pulsed (Note 1)	110	Α
E _{AS}	Single Pulsed Avalanche Energy	Single Pulsed Avalanche Energy (Note 2) Repetitive Avalanche (Note 1)		mJ
E _{AR}	Repetitive Avalanche (Note 1)			mJ
dv/dt	dv/dt MOSFET dv/dt Peak Diode Recovery dv/dt (Note 3)		100	V/ns
			20	V/ns
P _D	Power Dissipation	(T _C = 25°C)	312	W
		Derate Above 25°C	2.5	W/°C
T_{J} , T_{STG}	Operating and Storage Tempera	Operating and Storage Temperature Range		°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse–width limited by maximum junction temperature.

2. I_{AS} = 4.8 A, R_G = 25 Ω, starting T_J = 25°C.

3. I_{SD} < 44 A, di/dt ≤ 200 A/ms, VDD ≤ BVDSS, starting T_J = 25°C.

- 4. Essentially independent of operating temperature typical characteristics.

THERMAL CHARACTERISTICS

Symbol	Symbol Parameter		Unit
R _{θJ C}	Thermal Resistance, Junction to Case, Max	0.37	°C/W
$R_{ heta J A}$	Thermal Resistance, Junction to Ambient, Max	40	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

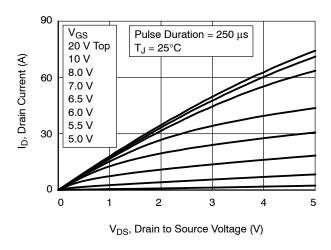
Part Number	Top Marking	Package	Packing Method	Shipping (Qty / Packing)
NVHL072N65S3	NVHL072N65S3	TO-247-3LD	Tube	30 Units / Tube

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACT	ERISTICS					
BV _{DSS}	Drain-to-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA, T _J = 25°C	650	-	-	V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700	-	-	V
ΔBVDSS / ΔTJ	Breakdown Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C	-	0.60	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	_	0.30	1	μΑ
		V _{DS} = 520 V, V _{GS} = 0 V, Tc = 125°C	-	7.30	-	
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±30 V, V _{DS} = 0 V	-	-	±100	nA
N CHARACTE	RISTICS					
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 1.0 \text{ mA}$	2.5	_	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 22 A, T _J = 25°C	-	61	72	mΩ
		V _{GS} = 10 V, I _D = 22 A, T _J = 100°C	-	107	-	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 44 A	-	29.7	-	S
YNAMIC CHAI	RACTERISTICS		•	•		
C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz	-	3300	_	pF
C _{oss}	Output Capacitance		-	72.8	-	pF
C _{rss}	Reverse Transfer Capacitance		-	14.6	-	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	-	724	-	pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	_	104	-	pF
Q _{g(tot)}	Total Gate Charge	V _{DS} = 400 V, V _{GS} = 10 V, I _D = 44 A	_	82.0	_	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	_	23.3	-	nC
Q _{gd}	Gate to Drain "Miller" Charge		_	34.0	-	nC
R _G	Gate Resistance	f = 1 MHz	-	0.685	-	mΩ
WITCHING CH	ARACTERISTICS		•	•		
t _{d(on)}	Turn-On Delay Time	V _{DD} = 400 V, I _D = 44 A, V _{GS} = 10 V,	-	26.3	-	ns
t _r	Turn-On Rise Time	$R_G = 4.7 \Omega$ (Note 4)	_	50	-	ns
t _{d(off)}	Turn-Off Delay Time		_	65.9	-	ns
t _f	Fall Time		_	32	-	ns
	E DIODE CHARACTERISTICS		•	•		
I _S	Maximum Continuous Drain to Source Diode Forward Current		_	_	44	Α
I _{SM}	Maximum Plused Drain to Source Diode Forward Current		_	-	110	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 22 A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 44 A dI _F /dt = 100 A/μs	_	576	-	nS
Q _{rr}	Reverse Recovery Charge	1	_	14.3	_	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

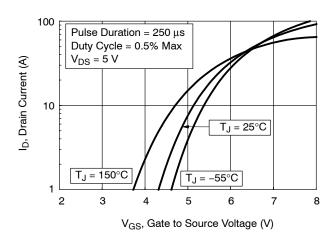
TYPICAL CHARACTERISTICS



40 V_{GS} 20 V Top Pulse Duration = 250 μs $T_J = 150^{\circ}C$ 10 V ID, Drain Current (A) 30 8.0 V 7.0 V 6.5 V 20 6.0 V 5.5 V 10 2 3 5 4 V_{DS}, Drain to Source Voltage (V)

Figure 1. Saturation Characteristics

Figure 2. Saturation Characteristics



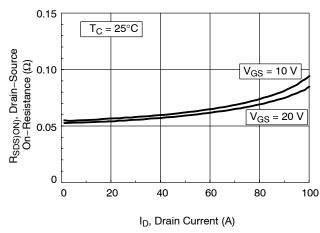
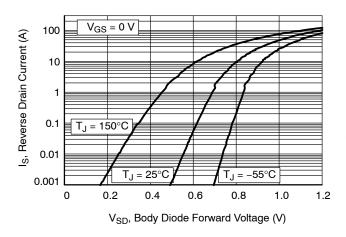


Figure 3. Transfer Characteristic

Figure 4. On-Resistance Variation vs. Drain Current and Gate Voltage



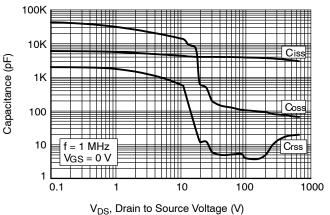


Figure 5. Forward Diode Characteristics

Figure 6. Capacitance vs. Drain to Source Volatage

TYPICAL CHARACTERISTICS (continued)

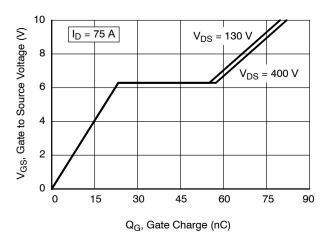


Figure 7. Gate Charge vs. Gate to Source Voltage

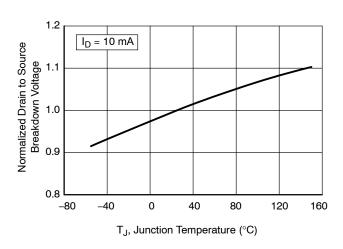


Figure 8. Normalized Drain to Source Breakdown Voltage vs. Junction Temperature

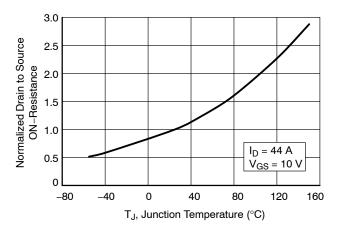


Figure 9. Normalized R_{DSON} vs. Junction Temperature

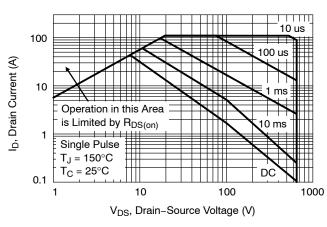


Figure 10. Forward Bias Safe Operating Area

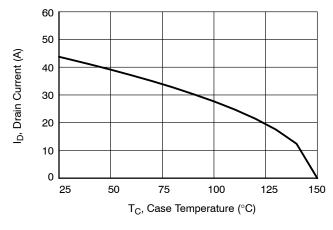


Figure 11. Maximum Continuous Drain Current vs. Case Temperature

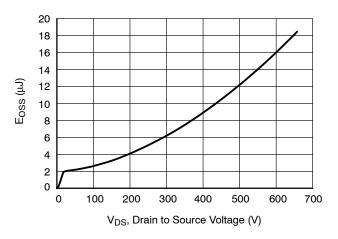
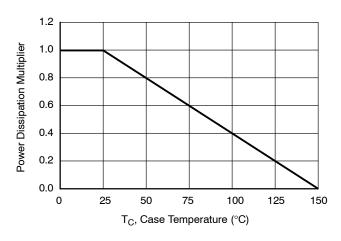


Figure 12. E_{OSS} vs. Drain to Source Voltage

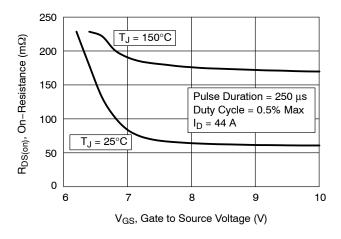
TYPICAL CHARACTERISTICS (continued)



1000 (V) tues 100 (V) 100 (V)

Figure 13. Normalized Power Dissipation vs. Case Temperature

Figure 14. Peak Current Capability



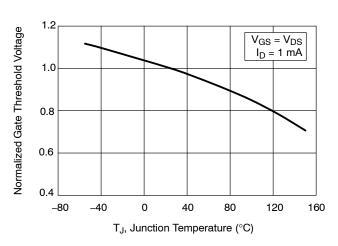


Figure 15. E_{OSS} vs. Drain to Source Voltage

Figure 16. Normalized Gate Threshold Voltage vs. Temperature

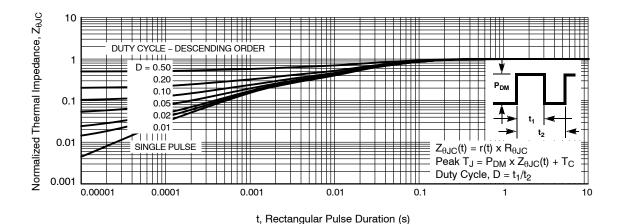
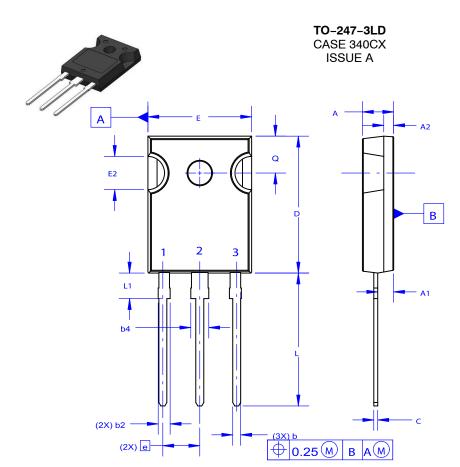


Figure 17. Normalized Maximum Transient Thermal Impedance

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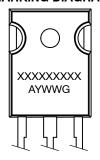
DATE 06 JUL 2020



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

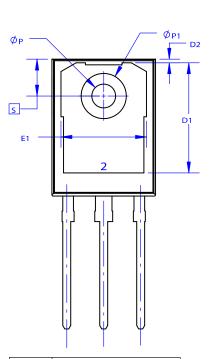
GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present. Some products may not follow the Generic Marking.



DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.58	4.70	4.82		
A 1	2.20	2.40	2.60		
A2	1.40	1.50	1.60		
D	20.32	20.57	20.82		
E	15.37	15.62	15.87		
E2	4.96	5.08	5.20		
е	~	5.56	~		
L	19.75	20.00	20.25		
L1	3.69	3.81	3.93		
ØΡ	3.51	3.58	3.65		
Q	5.34	5.46	5.58		
S	5.34	5.46	5.58		
b	1.17	1.26	1.35		
b2	1.53	1.65	1.77		
b4	2.42	2.54	2.66		
С	0.51	0.61	0.71		
D1	13.08	~	~		
D2	0.51	0.93	1.35		
E1	12.81	~	~		
ØP1	6.60	6.80	7.00		

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