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T-Type NPC Power Module 1200 V 100 A IGBT, 650 V 75 A IGBT

NXH100T120L3Q0S1NG

Description

The NXH100T120L3Q0S1NG is a power module containing a T-type Neutral Point Clamped (NPC) 3-level inverter consisting of 100 A/1200 V half-bridge IGBTs with 40 A/1200 V half-bridge diodes and 75 A/650 V NP IGBTs with 50 A/650 V NP diodes. The module also contains an on-board thermistor.

Features

- T-type NPC Module with 100 A/1200 V and 75 A/650 V IGBTs
- HB IGBT Specifications: $V_{CE(SAT)} = 1.8 \text{ V}$, $E_{SW} = 2.5 \text{ mJ}$
- NP IGBT Specifications: V_{CE(SAT)} = 1.4 V, E_{SW} = 1.25 mJ
- Solder Pins
- Thermistor
- These Device is Pb-Free, Halogen Free and is RoHS Compliant

Typical Applications

- Solar Inverter
- Uninterruptible Power Supplies

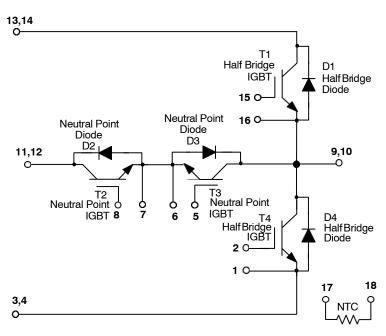
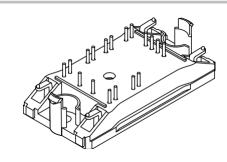


Figure 1. NXH100T120L3Q0S1NG Schematic Diagram



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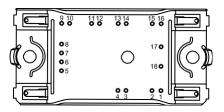
Q0PACK CASE 180AH

MARKING DIAGRAM



NXH100T120L3Q0S1NG = Specific Device Code
YYWW = Year and Work Week Code
A = Assembly Site Code
T = Test Site Code
G = Pb- Free Package

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information on page 19 of this data sheet.

ABSOLUTE MAXIMUM RATINGS (Note 1) $T_i = 25$ °C unless otherwise noted

Rating	Symbol	Value	Unit
HALF BRIDGE IGBT	•		•
Collector-Emitter Voltage	V _{CES}	1200	V
Gate-Emitter Voltage	V_{GE}	±20	V
Continuous Collector Current @ T _h = 80°C (T _J = 175°C)	I _C	100	Α
Pulsed Collector Current (T _J = 175°C)	I _{Cpulse}	300	А
Maximum Power Dissipation @ $T_h = 80^{\circ}C (T_J = 175^{\circ}C)$	P _{tot}	328	W
Minimum Operating Junction Temperature	T_JMIN	-40	°C
Maximum Operating Junction Temperature	T_{JMAX}	175	°C
NEUTRAL POINT IGBT			
Collector-Emitter Voltage	V _{CES}	650	V
Gate-Emitter Voltage	V_{GE}	±20	V
Continuous Collector Current @ T _h = 80°C (T _J = 175°C)	I _C	54	А
Pulsed Collector Current (T _J = 175°C)	I _{Cpulse}	162	А
Maximum Power Dissipation @ T _h = 80°C (T _J = 175°C)	P _{tot}	122	W
Minimum Operating Junction Temperature	T_JMIN	-40	°C
Maximum Operating Junction Temperature	T_{JMAX}	175	°C
HALF BRIDGE DIODE			
Peak Repetitive Reverse Voltage	V_{RRM}	1200	V
Continuous Forward Current @ T _h = 80°C (T _J = 175°C)	l _F	30	А
Repetitive Peak Forward Current ($T_J = 175^{\circ}C$, t_p limited by T_{Jmax})	I _{FRM}	90	A
Maximum Power Dissipation @ $T_h = 80^{\circ}C (T_J = 175^{\circ}C)$	P _{tot}	101	W
Minimum Operating Junction Temperature	T_JMIN	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	175	°C
NEUTRAL POINT DIODE			
Peak Repetitive Reverse Voltage	V_{RRM}	650	V
Continuous Forward Current @ T _h = 80°C.(T _J = 175°C)	I _F	47	A
Repetitive Peak Forward Current (T _J = 175°C, t _p limited by T _{Jmax})	I _{FRM}	141	А
Maximum Power Dissipation @ T _h = 80°C (T _J = 175°C)	P _{tot}	101	W
Minimum Operating Junction Temperature	T _{JMIN}	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	175	°C
THERMAL PROPERTIES			
Storage Temperature Range	T _{stg}	-40 to 150	°C
INSULATION PROPERTIES			
Isolation Test Voltage, t = 1 sec, 60 Hz	V _{is}	3000	V _{RMS}
Creepage Distance		12.7	mm

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Refer to ELECTRICAL CHARACTERISTICS, RECOMMENDED OPERATING RANGES and/or APPLICATION INFORMATION for Safe

RECOMMENDED OPERATING RANGES

Rating	Symbol	Min	Max	Unit
Module Operating Junction Temperature	TJ	-40	T _{jmax} -25	°C

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

Operating parameters.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
HALF BRIDGE IGBT CHARACTERISTIC		Cymbol	14	1,76	Mux	- Onne
Collector–Emitter Cutoff Current	V _{GE} = 0 V, V _{CE} = 1200 V	lana	l _		200	μА
Collector-Emitter Saturation Voltage	$V_{GE} = 15 \text{ V}, V_{CE} = 1200 \text{ V}$ $V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25^{\circ}\text{C}$	I _{CES}		1.8	2.3	V
Collector – Emitter Saturation Voltage	$V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25 \text{ C}$ $V_{GF} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 175 ^{\circ}\text{C}$	V _{CE(sat)}		1.9	2.0	V
Cata Emittar Throphold Voltage	32 0	V	4.6		6.5	V
Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 2 \text{ mA}$	V _{GE(TH)}	4.6	5.3	6.5 800	nA
Gate Leakage Current	V _{GE} = 20 V, V _{CE} = 0 V	I _{GES}	_	- 50	800	
Turn-on Delay Time	$T_J = 25^{\circ}C$, $V_{CE} = 350 \text{ V}$, $I_C = 100 \text{ A}$,	t _{d(on)}		59	_	ns
Rise Time	$V_{GE} = +15/-8 \text{ V},$ $R_{G} = 4.7 \Omega$	t _r	<u> </u>	38	_	-
Turn-off Delay Time	116 - 4.7 32	t _{d(off)}	<u> </u>	229	_	-
Fall Time	4	t _f	_	77	_	
Turn-on Switching Loss per Pulse	4	E _{on}	_	1.2	_	mJ
Turn-off Switching Loss per Pulse		E _{off}	_	2.5	_	
Turn-on Delay Time	$T_J = 125^{\circ}C$ $V_{CE} = 400 \text{ V, } I_C = 100 \text{ A,}$	t _{d(on)}	_	55	_	ns
Rise Time	$V_{GE} = +15/-15 \text{ V},$	t _r	_	38	_	1
Turn-off Delay Time	$R_G = 4.7 \Omega$	t _{d(off)}	=	261	-	1
Fall Time	_	t _f	_	151		
Turn-on Switching Loss per Pulse	_	E _{on}	_	1.8	_	mJ
Turn-off Switching Loss per Pulse		E _{off}	_	3.7	_	
Input Capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 10 kHz	C _{ies}	_	18150	_	pF
Output Capacitance		C _{oes}	_	346	_]
Reverse Transfer Capacitance		C _{res}	_	294	_	
Total Gate Charge	$V_{CE} = 600 \text{ V}, I_{C} = 80 \text{ A}, V_{GE} = 15 \text{ V}$	Q_g	_	817	-	nC
Thermal Resistance - chip-to-heatsink	Thermal grease, Thickness = 100 μ m, λ = 2.87 W/mK	R_{thJH}	=	0.29	_	°C/W
NEUTRAL POINT DIODE CHARACTER	STICS					
Diode Forward Voltage	I _F = 50 A, T _J = 25°C	V _F	_	2.2	2.8	V
	I _F = 50 A, T _J = 175°C		_	1.7	-	1
Reverse Recovery Time	T _J = 25°C,	t _{rr}	_	34	-	ns
Reverse Recovery Charge	$V_{CE} = 350 \text{ V}, I_{C} = 100 \text{ A},$ $V_{GE} = +15/-15 \text{ V},$	Q _{rr}	_	688	-	μC
Peak Reverse Recovery Current	$R_G = 4.7 \Omega$	I _{RRM}	_	35	_	Α
Peak Rate of Fall of Recovery Current	1	di/dt	_	1764	_	A/μs
Reverse Recovery Energy	1	E _{rr}	_	143	_	μJ
Reverse Recovery Time	T _J = 125°C,	t _{rr}	_	100	-	ns
Reverse Recovery Charge	$V_{CE} = 400 \text{ V}, I_{C} = 100 \text{ A},$ $V_{GF} = +15/-15 \text{ V},$	Q _{rr}	_	2740	-	nC
Peak Reverse Recovery Current	$R_G = 4.7 \Omega$	I _{RRM}	=	67	-	Α
Peak Rate of Fall of Recovery Current	1	di/dt	=	872	-	A/μs
Reverse Recovery Energy	1	E _{rr}	=	645	-	μJ
Thermal Resistance - chip-to-heatsink	Thermal grease, Thickness = 100 μ m, λ = 2.87 W/mK	R_{thJH}	_	0.94	_	°C/W
NEUTRAL POINT IGBT CHARACTERIS	TICS					
Collector–Emitter Cutoff Current	V _{GE} = 0 V, V _{CE} = 650 V	I _{CES}	_	_	200	μΑ
Collector-Emitter Saturation Voltage	V _{GE} = 15 V, I _C = 75 A, T _J = 25°C	V _{CE(sat)}	_	1.39	2.2	V
_	V _{GE} = 15 V, I _C = 75 A, T _J = 175°C	()	_	1.58	-	1
Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}, I_{C} = 250 \mu A$	V _{GE(TH)}	3.5	4.0	4.7	V
Gate Leakage Current	V _{GE} = 20 V, V _{CE} = 0 V	I _{GES}	_	_	300	nA
	SL , UL	410	1	1		<u> </u>

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted) (continued)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
NEUTRAL POINT IGBT CHARACTERIS	тісѕ					
Turn-on Delay Time	T _J = 25°C	t _{d(on)}	_	68	_	ns
Rise Time	$V_{CE} = 350 \text{ V, } I_{C} = 100 \text{ A,}$ $V_{GE} = \pm 15 \text{ V, } R_{G} = 10 \Omega$	t _r	_	44	_	1
Turn-off Delay Time	- rae =	t _{d(off)}	_	108	_	1
Fall Time	1	t _f	-	21	_	1
Turn-on Switching Loss per Pulse	1	E _{on}	_	0.95	_	mJ
Turn-off Switching Loss per Pulse] [E _{off}	_	1.25	_	
Turn-on Delay Time	T _J = 125°C	t _{d(on)}	_	73	_	ns
Rise Time	$V_{CE} = 350 \text{ V, } I_{C} = 100 \text{ A,}$ $V_{GE} = \pm 15 \text{ V, } R_{G} = 10 \Omega$	t _r	_	49	_	
Turn-off Delay Time	a ac , a	t _{d(off)}	-	114	_	
Fall Time] [t _f	_	29	_	
Turn-on Switching Loss per Pulse] [E _{on}	_	1.95	=	mJ
Turn-off Switching Loss per Pulse	1 1	E _{off}	-	2.22	=	
Input Capacitance	V _{CE} = 25 V, V _{GE} = 0 V, f = 10 kHz	C _{ies}	_	4877	_	pF
Output Capacitance	1	C _{oes}	-	77	_	-
Reverse Transfer Capacitance	1	C _{res}	_	21	_	1
Total Gate Charge	V _{CE} = 480 V, I _C = 50 A, V _{GE} = 15 V	Qg	-	550	_	nC
Thermal Resistance – chip-to-heatsink	Thermal grease, Thickness < 100 μ m, λ = 2.87 W/mK	R_{thJH}	_	0.78	_	°C/W
HALF BRIDGE DIODE CHARACTERIST	ics		•		•	
Diode Forward Voltage	I _F = 30 A, T _J = 25°C	V _F	-	2.4	3.2	V
	I _F = 30 A, T _J = 175°C		_	1.8	_	1
Reverse Recovery Time	T _J = 25°C	t _{rr}	_	53	_	ns
Reverse Recovery Charge	$V_{CE} = 350 \text{ V, } I_{C} = 100 \text{ A,}$ $V_{GE} = \pm 15 \text{ V, } R_{G} = 10 \Omega$	Q _{rr}	_	1862	_	nC
Peak Reverse Recovery Current	1	I _{RRM}	-	63	_	Α
Peak Rate of Fall of Recovery Current] [di/dt	-	2259	_	A/μs
Reverse Recovery Energy] [E _{rr}	-	371	_	μJ
Reverse Recovery Time	T _J = 125°C	t _{rr}	-	308	_	ns
Reverse Recovery Charge	V_{CE} = 350 V, I_{C} = 100 A, V_{GE} = ±15 V, R_{G} = 10 Ω	Q _{rr}	-	7290	_	nC
Peak Reverse Recovery Current	1	I _{RRM}	_	95	_	Α
Peak Rate of Fall of Recovery Current] [di/dt	_	238	_	A/μs
Reverse Recovery Energy] [E _{rr}	-	2025	_	μJ
Thermal Resistance - chip-to-heatsink	Thermal grease, Thickness < 100 μ m, λ = 2.87 W/mK	R_{thJH}	-	1.2	-	°C/W
THERMISTOR CHARACTERISTICS						
Nominal Resistance		R ₂₅	_	22	_	kΩ
Nominal Resistance	T = 100°C	R ₁₀₀	-	1486	-	Ω
Deviation of R25		R/R	-5	-	5	%
Power Dissipation		P _D	-	200	-	mW
Power Dissipation Constant			-	2	-	mW/K
	D(05/50) taloropeo 109/		_	3950	_	K
B-value	B(25/50), tolerance $\pm 3\%$		_	3930	_	1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS - HALF BRIDGE IGBT AND DIODE

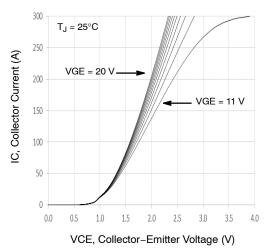


Figure 2. Typical Output Characteristics

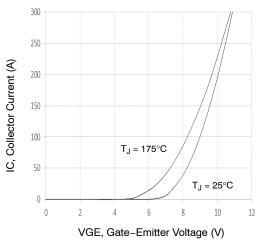


Figure 4. Typical Transfer Characteristics

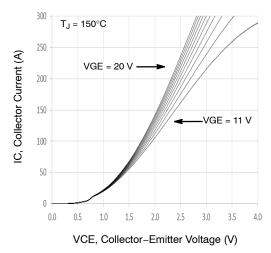


Figure 3. Typical Output Characteristics

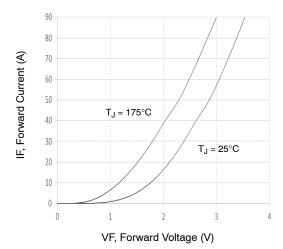


Figure 5. Typical Diode Forward Characteristics

TYPICAL CHARACTERISTICS - HALF BRIDGE IGBT AND DIODE (continued)

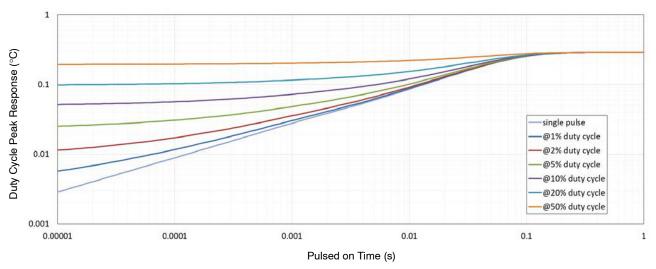


Figure 6. Transient Thermal Impedance (Half Bridge IGBT)

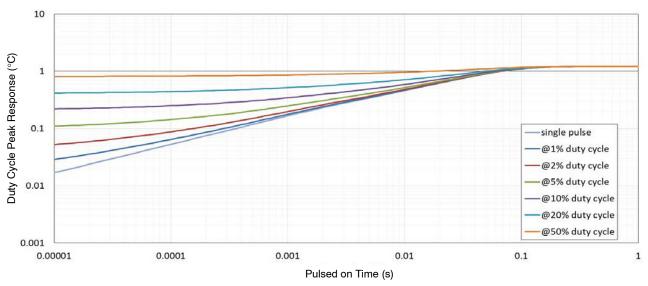


Figure 7. Transient Thermal Impedance (Half Bridge Diode)

TYPICAL CHARACTERISTICS - HALF BRIDGE IGBT AND DIODE (continued)

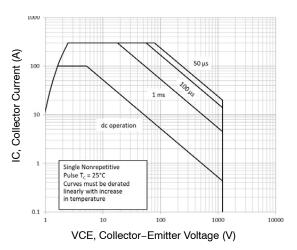


Figure 8. FBSOA

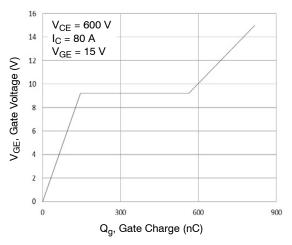


Figure 10. Gate Voltage vs. Gate Charge

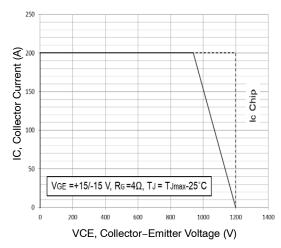


Figure 9. RBSOA

TYPICAL CHARACTERISTICS - NEUTRAL POINT IGBT AND DIODE

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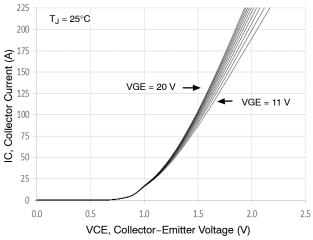
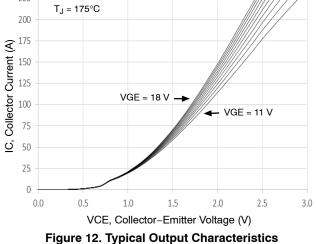


Figure 11. Typical Output Characteristics



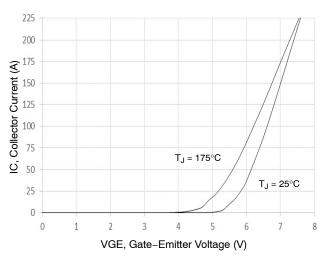


Figure 13. Typical Transfer Characteristics

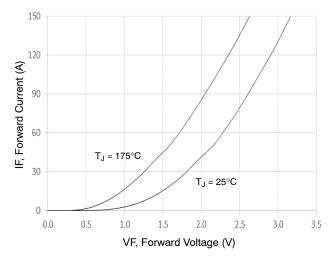


Figure 14. Typical Diode Forward Characteristics

TYPICAL CHARACTERISTICS - NEUTRAL POINT IGBT AND DIODE (continued)

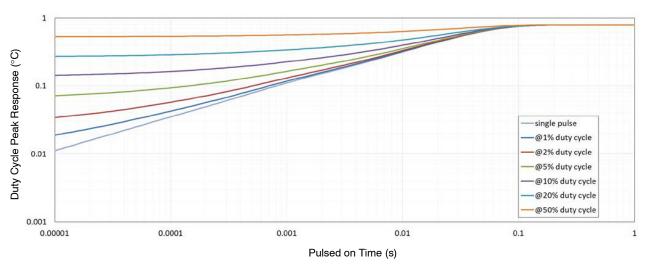


Figure 15. Transient Thermal Impedance (Neutral Point IGBT)

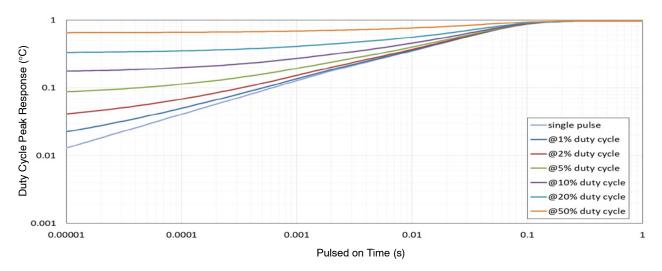
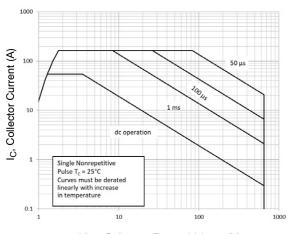


Figure 16. Transient Thermal Impedance (Neutral Point Diode)

TYPICAL CHARACTERISTICS - NEUTRAL POINT IGBT AND DIODE (continued)



V_{CE}, Collector-Emitter Voltage (V)

Figure 17. FBSOA

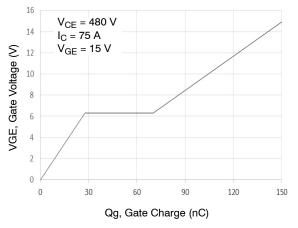


Figure 19. Gate Voltage vs. Gate Charge

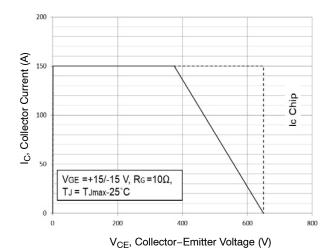


Figure 18. RBSOA

TYPICAL CHARACTERISTICS - HALF BRIDGE IGBT COMUTATES NEUTRAL POINT DIODE

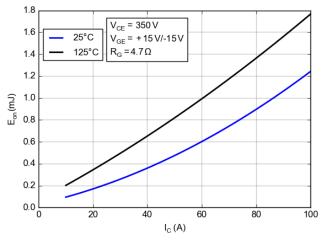
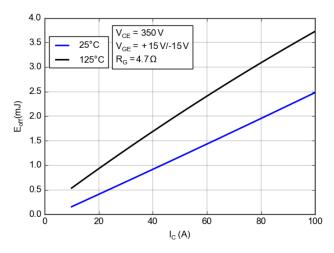


Figure 20. Typical Turn On Loss vs. I_C

Figure 21. Typical Turn On Loss vs. R_G



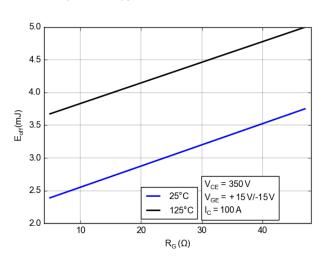
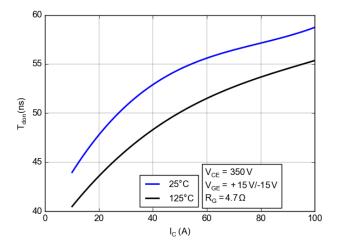


Figure 22. Typical Turn Off Loss vs. I_C

Figure 23. Typical Turn Off Loss vs. R_G



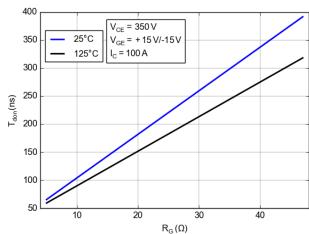
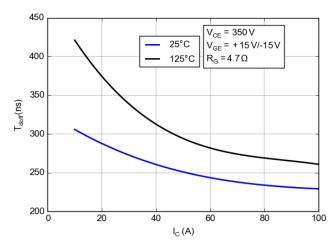


Figure 24. Typical Switching Times Tdon vs. I_C

Figure 25. Typical Switching Times Tdon vs. R_G

TYPICAL CHARACTERISTICS - HALF BRIDGE IGBT COMUTATES NEUTRAL POINT DIODE (continued)



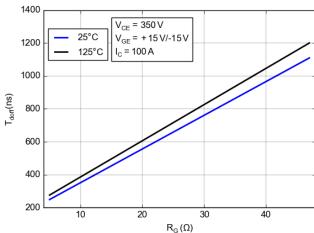
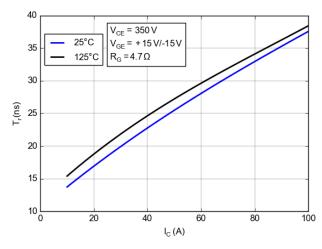


Figure 26. Typical Switching Times Tdoff vs. I_C

Figure 27. Typical Switching Times Tdoff vs. $R_{\mbox{\scriptsize G}}$



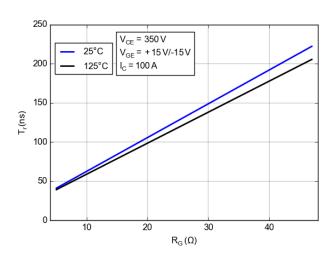
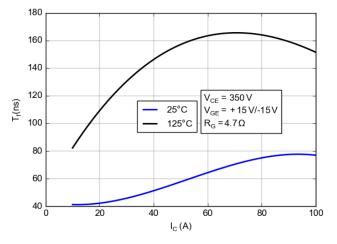


Figure 28. Typical Switching Times Tron vs. I_C

Figure 29. Typical Switching Times Tron vs. R_G



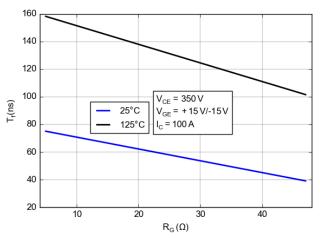
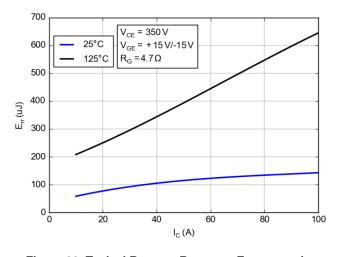


Figure 30. Typical Switching Times Tf vs. I_C

Figure 31. Typical Switching Times Tf vs. R_G

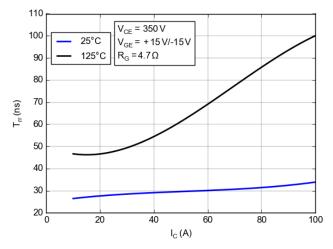
TYPICAL CHARACTERISTICS - HALF BRIDGE IGBT COMUTATES NEUTRAL POINT DIODE (continued)



600 V_{CE} = 350 V 550 25°C $V_{GE} = +15 V/-15 V$ 125°C $I_{\rm C} = 100 \, A$ 500 450 E_{rr} (uJ) 400 350 300 250 200 20 30 40 $R_G(\Omega)$

Figure 32. Typical Reverse Recovery Energy vs. I_C

Figure 33. Typical Reverse Recovery Energy vs. $R_{\mbox{\scriptsize G}}$



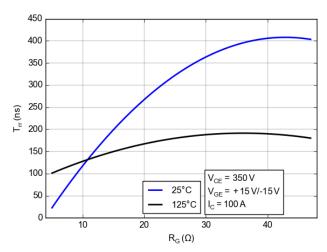
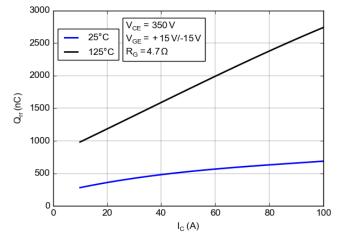


Figure 34. Typical Reverse Recovery Time vs. I_C

Figure 35. Typical Reverse Recovery Time vs. R_G



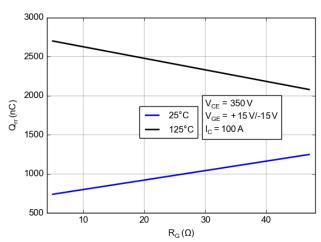
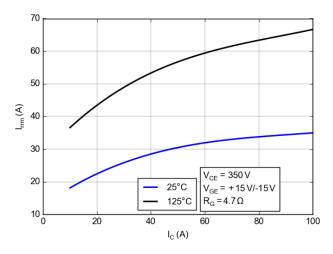


Figure 36. Typical Reverse Recovery Charge vs. I_C

Figure 37. Typical Reverse Recovery Charge vs. R_G

TYPICAL CHARACTERISTICS - HALF BRIDGE IGBT COMUTATES NEUTRAL POINT DIODE (continued)



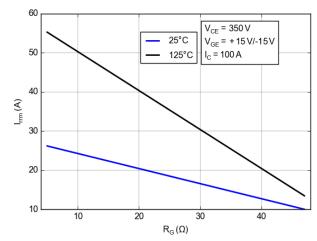
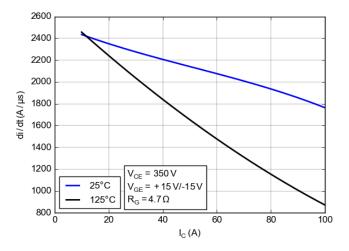


Figure 38. Typical Reverse Recovery Current vs. I_C

Figure 39. Typical Reverse Recovery Current vs. R_G





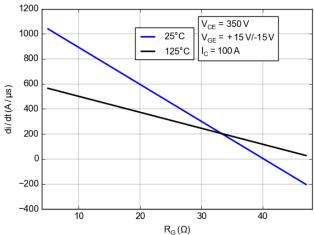
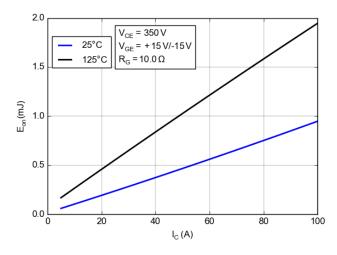


Figure 41. Typical di/dt vs. R_G

TYPICAL CHARACTERISTICS - NEUTRAL POINT IGBT COMUTATES HALF BRIDGE DIODE

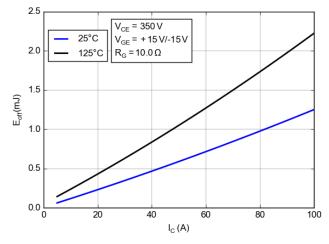
4.0



 $V_{CE} = 350 \text{ V}$ $V_{CE} = 15 \text{ V}/-15 \text{ V}$ $V_{CE} = 100 \text{ A}$ $V_{CE} = 100 \text{ A}$

Figure 42. Typical Turn On Loss vs. I_C

Figure 43. Typical Turn On Loss vs. R_G



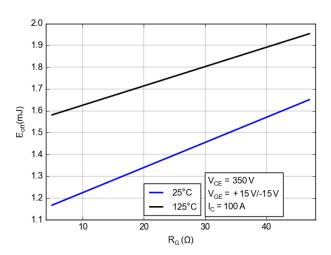
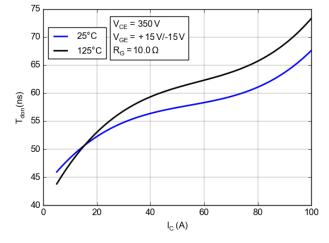


Figure 44. Typical Turn Off Loss vs. I_C

Figure 45. Typical Turn Off Loss vs. R_G



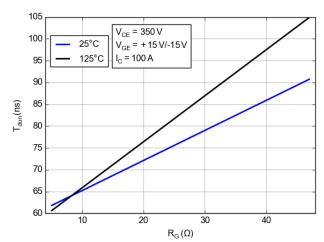


Figure 46. Typical Switching Times Tdon vs. I_C

Figure 47. Typical Switching Times Tdon vs. R_G

TYPICAL CHARACTERISTICS - NEUTRAL POINT IGBT COMUTATES HALF BRIDGE DIODE (continued)

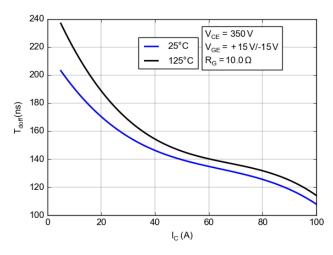
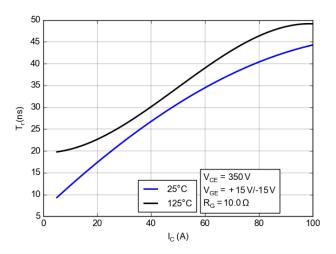


Figure 48. Typical Switching Times Tdoff vs. I_C

Figure 49. Typical Switching Times Tdoff vs. R_G



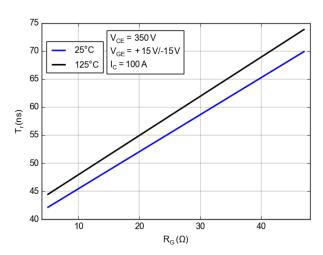
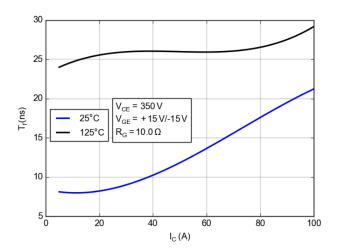


Figure 50. Typical Switching Times Tron vs. I_C

Figure 51. Typical Switching Times Tron vs. R_G



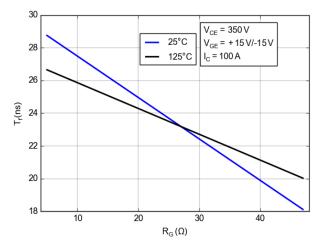
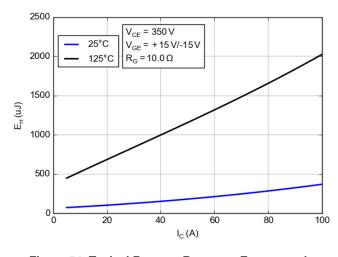


Figure 52. Typical Switching Times Tf vs. I_C

Figure 53. Typical Switching Times Tf vs. R_G

TYPICAL CHARACTERISTICS - NEUTRAL POINT IGBT COMUTATES HALF BRIDGE DIODE (continued)



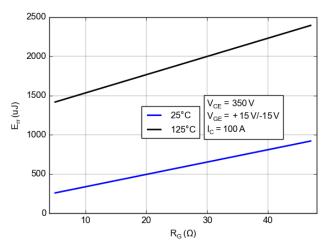
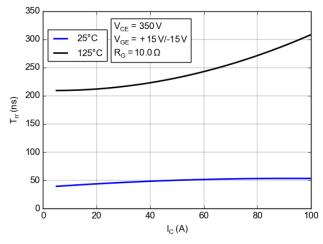


Figure 54. Typical Reverse Recovery Energy vs. I_C

Figure 55. Typical Reverse Recovery Energy vs. R_G



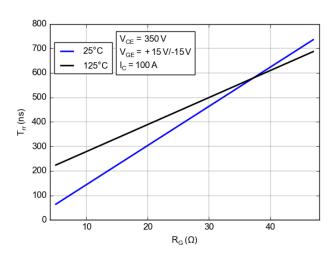
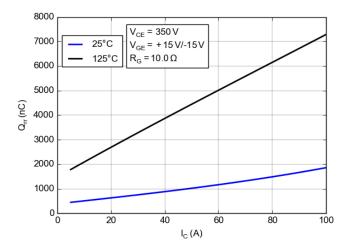


Figure 56. Typical Reverse Recovery Time vs. I_C

Figure 57. Typical Reverse Recovery Time vs. R_G



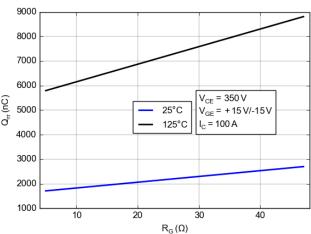
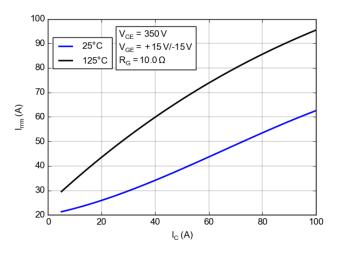


Figure 58. Typical Reverse Recovery Charge vs. I_C

Figure 59. Typical Reverse Recovery Charge vs. R_G

TYPICAL CHARACTERISTICS - NEUTRAL POINT IGBT COMUTATES HALF BRIDGE DIODE (continued)



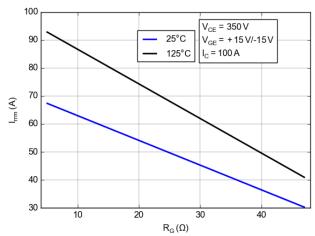
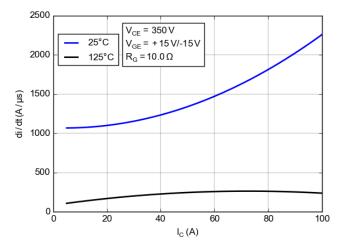


Figure 60. Typical Reverse Recovery Current vs. I_C

Figure 61. Typical Reverse Recovery Current vs. R_G





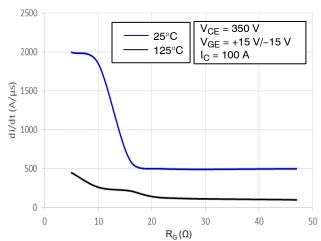


Figure 63. Typical di/dt vs. R_G

TYPICAL CHARACTERISTICS - THERMISTOR

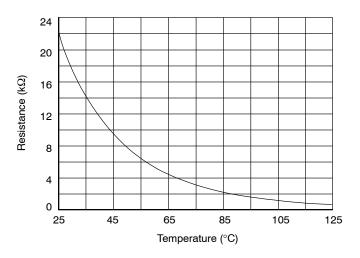


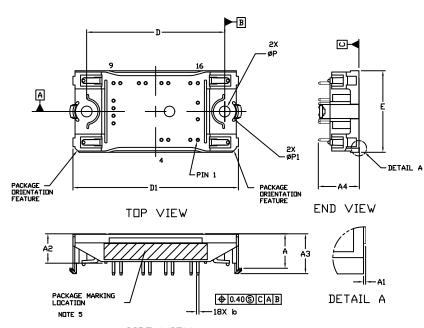
Figure 64. Thermistor Characteristics

PACKAGE MARKING AND ORDERING INFORMATION

Orderable Part Number	ble Part Number Marking Package		Shipping
NXH100T120L3Q0S1NG Q0PACK	NXH100T120L3Q0S1NG	Q0PACK - Case 180AH (Pb-Free and Halide-Free)	24 Units / Blister Tray

PACKAGE DIMENSIONS

PIM18, 55x32.5 / Q0PACK CASE 180AH ISSUE B



SIDE VIEW

MOUNTING HOLE POSITION

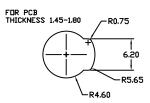
NUTE	NUTE	

	PIN P	NOITIZE		PIN PI	NOITIZE		PIN PI	NDITIZE		PIN P	NDITIZE
PIN	×	Υ	PIN	×	Y	PIN	Х	Υ	PIN	X	Y
1	16.80	11.30	10	-14.10	-10.70	1	16.80	-11.30	10	-14.10	10.70
a	13.80	11.30	11	-6.70	-10.70	2	13.80	-11.30	11	-6.70	10.70
3	5.00	11.30	12	-4.00	-10.70	3	5.00	-11.30	12	-4.00	10.70
4	2.30	11.30	13	2.30	-10.70	4	2.30	-11.30	13	2.30	10.70
5	-16.80	4.70	14	5.00	-10.70	5	-16.80	-4.70	14	5.00	10.70
6	-16.80	1.70	15	13.80	-10.70	6	-16.80	-1.70	15	13.80	10.70
7	-16.80	-1.30	16	16.80	-10.70	7	-16.80	1.30	16	16.80	10.70
8	-16.80	-4.30	17	16.80	-3.50	8	-16.80	4.30	17	16.80	3.50
9	-16.80	-10.70	18	16.80	3.10	9	-16.80	10.70	18	16.80	-3.10

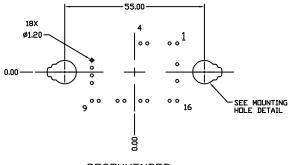
NOTES:

- DIMENSIONING AND TOLERANCING PER. ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION 6 APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 1.00 AND 3.00 FROM THE TERMINAL TIP.
- 4. POSITION OF THE CENTER OF THE TERMINALS
 IS DETERMINED FROM DATUM B THE CENTER OF
 DIMENSION D, X DIRECTION, AND FROM DATUM A,
 Y DIRECTION. POSITIONAL TOLERANCE, AS NOTED
 IN DRAWING, APPLIES TO EACH TERMINAL IN BOTH
 DIRECTIONS.
- PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES.

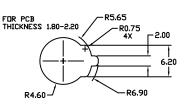
	MILLIMETERS					
DIM	MIN.	N□M.				
Α	13.50	13.90				
A1	0.10	0.30				
A2	11.50	11.90				
A3	15.65	16.05				
A4	16.35	REF				
۵	0.95	1.05				
D	54.80	55.20				
D1	65.60	66.20				
Ε	32.20	32.80				
Р	4.20	4.40				
P1	8.90	9.10				

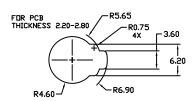


MOUNTING FOOTPRINT ON PAGE 2



RECOMMENDED MOUNTING PATTERN





MOUNTING HOLE DETAIL

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FF150R12KE3G FF200R06KE3 FF200R06YE3 FF300R06KE3_B2 FF600R12IP4V FF800R17KP4_B2 FF900R12IE4V
FP06R12W1T4_B3 FP100R07N3E4 FP100R07N3E4_B11 FP10R06W1E3_B11 FP10R12W1T4_B11 FP10R12YT3 FP15R12W2T4
FP15R12YT3 FP20R06W1E3 FP30R06W1E3 FP40R12KT3G FP75R06KE3 FS10R12YE3 FS150R07PE4 FS150R12PT4
FS150R17N3E4_B11 FS20R06W1E3_B11 FS30R06W1E3_B11 FS75R12KE3G FS75R12W2T4_B11 FZ1600R17HP4_B2
FZ300R12KE3G FZ400R17KE3 FZ400R17KE4 FZ600R65KE3 DF1000R17IE4D_B2 APTGT75DA60T1G DZ800S17K3 F1225R12KT4G F3L200R12W2H3_B11 F3L300R12ME4_B22 F3L75R07W2E3_B11 F4-150R12KS4 F475R07W1H3B11ABOMA1
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