## **TMPIM 50 A CIB/CI Module**

# NXH50C120L2C2ESG, NXH50C120L2C2ES1G

The NXH50C120L2C2ESG is a transfer-molded power module with low thermal resistance substrate containing a converter-inverter-brake circuit consisting of six 50 A, 1600 V rectifiers, six 50 A, 1200 V IGBTs with inverse diodes, one 35 A, 1200 V brake IGBT with brake diode and an NTC thermistor.

The NXH50C120L2C2ES1G is a transfer-molded power module with low thermal resistance substrate containing a converter-inverter circuit consisting of six 50 A, 1600 V rectifiers, six 50 A, 1200 V IGBTs with inverse diodes, and an NTC thermistor.

#### **Features**

- Low Thermal Resistance Substrate for Low Thermal Resistance
- Lower Package Height than Standard Case Modules
- 6 mm Clearance distance between pin to heatsink
- Compact 73 mm × 40 mm × 8 mm Package
- Solderable Pins
- Thermistor
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

### **Typical Applications**

- Industrial Motor Drives
- Servo Drives

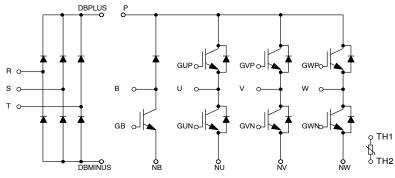


Figure 1. NXH50C120L2C2ESG Schematic Diagram

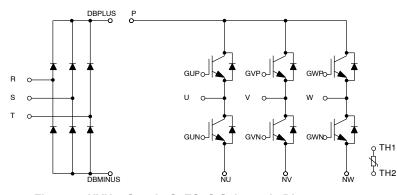
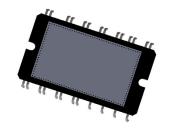


Figure 2. NXH50C120L2C2ES1G Schematic Diagram



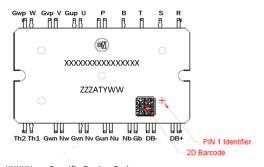
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DIP26 67.8x40 CASE 181AD

#### **MARKING DIAGRAM**



XXXXX = Specific Device Code

ZZZ = Assembly Lot Code

AT = Assembly & Test Site Code

YYWW = Year and Work Week Code

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NXH50C120L2C2ESG	DIP26	6 Units /
NXH50C120L2C2ES1G	(Pb-Free)	Tube

### MAXIMUM RATINGS (Note 1)

Rating	Symbol	Value	Unit
IGBT (INVERTER, BRAKE)			
Collector-emitter Voltage	V <sub>CES</sub>	1200	V
Gate-emitter Voltage	$V_{GE}$	±20	V
Inverter IGBT Continuous Collector Current @ T <sub>C</sub> = 100°C (T <sub>VJmax</sub> = 175°C)	I <sub>C</sub>	50	Α
Inverter IGBT Pulsed Collector Current (T <sub>VJmax</sub> = 175°C)	I <sub>Cpulse</sub>	150	Α
Brake IGBT Continuous Collector Current @ T <sub>C</sub> = 100°C (T <sub>VJmax</sub> = 175°C)	Ic	35	Α
Brake IGBT Pulsed Collector Current (T <sub>VJmax</sub> = 175°C)	I <sub>Cpulse</sub>	105	Α
DIODE (INVERTER, BRAKE)			
Peak Repetitive Reverse Voltage	$V_{RRM}$	1200	V
Inverter Diode Continuous Forward Current @ T <sub>c</sub> = 80°C (Tv <sub>Jmax</sub> = 17°C)	I <sub>F</sub>	50	Α
Inverter Diode Repetitive Peak Forward Current (T <sub>VJmax</sub> = 175°C)	I <sub>FRM</sub>	150	Α
Inverter Diode I <sup>2</sup> t value (60 Hz single half-sine wave)	l <sup>2</sup> t	94	A <sup>2</sup> t
Brake Diode Continuous Forward Current @ Tc = 80°C (T <sub>VJmax</sub> = 175°C)	I <sub>F</sub>	35	Α
Brake Diode Repetitive Peak Forward Current (T <sub>VJmax</sub> = 175°C)	I <sub>FRM</sub>	105	Α
Brake Diode I2t value (60 Hz single half-sine wave)	l <sup>2</sup> t	46	A <sup>2</sup> t
RECTIFIER DIODE			
Peak Repetitive Reverse Voltage	$V_{RRM}$	1600	V
Continuous Forward Current @ T <sub>C</sub> = 80°C (T <sub>VJmax</sub> = 150°C)	I <sub>F</sub>	50	Α
Repetitive Peak Forward Current (T <sub>VJmax</sub> = 150°C)	I <sub>FRM</sub>	150	Α
I <sup>2</sup> t value (60 Hz single half–sine wave) @ 25°C	I <sup>2</sup> t	1126	A <sup>2</sup> t
(60 Hz single half-sine wave) @ 150°C		510	
Surge current (10ms sin180°) @ 25°C	IFSM	520	Α
MODULE THERMAL PROPERTIES			_
Storage Temperature Range	T <sub>stg</sub>	-40 to 125	°C
INSULATION PROPERTIES			
Isolation Test Voltage, t = 1 s, 50 Hz	V <sub>is</sub>	3000	$V_{RMS}$
Internal Isolation		HPS	
Creepage Distance		6.0	mm
Clearance Distance		6.0	mm
Comperative Tracking Index	CTI	>400	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Refer to ELECTRICAL CHARACTERISTICS, RECOMMENDED OPERATING RANGES and/or APPLICATION INFORMATION for Safe

Operating parameters.

### **ELECTRICAL CHARACTERISTICS** (T<sub>.1</sub> = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
INVERTER IGBT CHARACTERISTICS						
Collector-emitter Cutoff Current	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V	I <sub>CES</sub>	_	_	250	μА
Collector-emitter Saturation Voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 25°C	V <sub>CE(sat)</sub>	=	1.8	2.4	V
	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 50 A, T <sub>J</sub> = 150°C	, ,	_	2	-	
Gate-emitter Threshold Voltage	$V_{GE} = V_{CE}$ , $I_C = 6 \text{ mA}$	V <sub>GE(TH)</sub>	4.8	6	6.8	V
Gate Leakage Current	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0 V	I <sub>GES</sub>	=	_	400	nA
Turn-on Delay Time	$T_J = 25$ °C $V_{CE} = 600 \text{ V}, I_C = 50 \text{ A}$ $V_{GE} = \pm 15 \text{ V}, R_G = 15 \Omega$	t <sub>d(on)</sub>	=	144	_	ns
Rise Time		t <sub>r</sub>	_	104	_	1
Turn-off Delay Time	- 'GE = '0 1, ' 'G	t <sub>d(off)</sub>	=	380	_	1
Fall Time	1	t <sub>f</sub>	_	52	_	1
Turn-on Switching Loss per Pulse	1	E <sub>on</sub>	_	5870	_	μJ
Turn-off Switching Loss per Pulse	1	E <sub>off</sub>	_	1700	_	1
Turn-on Delay Time	T <sub>J</sub> = 150°C	t <sub>d(on)</sub>	_	136	_	ns
Rise Time	$V_{CE} = 600 \text{ V, } I_{C} = 50 \text{ A}$ $V_{GE} = \pm 15 \text{ V, } R_{G} = 15 \Omega$	t <sub>r</sub>	=	112	_	1
Turn-off Delay Time	VGE = ±10 V, NG = 10 LL	t <sub>d(off)</sub>	=	432	-	
Fall Time	1	t <sub>f</sub>	_	184	_	1
Turn-on Switching Loss per Pulse	1	E <sub>on</sub>	=	9530	_	μJ
Turn-off Switching Loss per Pulse	1	E <sub>off</sub>	_	3800	_	1
Input Capacitance	V <sub>CE</sub> = 20 V, V <sub>GE</sub> = 0 V, f = 100	C <sub>ies</sub>	_	11897	_	pF
Output Capacitance	H kHz	C <sub>oes</sub>	=	416	-	1
Reverse Transfer Capacitance	1	C <sub>res</sub>	-	240	_	1
Total Gate Charge	$V_{CE} = 600 \text{ V}, I_{C} = 50 \text{ A}, \ V_{GE} = 0 \text{ V} \sim \pm 15 \text{ V}$	$Q_g$	-	558	-	nC
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance - Chip-to-Case		$R_{thJC}$	=	0.26	=	°C/W
INVERSE DIODE CHARACTERISTICS						
Diode Forward Voltage	I <sub>F</sub> = 50 A, T <sub>J</sub> = 25°C	V <sub>F</sub>	_	1.9	2.7	V
	I <sub>F</sub> = 50 A, T <sub>J</sub> = 150°C		=	1.7	_	1
Reverse Recovery Charge	T <sub>J</sub> = 25°C	Q <sub>rr</sub>	_	2.58	_	μС
Peak Reverse Recovery Current	$V_{CE} = 600 \text{ V}, I_{C} = 50 \text{ A}$ $V_{GE} = \pm 15 \text{ V}, R_{G} = 15 \Omega$	I <sub>RRM</sub>	-	20	-	Α
Reverse Recovery Energy		E <sub>rr</sub>	_	640	_	μJ
Reverse Recovery Charge	T <sub>J</sub> = 150°C	Q <sub>rr</sub>	_	8.0	-	μС
Peak Reverse Recovery Current	$V_{CE}$ = 600 V, $I_{C}$ = 50 A $V_{GE}$ = ±15 V, $R_{G}$ = 15 Ω	I <sub>RRM</sub>	_	32.5	_	Α
Reverse Recovery Energy	GL =,u	E <sub>rr</sub>	_	2300	_	μJ
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance - Chip-to-Case		R <sub>thJC</sub>	_	0.42	_	°C/W

**ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified) (continued)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
RECTIFIER DIODE CHARACTERISTICS						
Diode Forward Voltage	I <sub>F</sub> = 50 A, T <sub>J</sub> = 25°C	V <sub>F</sub>	=	1.2	1.6	V
	I <sub>F</sub> = 50 A, T <sub>J</sub> = 150°C		-	1.1	_	
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance - Chip-to-Case		R <sub>thJC</sub>	_	0.33	_	°C/W
BRAKE IGBT CHARACTERISTICS					•	
Collector-emitter Cutoff Current	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V	I <sub>CES</sub>	=	-	250	μΑ
Collector-emitter Saturation Voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 35 A, T <sub>J</sub> = 25°C	V <sub>CE(sat)</sub>	-	1.8	2.4	V
	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 35 A, T <sub>J</sub> = 125°C	,	-	1.9	_	<u> </u>
Gate-emitter Threshold Voltage	$V_{GE} = V_{CE}, I_{C} = 4.25 \text{ mA}$	V <sub>GE(TH)</sub>	4.8	6	6.8	V
Gate Leakage Current	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0 V	I <sub>GES</sub>	-	-	400	nA
Turn-on Delay Time	T <sub>J</sub> = 25°C	t <sub>d(on)</sub>	-	104	_	ns
Rise Time	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 35 A	t <sub>r</sub>	_	64	_	
Turn-off Delay Time	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	t <sub>d(off)</sub>	-	277	-	
Fall Time	1	t <sub>f</sub>	-	53	_	
Turn-on Switching Loss per Pulse	1	E <sub>on</sub>	-	2900	_	μJ
Turn off Switching Loss per Pulse	1 1	E <sub>off</sub>	=	1200	-	
Turn-on Delay Time	T <sub>J</sub> = 150°C	t <sub>d(on)</sub>	=	168	_	ns
Rise Time	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 35 A	t <sub>r</sub>	=	72	=	
Turn-off Delay Time	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	t <sub>d(off)</sub>	=	320	=	
Fall Time	1	t <sub>f</sub>	=	165	=	
Turn-on Switching Loss per Pulse	1	E <sub>on</sub>	-	4030	-	μJ
Turn off Switching Loss per Pulse	1	E <sub>off</sub>	_	2200	_	
Input Capacitance	V <sub>CE</sub> = 20 V. V <sub>GE</sub> = 0 V.	C <sub>ies</sub>	=	8333	=	pF
Output Capacitance	f = 100 kHz	C <sub>oes</sub>	-	298	-	
Reverse Transfer Capacitance	1 i	C <sub>res</sub>	_	175	_	
Total Gate Charge	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 35 A, V <sub>GE</sub> = 0V ~ +15 V	$Q_g$	-	360	_	nC
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance - Chip-to-Case		R <sub>thJC</sub>	-	0.42	_	°C/W
BRAKE DIODE CHARACTERISTICS						
Brake Diode Reverse Leakage Current	VR = 1200 V	IR	-	-	200	μΑ
Diode Forward Voltage	I <sub>F</sub> = 35 A, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	2.2	2.7	V
	I <sub>F</sub> = 35 A, T <sub>J</sub> = 150°C		=	2	=	
Reverse Recovery Time	T <sub>J</sub> = 25°C	t <sub>rr</sub>		224		ns
Reverse Recovery Charge	V <sub>CE</sub> = 600 V, I <sub>C</sub> = 35 A	Q <sub>rr</sub>	-	1.51	-	°C
Peak Reverse Recovery Current	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	I <sub>RRM</sub>	=	18	_	Α
Reverse Recovery Energy	1	E <sub>rr</sub>	=	410	_	μJ
Reverse Recovery Time	T <sub>J</sub> = 150°C	t <sub>rr</sub>	-	532	-	ns
Reverse Recovery Charge	$V_{CE} = 600 \text{ V}, I_{C} = 35 \text{ A}$	Q <sub>rr</sub>	-	5,36	_	°C
Peak Reverse Recovery Current	$V_{GE}$ = ±15 V, $R_{G}$ = 15 $\Omega$	I <sub>RRM</sub>	_	30	_	Α
Reverse Recovery Energy	1	E <sub>rr</sub>	-	1983	_	μJ
Temperature under switching conditions		Tvj op	-40		150	°C
Thermal Resistance - Chip-to-Case		R <sub>thJC</sub>	_	0.65	_	°C/W

### **ELECTRICAL CHARACTERISTICS** (T<sub>.J</sub> = 25°C unless otherwise specified) (continued)

ELECTRICAL CHARACTERISTICS (1) = 25 °C unicos cinerwise specifical) (continued)									
Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit			
THERMISTOR CHARACTERISTICS									
Nominal Resistance	T = 25°C	R <sub>25</sub>	_	5	=	kΩ			
Nominal Resistance	T = 100°C	R <sub>100</sub>	=	493.3	=	Ω			
Deviation of R25		ΔR/R	-5	=	5	%			
Power Dissipation		P <sub>D</sub>	=	20	-	mW			
Power Dissipation Constant			_	1.4	=	mW/K			
B-value	B(25/50), tolerance ±2%		=	3375	-	K			
B-value	B(25/100), tolerance ±2%		=	3433	-	K			

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### TYPICAL CHARACTERISTICS - INVERTER IGBT & INVERSE DIODE

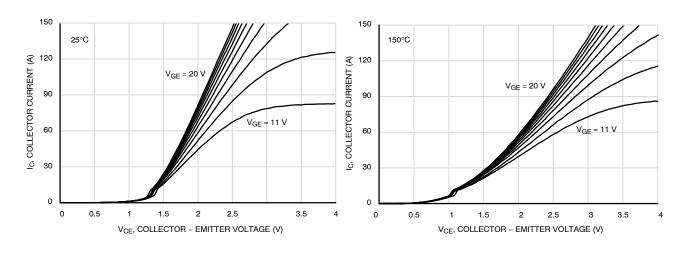


Figure 3. Inverter IGBT Typical Output Characteristic (25°C)

Characteristic (150°C) 150 150 Ic, COLLECTOR CURRENT (A) 30 90 90 30 120 I<sub>F</sub>, Forward Current (A) 90 60 150°C 150°0 25°C 30 0 2 10 12 14 0 0.5 1.5 2.5

Figure 5. Inverter IGBT Typical Transfer Characteristic

V<sub>GE</sub>, Gate-Emitter Voltage (V)

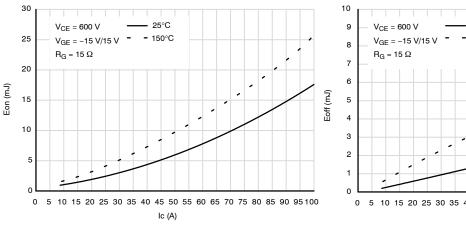


Figure 7. Inverter IGBT Typical Turn On Loss vs IC

Figure 6. Inverter Diode Typical Forward Characteristic

V<sub>F</sub>, Forward Voltage (V)

3

Figure 4. Inverter IGBT Typical Output

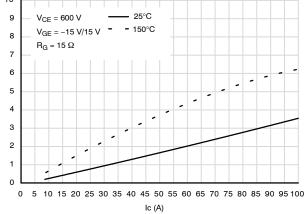
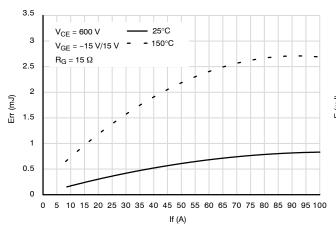


Figure 8. Inverter IGBT Typical Turn Off Loss vs IC

### TYPICAL CHARACTERISTICS - INVERTER IGBT & INVERSE DIODE



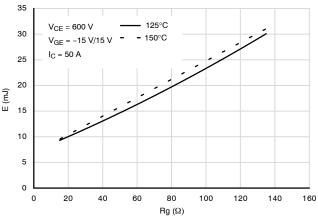
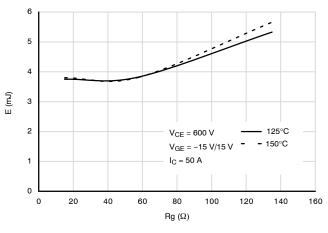


Figure 9. Inverter Diode Typical Reverse Recovery Energy vs IC

Figure 10. Inverter IGBT Typical Turn On Loss vs RG



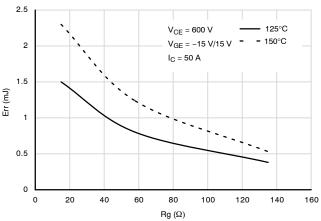


Figure 11. Inverter IGBT Typical Turn Off Loss vs RG

Figure 12. Inverter Diode Typical Reverse Recovery Energy vs RG

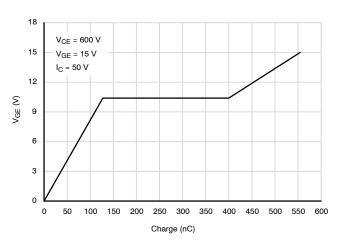


Figure 13. Inverter IGBT Gate Voltage vs Gate Charge

### TYPICAL CHARACTERISTICS - INVERTER IGBT & INVERSE DIODE

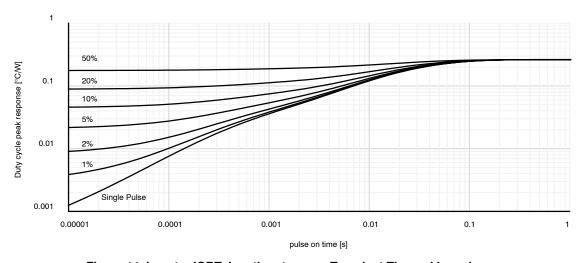


Figure 14. Inverter IGBT Junction-to-case Transient Thermal Impedance

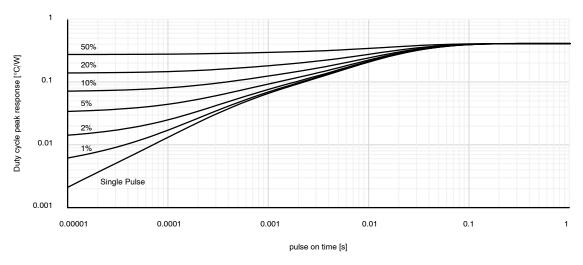


Figure 15. Inverter Diode Junction-to-case Transient Thermal Impedance

#### TYPICAL CHARACTERISTICS - BRAKE IGBT & BRAKE DIODE

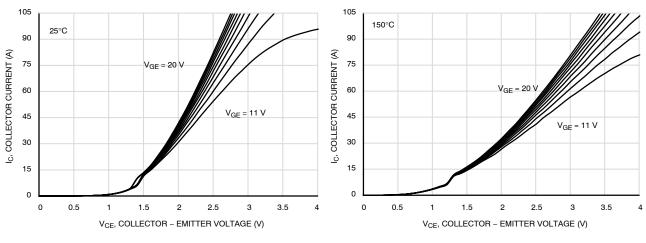
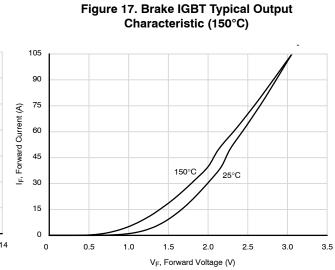


Figure 16. Brake IGBT Typical Output Characteristic (25°C)



105 90 (£) 75 60 150°C 25°C 150°C 25°C

Figure 18. Brake IGBT Typical Transfer Characteristic

Figure 19. Brake Diode Typical Forward Characteristic

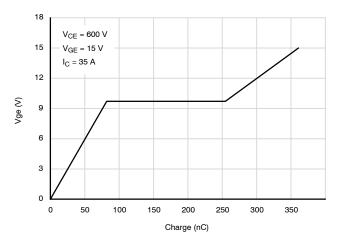


Figure 20. Brake IGBT Gate Voltage vs Gate Charge

### **TYPICAL CHARACTERISTICS - RECTIFIER**

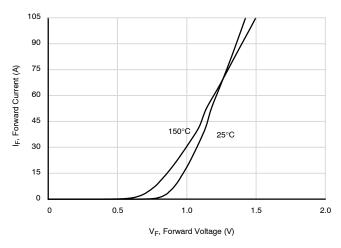


Figure 21. Rectifier Typical Forward Characteristic

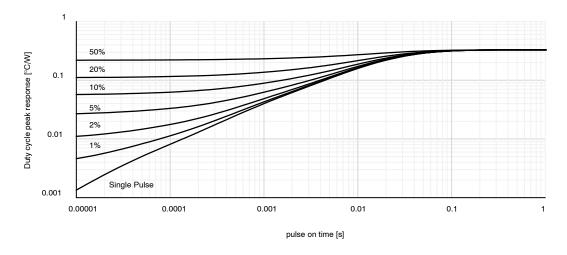
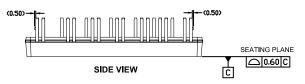


Figure 22. Rectifier Junction-to-Case Transient Thermal Impedance



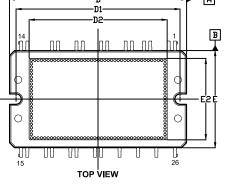
DIP26 67.8x40 CASE 181AD **ISSUE B** 

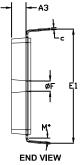
**DATE 05 AUG 2021** 

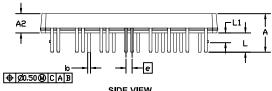


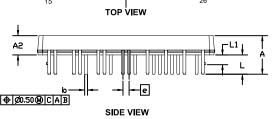


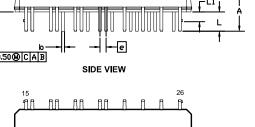
- Dimensioning and tolerancing as per ASME Y14.5M, 2009 1.
- Controlling Dimension: Millimeters 2.
- 3. Dimensions are exclusive of Burrs, Mold Flash, and Tiebar extrusions
- 4. Dimensions "b" and "c" apply to plated leads
- 5. Position of the leads is determine at the root of the lead where it exits the package body

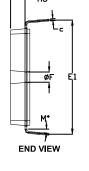












DIM	MILLIMETERS						
DIM	MIN	МОМ	MAX				
Α	15.50	16.00	16.50				
A2	7.80	8.00	8.20				
А3		6.00 REF					
b	1.10	1.20	1.30				
С	0.70	0.80	0.90				
D	72.70	73.20	73.70				
D1	67.30	68.30					
D2		57.30 REF	=				
E	39.70	40.20	40.70				
E1	46.70	47.20	47.70				
E2		33.87 REF					
е		2.54 BSC					
F	4.00	4.20	4.40				
L		8.00 REF					
L1	3.50	4.00	4.50				
М	4°	5°	6°				

### **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

ZZZ = Assembly Lot Code

AT = Assembly & Test Location

= Year

WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

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