## Zener Voltage Regulators 200 mW Micro Packaged

## NZD5V1MU Series

This Zener diode is designed to provide voltage regulation protection and is especially attractive in situations where space is at a premium. Because of its small size, it is suited for use in mobile applications.

## Specification Features:

- Standard Zener Breakdown Voltage Range:
2.2 V to 9.1 V
- Steady State Power Rating of 200 mW
- Small Body Outline Dimensions:
$0.60 \mathrm{~mm} \times 0.30 \mathrm{~mm}$
- Low Body Height: 0.30 mm
- ESD Rating of Class $3(>8 \mathrm{kV})$ per Human Body Model
- These Devices are $\mathrm{Pb}-$ Free, Halogen Free/BFR Free and are RoHS Compliant


## Mechanical Characteristics:

MOUNTING POSITION: Any
QUALIFIED MAX REFLOW TEMPERATURE: $260^{\circ} \mathrm{C}$
Device Meets MSL 1 Requirements

## MAXIMUM RATINGS

| Rating | Symbol | Max | Unit |
| :--- | :---: | :---: | :---: |
| Total Device Dissipation FR-5 Board, <br> (Note 1) @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | 200 | mW |
| Thermal Resistance from <br> Junction-to-Ambient | $\mathrm{R}_{\text {日JA }}$ | 400 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Junction and Storage Temperature Range | $\mathrm{T}_{\mathrm{J}}, \mathrm{T}_{\text {stg }}$ | -55 to <br> +150 | ${ }^{\circ} \mathrm{C}$ |
| Lead Solder Temperature - Maximum <br> (10 Second Duration) | $\mathrm{T}_{\mathrm{L}}$ | 260 | ${ }^{\circ} \mathrm{C}$ |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $F R-5=1.0 \times 0.75 \times 0.62 \mathrm{in}$.

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ORDERING INFORMATION

| Device | Package | Shipping $\dagger$ |
| :---: | :---: | :---: |
| NZDxxxMUT5G | X3DFN <br> (Pb-Free) |  <br> Reel |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## DEVICE MARKING INFORMATION

See specific marking information in the device marking column of the Electrical Characteristics tables starting on page 3 of this data sheet.

## NZD5V1MU Series

## ELECTRICAL CHARACTERISTICS

( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise noted,
$\mathrm{V}_{\mathrm{F}}=1.1 \mathrm{~V}$ Max. @ $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ for all types)

| Symbol | Parameter |
| :---: | :--- |
| $\mathrm{V}_{\mathrm{Z}}$ | Reverse Zener Voltage @ $\mathrm{I}_{\mathrm{ZT}}$ |
| $\mathrm{I}_{\mathrm{ZT}}$ | Reverse Current |
| $\mathrm{Z}_{\mathrm{ZT}}$ | Maximum Zener Impedance @ $\mathrm{I}_{\mathrm{ZT}}$ |
| $\mathrm{I}_{\mathrm{ZK}}$ | Reverse Current |
| $\mathrm{Z}_{\mathrm{ZK}}$ | Maximum Zener Impedance @ $\mathrm{I}_{\mathrm{ZK}}$ |
| $\mathrm{I}_{\mathrm{R}}$ | Reverse Leakage Current @ $\mathrm{V}_{\mathrm{R}}$ |
| $\mathrm{V}_{\mathrm{R}}$ | Reverse Voltage |
| $\mathrm{I}_{\mathrm{F}}$ | Forward Current |
| $\mathrm{V}_{\mathrm{F}}$ | Forward Voltage @ $\mathrm{I}_{\mathrm{F}}$ |
| $\Theta \mathrm{V}_{\mathrm{Z}}$ | Maximum Temperature Coefficient of $\mathrm{V}_{\mathrm{Z}}$ |
| C | Max. Capacitance @ $\mathrm{V}_{\mathrm{R}}=0$ and $\mathrm{f}=1 \mathrm{MHz}$ |




Figure 1. Steady State Power Derating

## NZD5V1MU Series

ELECTRICAL CHARACTERISTICS $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted, $\mathrm{V}_{\mathrm{F}}=1.1 \mathrm{~V}$ Max. $@ \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ for all types $)$

| Device | Device Marking | Zener Voltage (Note 1) |  |  | Zener Impedance |  |  | Leakage Current |  | $\begin{gathered} \Theta V_{\mathrm{Z}} \\ (\mathrm{mV} / \mathrm{k}) @ \mathrm{I}_{\mathrm{ZT}} \end{gathered}$ |  | $\begin{gathered} C \\ @ V_{R}=0 \\ f=1 \mathrm{MHz} \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\mathrm{V}_{\mathrm{Z}}$ (Volts) |  | $\begin{array}{\|c} @ \\ \hline \mathrm{~mA} \\ \hline \end{array}$ | $\begin{array}{\|c} \hline \begin{array}{c} \mathrm{Z}_{\mathrm{ZT}} \\ @ \mathrm{I}_{\mathrm{ZT}} \end{array} \\ \hline \boldsymbol{\Omega} \\ \hline \end{array}$ | $\mathrm{Z}_{\mathrm{zk}}$ @ $\mathrm{I}_{\mathrm{zk}}$ |  | $\mathrm{I}_{\mathrm{R}} @ \mathrm{~V}_{\mathrm{R}}$ |  |  |  |  |
|  |  | Min | Max |  |  | $\Omega$ | mA | $\mu \mathrm{A}$ | Volts | Min | Max | pF |
| NZD2V2MUT5G | K* | 2.08 | 2.30 | 4 | 100 | 1000 | 1 | 50 | 1 | -3.5 | 0 | 210 |
| NZD2V4MUT5G | $D^{* * *}$ | 2.28 | 2.52 | 5 | 100 | 1000 | 1 | 50 | 1 | -3.5 | 0 | 210 |
| NZD2V7MUT5G | E*** | 2.57 | 2.84 | 5 | 100 | 1000 | 1 | 20 | 1 | -3.5 | 0 | 210 |
| NZD3V0MUT5G | Q* | 2.85 | 3.15 | 5 | 100 | 1000 | 1 | 10 | 1 | -3.5 | 0 | 210 |
| NZD3V3MUT5G | $\mathrm{F}^{* * *}$ | 3.14 | 3.47 | 5 | 100 | 1000 | 1 | 10 | 1 | -3.5 | 0 | 210 |
| NZD3V6MUT5G | J*** | 3.42 | 3.78 | 5 | 100 | 1000 | 1 | 10 | 1 | -3.5 | 0 | 210 |
| NZD3V9MUT5G | L | 3.71 | 4.10 | 5 | 100 | 1000 | 1 | 5 | 1 | -3.5 | -2.5 | 210 |
| NZD4V3MUT5G | D** | 4.09 | 4.52 | 5 | 100 | 1000 | 1 | 5 | 1 | -3.5 | 0 | 210 |
| NZD4V7MUT5G | P | 4.47 | 4.94 | 5 | 100 | 800 | 0.5 | 2 | 1 | -3.5 | 0.2 | 150 |
| NZD5V1MUT5G | Q | 4.85 | 5.36 | 5 | 80 | 500 | 0.5 | 2 | 1.5 | -2.7 | 1.2 | 130 |
| NZD5V6MUT5G | R | 5.32 | 5.88 | 5 | 60 | 200 | 0.5 | 1 | 2.5 | -2.0 | 2.5 | 115 |
| NZD6V2MUT5G | T | 5.89 | 6.51 | 5 | 60 | 100 | 0.5 | 1 | 3.0 | 0.4 | 3.7 | 110 |
| NZD6V8MUT5G | K*** | 6.46 | 7.14 | 5 | 40 | 60 | 0.5 | 0.5 | 3.5 | 1.2 | 4.5 | 105 |
| NZD7V5MUT5G | L*** | 7.13 | 7.88 | 5 | 30 | 60 | 0.5 | 0.5 | 4.0 | 2.5 | 5.3 | 100 |
| NZD8V2MUT5G | 2 | 7.79 | 8.61 | 5 | 30 | 60 | 0.5 | 0.5 | 5.0 | 3.2 | 6.2 | 90 |
| NZD9V1MUT5G | E** | 8.65 | 9.56 | 5 | 30 | 60 | 0.5 | 0.5 | 6.0 | 3.8 | 7.0 | 80 |

*Rotated $90^{\circ}$.
**Rotated $180^{\circ}$.
***Rotated $270^{\circ}$.

1. Zener voltage is measured with a pulse test current $\mathrm{I}_{\mathrm{Z}}$ at an ambient temperature of $25^{\circ} \mathrm{C}$.

NOTES:

1. DIMENSIONING AND TOLERANCING PER
DIMENSIONING AND
ASME Y14.5M, 1994
ASME YONTLIN, 1994.

| MILLIMETERS |  |  |
| :---: | :---: | :---: |
| DIM | MIN | MAX |
| A | 0.25 | 0.33 |
| A1 | --- | 0.05 |
| b | 0.22 | 0.28 |
| D | 0.58 | 0.66 |
| E | 0.28 | 0.36 |
| e | 0.355 | BSC |
| L2 | 0.17 | 0.23 |

GENERIC MARKING DIAGRAM*

PIN 1
XM

X = Specific Device Code
M = Date Code

RECOMMENDED MOUNTING FOOTPRINT*


See Application Note AND8398/D for more mounting details
*For additional information on our Pb -Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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| ---: | :--- | :--- | :--- |
| DESCRIPTION: | X3DFN2, 0.62X0.32, 0.355P, (0201) | PAGE 1 OF 1 |

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