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December 2011

PF5102 N-Channel Switch

Features

- This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from process 51.
- · See J111 for characteristics.



Absolute Maximum Ratings* T_a = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V_{DG}	Drain-Gate Voltage	40	V	
V _{GS}	Gate-Source Voltage	-40	V	
I _{GF}	Forward Gate Current	50	mA	
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C	

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired. **NOTES:**

- 1. These ratings are based on a maximum junction temperature of 150 degrees C.
- 2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Max.	Units	
P _D	Total Device Dissipation	625	mW	
	Derate above 25°C	5.0	mW/°C	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient	357	°C/W	

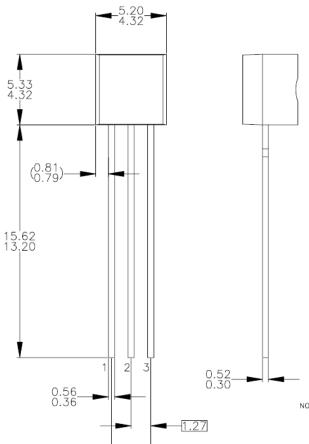
Electrical Characteristics T_a = 25°C unless otherwise noted

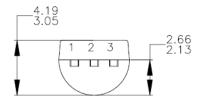
Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Characteri	stics				
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = -1.0 \mu A, V_{DS} = 0$	-40		V
I _{GSS}	Gate Reverse Current	$V_{GS} = -15V, V_{DS} = 0$		-1.0	nA
		$V_{GS} = -15V, V_{DS} = 0, T_A = 125^{\circ}C$		-0.2	μΑ
V _{GS(off)}	Gate-Source Cutoff Voltage	$V_{DS} = 15V, I_{D} = 1.0nA$	-0.7	-1.6	V
V _{GS(f)}	Gate-Source Forward Voltage	$I_{G} = 1.0 \text{mA}, V_{DS} = 0$		1.0	V
On Characteri	stics				
I _{DSS}	Zero-Gate Voltage Drain Current *	$V_{DS} = 15V, V_{GS} = 0$	4.0	20	mA
Small Signal C	Characteristics				
9 _{fs}	Forward Transfer Conductance	$V_{DS} = 15V, V_{GS} = 0, f = 1.0KHz$	11,000		μmhous
g _{oss}	Output Conductance	$V_{DS} = 15V$, $I_D = 500\mu A$, $f = 1.0KHz$		25	μmhous
C _{iss}	Input Capacitance	$V_{DG} = 15V, V_{GS} = 0, f = 1.0MHz$		16	pF
C _{rss}	Reverse Transfer Capacitance	$V_{DG} = 15V, V_{GS} = 0, f = 1.0MHz$		6	pF

^{*} Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 1.0%

Physical Dimension

TO-92





2.54

NOTES: UNLESS OTHERWISE SPECIFIED

- A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.

 B) ALL DIMENSIONS ARE IN MILLIMETERS.
 C) DRAWING CONFORMS TO ASME Y14.5M-1994.
 D) TO-92 (92,94,96,97,98) PIN CONFIGURATION:

z		92		94		96			97			98			
₫.	Р	F	М	Р	F	М	Ρ	F	М	Р	F	М	Р	F	М
1	Ε	S	S	Ε	S	S	В	D	G	С	G	D	C	G	D
2	В	D	G	С	G	D	Ε	S	S	В	D	G	Ε	S	S
3	С	G	D	В	D	G	С	G	D	Ε	S	S	В	D	G

LEGEND: P - BIPOLAR F - JFET M - DMOS E - EMITTER B - BASE C - COLLECTOR

- E) FOR PACKAGE 92, 94, 96, 97 AND 98:
 PIN CONFIGURATION DRAIN "D" AND SOURCE "S"
 ARE INTERCHANGEAGLE AT JFET "F" OPTION.
 F) DRAWING FILENAME: MKT-ZAO3DREV3.

Dimensions in Millimeters





The Power Franchise®

bwer

franchise

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TinyBuck™

TinyCalc™

TinyLogic[®]

TINYOPTO™

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Definition of Terms

Definition of Terms							
Datasheet Identification		Definition					
Advance Information Formative / In Design		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.					
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