PZT2222A

NPN Silicon Planar Epitaxial Transistor

This NPN Silicon Epitaxial transistor is designed for use in linear and switching applications. The device is housed in the SOT-223 package which is designed for medium power surface mount applications.

Features

- PNP Complement is PZT2907AT1
- The SOT-223 Package Can be Soldered Using Wave or Reflow
- SOT-223 Package Ensures Level Mounting, Resulting in Improved Thermal Conduction, and Allows Visual Inspection of Soldered Joints
- The Formed Leads Absorb Thermal Stress During Soldering, Eliminating the Possibility of Damage to the Die
- Available in 12 mm Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	40	Vdc
Collector-Base Voltage	V _{CBO}	75	Vdc
Emitter-Base Voltage (Open Collector)	V _{EBO}	6.0	Vdc
Collector Current	Ic	600	mAdc
Total Power Dissipation up to T _A = 25°C (Note 1)	P _D	1.5	W
Storage Temperature Range	T _{stg}	- 65 to +150	°C
Junction Temperature Range	T_J	– 55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Device mounted on an epoxy printed circuit board 1.575 inches x 1.575 inches x 0.059 inches; mounting pad for the collector lead min. 0.93 inches².

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	83.3	°C/W
Lead Temperature for Soldering, 0.0625" from case Time in Solder Bath	T _L	260 10	°C Sec

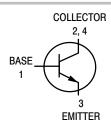


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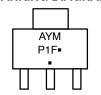
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SOT-223 PACKAGE NPN SILICON TRANSISTOR SURFACE MOUNT





MARKING DIAGRAM



A = Assembly Location

Y = Year M = Month Code

= Pb-Free Package(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
PZT2222AT1G	SOT-223 (Pb-Free)	1,000 Tape & Reel
SPZT2222AT1G	SOT-223 (Pb-Free)	1,000 Tape & Reel
PZT2222AT3G	SOT-223 (Pb-Free)	4,000 Tape & Reel

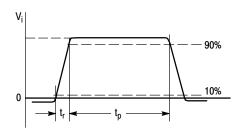
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
DFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (I _C = 10 mAdc, I _B = 0)	V _{(BR)CEO}	40	-	Vdc
Collector–Base Breakdown Voltage ($I_C = 10 \mu Adc, I_E = 0$)	V _{(BR)CBO}	75	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)	V _{(BR)EBO}	6.0	-	Vdc
Base-Emitter Cutoff Current (V _{CE} = 60 Vdc, V _{BE} = - 3.0 Vdc)	I _{BEX}	-	20	nAdd
Collector–Emitter Cutoff Current (V _{CE} = 60 Vdc, V _{BE} = - 3.0 Vdc)	I _{CEX}	-	10	nAdd
Emitter-Base Cutoff Current (V _{EB} = 3.0 Vdc, I _C = 0)	I _{EBO}	-	100	nAdo
Collector-Base Cutoff Current $(V_{CB} = 60 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 60 \text{ Vdc}, I_E = 0, T_A = 125^{\circ}\text{C})$	Ісво		10 10	nAdo μAdo
ON CHARACTERISTICS	•	•	•	
DC Current Gain $ \begin{aligned} &(I_C = 0.1 \text{ mAdc, } V_{CE} = 10 \text{ Vdc}) \\ &(I_C = 1.0 \text{ mAdc, } V_{CE} = 10 \text{ Vdc}) \\ &(I_C = 10 \text{ mAdc, } V_{CE} = 10 \text{ Vdc}) \\ &(I_C = 10 \text{ mAdc, } V_{CE} = 10 \text{ Vdc}) \\ &(I_C = 10 \text{ mAdc, } V_{CE} = 10 \text{ Vdc}) \\ &(I_C = 150 \text{ mAdc, } V_{CE} = 10 \text{ Vdc}) \\ &(I_C = 150 \text{ mAdc, } V_{CE} = 1.0 \text{ Vdc}) \\ &(I_C = 500 \text{ mAdc, } V_{CE} = 10 \text{ Vdc}) \end{aligned} $	h _{FE}	35 50 70 35 100 50 40	- - - - 300 -	-
Collector–Emitter Saturation Voltages ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)	V _{CE(sat)}	- -	0.3 1.0	Vdc
Base-Emitter Saturation Voltages ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)	V _{BE(sat)}	0.6	1.2 2.0	Vdc
Input Impedance $ (V_{CE}=10~Vdc,~I_C=1.0~mAdc,~f=1.0~kHz) \\ (V_{CE}=10~Vdc,~I_C=10~mAdc,~f=1.0~kHz) $	h _{ie}	2.0 0.25	8.0 1.25	kΩ
Voltage Feedback Ratio $(V_{CE} = 10 \text{ Vdc}, I_C = 1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$ $(V_{CE} = 10 \text{ Vdc}, I_C = 10 \text{ mAdc}, f = 1.0 \text{ kHz})$	h _{re}	- -	8.0x10 ⁻⁴ 4.0x10 ⁻⁴	-
Small–Signal Current Gain $(V_{CE} = 10 \text{ Vdc}, I_C = 1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$ $(V_{CE} = 10 \text{ Vdc}, I_C = 10 \text{ mAdc}, f = 1.0 \text{ kHz})$	h _{fe}	50 75	300 375	-
Output Admittance (V_{CE} = 10 Vdc, I_{C} = 1.0 mAdc, f = 1.0 kHz) (V_{CE} = 10 Vdc, I_{C} = 10 mAdc, f = 1.0 kHz)	h _{oe}	5.0 25	35 200	μmho
Noise Figure (V_{CE} = 10 Vdc, I_{C} = 100 μ Adc, f = 1.0 kHz)	F	-	4.0	dB
DYNAMIC CHARACTERISTICS				
Current-Gain - Bandwidth Product (I _C = 20 mAdc, V _{CE} = 20 Vdc, f = 100 MHz)	f _T	300	-	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _c	-	8.0	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}$, $I_{C} = 0$, $f = 1.0 \text{ MHz}$)	C _e	-	25	pF
SWITCHING TIMES (T _A = 25°C)				
Delay Time (V _{CC} = 30 Vdc, I _C = 150 mAdc,	t _d	_	10	ns
Rise Time I _{B(on)} = 15 mAdc, V _{EB(off)} = 0.5 Vdc) Figure 1	t _r	_	25	
· ·	t _s	_	225	ns
Storage Time $(V_{CC} = 30 \text{ Vdc}, I_C = 150 \text{ mAdc},$				



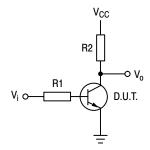
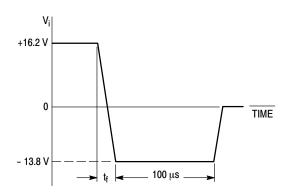


Figure 1. Input Waveform and Test Circuit for Determining Delay Time and Rise Time

 $\mbox{V}_{\mbox{\scriptsize i}}$ = - 0.5 V to +9.9 V, $\mbox{V}_{\mbox{\scriptsize CC}}$ = +30 V, R1 = 619 $\Omega,$ R2 = 200 $\Omega.$

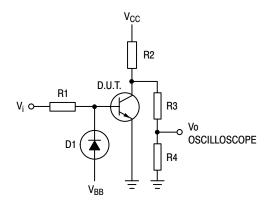


Figure 2. Input Waveform and Test Circuit for Determining Storage Time and Fall Time

TYPICAL CHARACTERISTICS

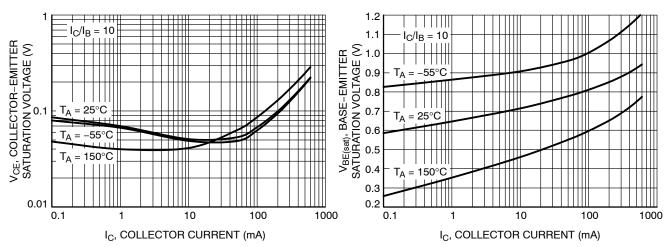


Figure 3. Collector Emitter Saturation Voltage vs. Collector Current

Figure 4. Base Emitter Saturation Voltage vs.
Collector Current

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TYPICAL CHARACTERISTICS

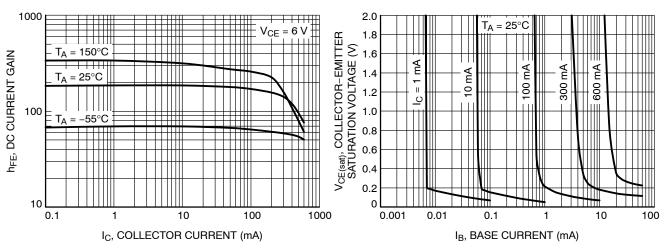


Figure 5. DC Current Gain vs. Collector Current

Figure 6. Saturation Region

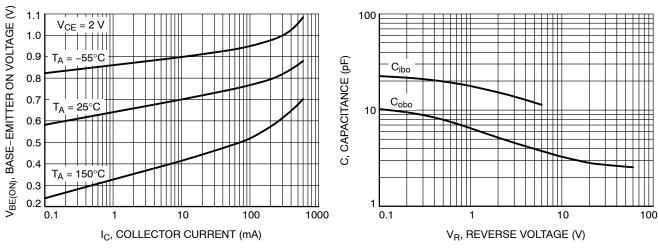


Figure 7. Base–Emitter Turn–On Voltage vs.
Collector Current

Figure 8. Capacitance

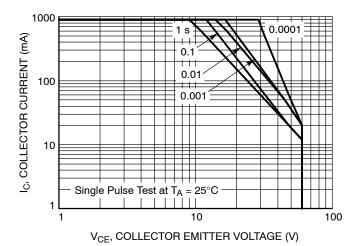


Figure 9. Safe Operating Area

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