Ultrafast Power Rectifier

6 A, 600 V

RURD660S9A-F085

The RURD660S9A-F085 is an ultrafast diode with soft recovery characteristics (trr < 83 ns). It has a low forward voltage drop and is of silicon nitride passivated ion-implanted epitaxial planar construction. This device is intended for use as a freewheeling/clamping diode and rectifier in a variety of switching power supplies and other power switching applications. Its low stored charge and ultrafast soft recovery minimize ringing and electrical noise in many power switching circuits, thus reducing powerloss in the switching transistors.

Features

- High Speed Switching ($t_{rr} = 63 \text{ ns (Typ.)} @ I_F = 6 \text{ A}$)
- Low Forward Voltage ($V_F = 1.26 \text{ V (Typ.)} @ I_F = 6 \text{ A}$)
- Avalanche Energy Rated
- AEC-Q101 Qualified and PPAP Capable
- This is a Pb-Free Device

Applications

- General Purpose
- Switching Mode Power Supply
- Power Switching Circuits

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit	
V_{RRM}	Peak Repetitive Reverse Voltage	600	V	
V_{RWM}	Working Peak Reverse Voltage	king Peak Reverse Voltage 600		
V _R	DC Blocking Voltage	oltage 600		
I _{F(AV)}	Average Rectified Forward Current @ $T_C = 25^{\circ}C$	6	Α	
I _{FSM}	Non-repetitive Peak Surge Current	60	Α	
T _J , T _{STG}	Operating Junction and Storage Temperature	– 55 to +175	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

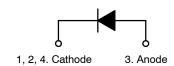
(9					
Symbol	Parameter	Max	Unit		
$R_{\theta JC}$	Maximum Thermal Resistance, Junction to Case	3	°C/W		
R _{θJA} (Note 1)	Maximum Thermal Resistance, Junction to Ambient	140	°C/W		
R _{θJA} (Note 2)	Maximum Thermal Resistance, Junction to Ambient	50	°C/W		

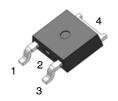
- Mounted on a minimum pad follow by JEDEC standard.
- 2. Mounted on a 1 in² pad of 2 oz copper follow by JEDEC standard.



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DPAK3 (TO-252 3 LD) CASE 369AS

MARKING DIAGRAM

\$Y&Z&3&K RUR660

RUR660 = Specific Device Code \$Y = ON Semiconductor Logo &Z = Assembly Plant Code

&3 = 3-Digit Date Code

&K = 2-Digits Lot Run Traceability Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

RURD660S9A-F085

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Condition		Min	Тур	Max	Unit
I _R	Instantaneous Reverse	V _R = 600 V	T _C = 25°C	-	-	100	μΑ
	Current		T _C = 175°	-	_	500	μΑ
V _{FM} (Note 3)	Instantaneous Forward Voltage	I _F = 6 A	T _C = 25°C T _C = 175°	- -	1.26 1.04	1.5 -	V V
t _{rr} (Note 4)	Reverse Recovery Time	I_F = 1 A, di/dt = 200 A/ μ s, V_{CC} = 390 V	T _C = 25°C	-	25	33	ns
		$I_F = 6 \text{ A, di/dt} = 200 \text{ A/}\mu\text{s, V}_{CC} = 390 \text{ V}$	T _C = 25°C T _C = 175°	- -	63 119	83 -	ns ns
t _a t _b Qrr	Reverse Recovery Time Reverse Recovery Charge	$I_F = 6 \text{ A}, \text{ di/dt} = 200 \text{ A/}\mu\text{s}, \text{ V}_{CC} = 390 \text{ V}$	T _C = 25°C	- - -	23 40 151	- - -	ns ns nC
W _{AVL}		Avalanche Energy (L = 20 mH)		10	-	-	mJ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 3. Pulse: Test Pulse width = 300 μs, Duty Cycle = 2%
- 4. Guaranteed by design

TEST CIRCUIT AND WAVEFORMS

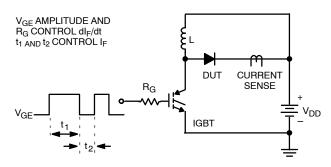


Figure 1. t_{rr} Test Circuit

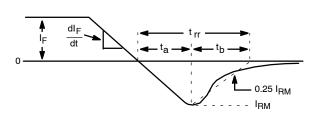


Figure 2. trr Waveforms and Definitions

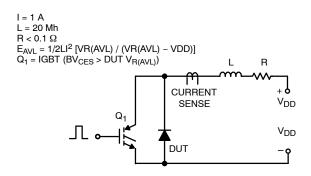


Figure 3. Avalanche Energy Test Circuit

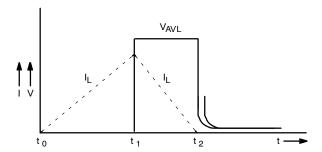
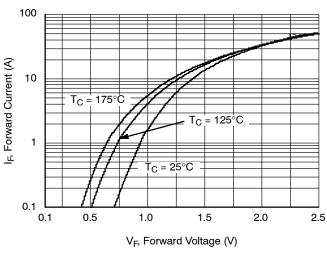


Figure 4. Avalanche Current and Voltage Waveforms

RURD660S9A-F085

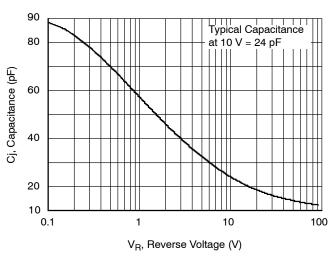
TYPICAL PERFORMANCE CHARACTERISTICS



100 T_C = 175°C IR, Forward Current (µA) 10 T_C = 125°C 1 0.1 T_C = 25°C 0.01 100 200 300 400 500 600 V_R, Reverse Voltage (V)

Figure 9. Typical Forward Voltage Drop vs. Forward Current

Figure 10. Typical Reverse Current vs. Reverse Voltage



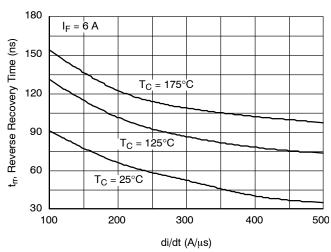
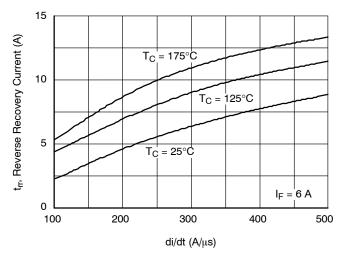


Figure 5. Typical Junction Capacitance

Figure 6. Typical Reverse Recovery Time vs. di/dt



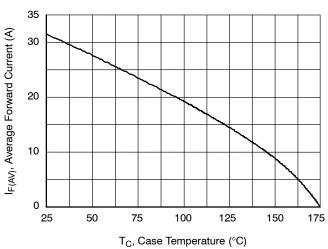


Figure 7. Typical Reverse Recovery Current vs. di/dt

Figure 8. Forward Current Derating Curve

RURD660S9A-F085

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

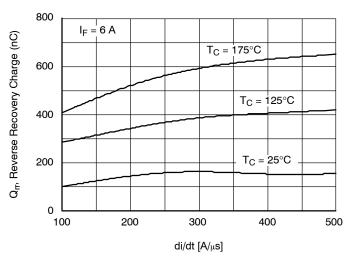


Figure 12. Reverse Recovery Charge

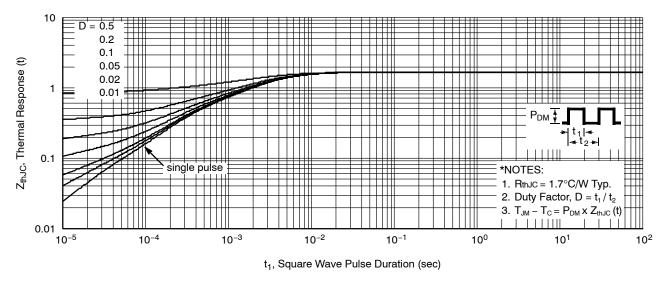


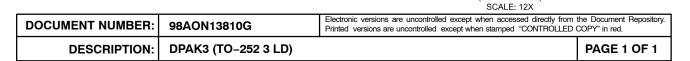
Figure 11. Transient Thermal Response Curve

ORDERING INFORMATION

Device	Device Marking	Package	Shipping [†]
RURD660S9A-F085	RUR660	TO-252 3 LD (Pb-Free)	2500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

DPAK3 (TO-252 3 LD) CASE 369AS **ISSUE O DATE 30 SEP 2016** 6.73 6.35 5,46 5.55 MIN-6.50 MIN 6.40 Ċ 0.25 MAX PLASTIC BODY STUB MIN DIODE PRODUCTS VERSION (0.59)-1.25 MIN 0.89 ⊕ 0.25 M AM C 2.29 2.28 4.56 4.57 LAND PATTERN RECOMMENDATION NON-DIODE PRODUCTS VERSION В 2.39 SEE 2.18 4.32 MIN **NOTE D** 0.58 0.45 5.21 MIN 10.41 9.40 SEE DETAIL A 2 3 NON-DIODE PRODUCTS VERSION DIODE PRODUCTS VERSION ○ 0.10 B 0,51 **GAGE PLANE** NOTES: UNLESS OTHERWISE SPECIFIED 0.61 0.45 A) THIS PACKAGE CONFORMS TO JEDEC, TO-252, (1.54)ISSUE C, VARIATION AA. B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONING AND TOLERANCING PER 10° ASME Y14.5M-2009. D) SUPPLIER DEPENDENT MOLD LOCKING HOLES OR CHAMFERED 1 78 CORNERS OR EDGE PROTRUSION.



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0.127 MAX

DETAIL A (ROTATED -90°)

SEATING PLANE

1,40

(2.90)

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ACGRB207-HF CLH03(TE16L,Q) ACGRC307-HF ACEFC304-HF NTE6356 NTE6359 NTE6002 NTE6023 NTE6039 NTE6077
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