# **SB05-05P**

# **Schottky Barrier Diode** 50V, 0.5A, Low IR, Single PCP



#### **Applications**

· High frequency rectification (switching regulators, converters, choppers)

#### **Features**

- Low forward voltage (VF max=0.55V)
- Fast reverse recovery time (t<sub>rr</sub> max=10ns)

- Low switching noise
- · Low leakage current and high reliability due to highly reliable planar structure

#### **Specifications**

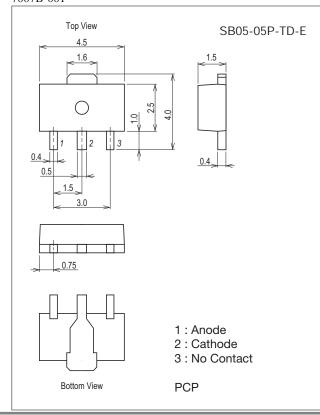
#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>		50	V
Nonrepetitive Peak Reverse Surge Voltage	V <sub>RSM</sub>		55	V
Average Output Current	IO		500	mA
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	5	А
Junction Temperature	Тј		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### Package Dimensions

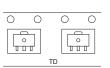
unit : mm (typ) 7007B-001



#### **Product & Package Information** : PCP

- Package
- JEITA. JEDEC : SC-62, SOT-89, TO-243
- Minimum Packing Quantity : 1,000 pcs./reel

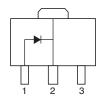
#### Packing Type: TD





Marking

#### **Electrical Connection**

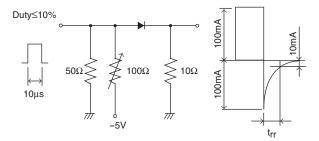


Semiconductor Components Industries, LLC, 2013 September, 2013

#### Electrical Characteristics at Ta=25°C

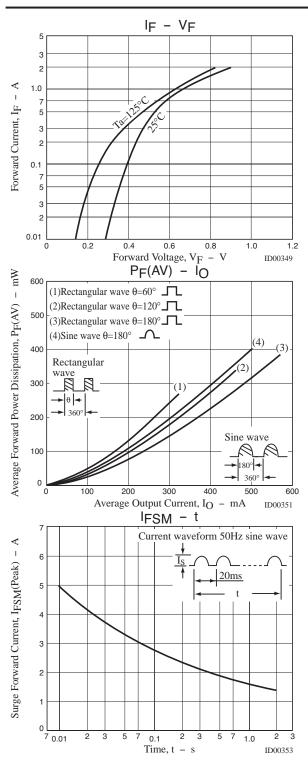
Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Reverse Voltage	VR	I <sub>R</sub> =200μA	50			V	
Forward Voltage	VF	IF=500mA			0.55	V	
Reverse Current	IR	V <sub>R</sub> =25V			50	μΑ	
Interterminal Capacitance	С	V <sub>R</sub> =10V, f=1MHz		18		рF	
Reverse Recovery Time	trr	IF=IR=100mA, See specified Test Circuit.			10	ns	
Thermal Resistance	Rth(j-a)1			310		°C / W	
	Rth(j-a)2	When mounted on ceramic substrate (250mm <sup>2</sup> ×0.8mm)		120		°C / W	

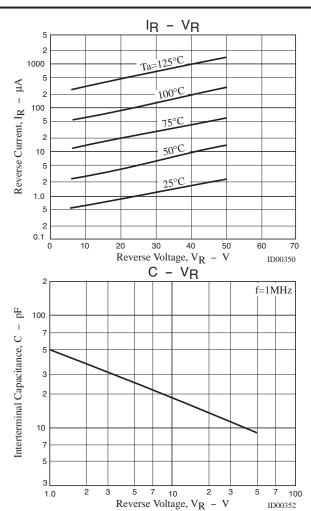
### trr Test Circuit



#### **Ordering Information**

Device	Package	Shipping	memo		
SB05-05P-TD-E	B05-05P-TD-E PCP		Pb Free		





#### Embossed Taping Specification SB05-05P-TD-E

1. Packing Format

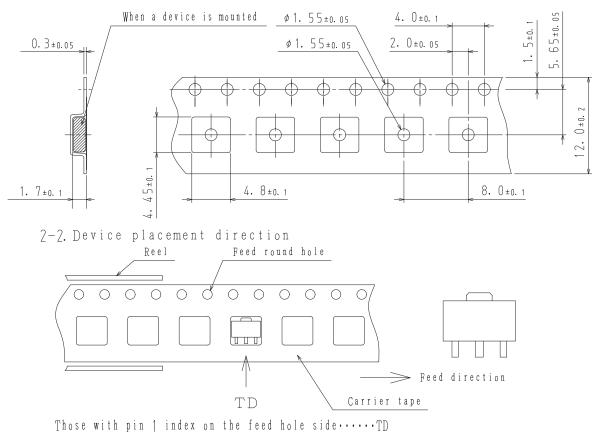
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format			
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)		(C-1)	Outer BOX (A-7)
РСР	PCP	1,000	4,000	24,000	4 ree	ls containe	d	6 inner boxes contained
					Dimensions:mm (external)		external)	Dimensions:mm (external)
					18	$3 \times 72 \times$	185	440×195×210
Packing met	hod			(u 1	nit:r	nm)	The form	label at the time of factory shipments, n of a label may change in physical ution process.
°			<	(	59	~	<	108
	Type LOT Quan Orig Reel la	No. tity in	-> (1' -> (2' (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z)	11 11111 111111 111111 17 LOT 00 11 11111 11111111111111111111111111	() () () () () () () () () () () () () (		0 1 1 5 5 1 1 1 1 1 1 1 1 1 1 1 1 1	************************************
				Label		JEITA	-	
				LEAD FRE	EE 3	JEITA P	hase 3A	

LEAD FREE 4

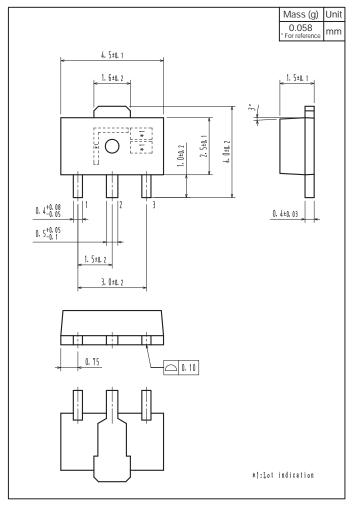
JEITA Phase 3

## 2. Taping configuration

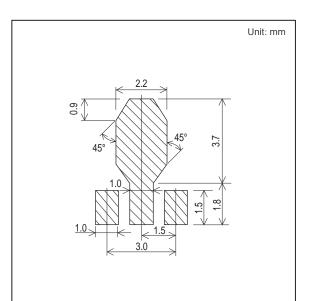
2-1. Carrier tape size (unit:mm)



#### Outline Drawing SB05-05P-TD-E



#### Land Pattern Example



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