

T2500D



Description

Designed primarily for full-wave ac control applications, such as light dimmers, motor controls, heating controls and power supplies.

Features

- Blocking Voltage 400 V
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability

• High Surge Current Capability 60 A Peak at T = 80°C

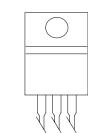
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• Pb-Free Package is Available

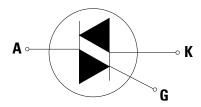
Pin Out



Style 4



Functional Diagram



Additional Information







Samples



Maximum Ratings (T = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Note 1) (Sine Wave 50 to 60 Hz, $T_{\rm J}$ = -40 to +100°C, Gate Open)	V _{drm} , V _{rrm}	400	V
On-State RMS Current (Full Cycle Sine Wave 50 to 60 Hz) (T _c = +80°C)	I _{T (RMS)}	6.0	A
Peak Non-Repetitive Surge Current (One Full Cycle, Sine Wave 60 Hz, T _c = +80°C)	I _{TSM}	60	A
Circuit Fusing Considerations (t = 8.3 ms)	l²t	15	A2s
Peak Gate Power (Pulse Width = 10 μ sec, T _c = +80°C)	P _{GM}	16	W
Average Gate Power (t = 8.3 msec, $T_c = +80^{\circ}$ C)	P _{GM (AV)}	0.2	W
Peak Gate Current (Pulse Width = 10 µsec)	I _{GM}	4.0	A
Operating Junction Temperature Range @ Rated $\rm V_{_{RRM}}$ and $\rm V_{_{DRM}}$	T	-40 to +125	°C
Storage Temperature Range	T _{stg}	-40 to +150	°C

Thermal Characteristics

Rating	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{suc}	2.7	°C/W
Maximum Device Temperature for Soldering Purposes for 10 Sec	TL	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.
1. V_{DBM} and V_{RBM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

Electrical Characteristics - OFF (T _c = 25°C unless otherwise noted)							
Characteristic		Symbol	Min	Тур	Max	Unit	
†Peak Repetitive Blocking Current	T_ = 25°C	I _{DBM} ,	-	-	10	μA	
$(V_{AK} = V_{DRM} = V_{RRM}; \text{ Gate Open})$	T_ = 100°C	I _{RRM}	-	-	2.0	mA	

Electrical Characteristics - **ON** ($T_c = 25^{\circ}C$ unless otherwise noted; Electricals apply in both directions)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Forward On-State Voltage (Note 3) $(I_{TM} = \pm 30 \text{ A})$		V _{TM}	-	-	2.0	V
	MT2(+), G(+)		-	10	25	
Gate Trigger Current (Continuous dc)	MT2(+), G(-)		_	20	60	
$(V_{\rm D} = 12 \text{ V}, \text{ R}_{\rm L} = 100 \Omega)$	MT2(-), G(-)	GT	_	15	25	mA
	MT2(-), G(+)		_	30	60	
Gate Trigger Voltage (Continuous dc) (All Four Quadrants) (V $_{\rm D}$ = 12 Vdc, R $_{\rm L}$ = 100 Ω)		V _{gt}	-	1.25	2.5	V
Gate Non–Trigger Voltage (Continuous dc) ($V_p = 12 \text{ Vdc}, R_L = 100 \Omega, T_c = 100^{\circ}\text{C}$)		V _{gD}	0.2	-	-	V
Holding Current (Main Terminal Voltage = 12 Vdc, Gate Open , Initiating Current) = ±200 mA)		I _H	-	15	30	mA
Gate Controlled Turn-On Time (Rated $V_{DRM'}$ I _T = 10 A , I _{GT} = 160 mA, Rise Time = 0.1 µs)		t _{gt}	-	1.6	-	μs

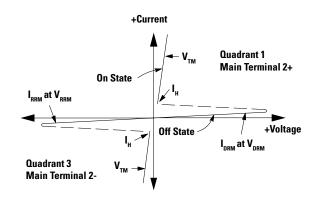
Dynamic Characteristics					
Characteristic	Symbol	Min	Тур	Мах	Unit
Critical Rate of Rise of Off-State Voltage ($V_{D} = 0.66 \times V_{DRM'}$ Exponential Waveform, Gate Open, $T_{J} = 100^{\circ}$ C)	dV/dt	-	50	-	V/µs

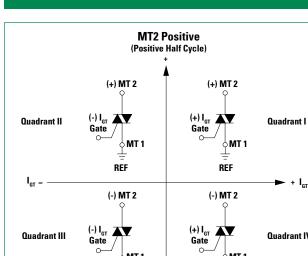
Characteristic	Symbol	Min	Тур	Мах	Unit
Critical Rate-of-Rise of Commutation Voltage $(V_D = Rated V_{DRM}, I_{T(RMS)} = 6 A, Commutating di/dt = 3.2 A/ms, Gate Unenergized, T_c = 80°C)$	dv/dt	-	10	-	V/µs
Critical Rate-of-Rise of Off-State Voltage (V_{D} = Rated V_{DRM} , Exponential Voltage Rise, Gate Open, T _c = 100°C)	di/dt	-	75	_	A/µs

2. Pulse Test: Pulse Width \leq 2.0 ms, Duty Cycle \leq 2%.

Voltage Current Characteristic of SCR

Symbol	Parameter
V _{drm}	Peak Repetitive Forward Off State Voltage
I _{DRM}	Peak Forward Blocking Current
V _{RRM}	Peak Repetitive Reverse Off State Voltage
I _{RRM}	Peak Reverse Blocking Current
V _{TM}	Maximum On State Voltage
I _H	Holding Current





Quadrant Definitions for a Triac

Quadrant I + I_{GT} Quadrant IV **∲MT** 1 ↓MT1 REF REF

MT2 Negative (Negative Half Cycle)

All Polarities are referenced to MT1.

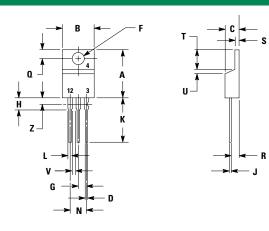
With in-phase signals (using standard AC lines) quadrants I and III are used



Part Marking System

Ordering Information

Dimensions



TO-220AB Case 221A Style 3 YMAXX T2500DG Y =Year M =Month =Assembly Site =Diode Polarity =Pb-Free Package A AKA G

D:	Inc	hes	Millin	neters
Dim	Min	Max	Min	Max
Α	0.590	0.620	14.99	15.75
В	0.380	0.420	9.65	10.67
С	0.178	0.188	4.52	4.78
D	0.025	0.035	0.64	0.89
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.41	2.67
Н	0.110	0.130	2.79	3.30
J	0.018	0.024	0.46	0.61
К	0.540	0.575	13.72	14.61
L	0.060	0.075	1.52	1.91
N	0.195	0.205	4.95	5.21
٥	0.105	0.115	2.67	2.92
R	0.085	0.095	2.16	2.41
S	0.045	0.060	1.14	1.52
т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045		1.15	
Z		0.080		2.04

Pin Assignment			
1 Cathode			
2	Anode		
3	Gate		
4	Anode		
	·		

Device	Package	Shipping
T2500D	TO220AB	
T2500DG	TO220AB (Pb-Free)	500 Units/Box

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
 DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

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 2N6075A

 NTE5629
 NTE5688
 CTB08-400CW
 D31410
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 BT134-800E
 BT136D

 BTB16Q-600BW
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