ON Semiconductor

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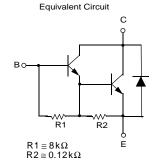
ON Semiconductor®

TIP120 / TIP121 / TIP122 NPN Epitaxial Darlington Transistor

Features

- Medium Power Linear Switching Applications
- Complementary to TIP125 / TIP126 / TIP127





1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	Top Mark	Package	Packing Method
TIP120	TIP120	TO-220 3L (Single Gauge)	Bulk
TIP120TU	TIP120	TO-220 3L (Single Gauge)	Rail
TIP121	TIP121	TO-220 3L (Single Gauge)	Bulk
TIP121TU	TIP121	TO-220 3L (Single Gauge)	Rail
TIP122	TIP122	TO-220 3L (Single Gauge)	Bulk
TIP122TU	TIP122	TO-220 3L (Single Gauge)	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value	Unit
		TIP120	60	
V_{CBO}	Collector-Base Voltage	TIP121	80	V
		TIP122	100	
V _{CEO}	Collector-Emitter Voltage	TIP120	60	
		TIP121	80	V
		TIP122	100	
V _{EBO}	Emitter-Base Voltage	<u>.</u>	5	V
I _C	Collector Current (DC)		5	Α
I _{CP}	Collector Current (Pulse)		8	А
Ι _Β	Base Current (DC)		120	mA
T _J	Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-65 to 150	°C

Thermal Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
P _C	Collector Dissipation (T _A = 25°C)	2	- V
	Collector Dissipation (T _C = 25°C)	65	

Electrical Characteristics

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage	TIP120	I _C = 100 mA, I _B = 0	60		V
		TIP121		80		
		TIP122		100		
I _{CEO}	Collector Cut-Off Current	TIP120	$V_{CE} = 30 \text{ V}, I_{B} = 0$		0.5	mA
		TIP121	$V_{CE} = 40 \text{ V}, I_{B} = 0$		0.5	
		TIP122	$V_{CE} = 50 \text{ V}, I_{B} = 0$		0.5	
	Collector Cut-Off Current	TIP120	$V_{CB} = 60 \text{ V}, I_{E} = 0$		0.2	mA
I _{CBO}		TIP121	$V_{CB} = 80 \text{ V}, I_{E} = 0$		0.2	
		TIP122	$V_{CB} = 100 \text{ V}, I_{E} = 0$		0.2	
I _{EBO}	Emitter Cut-Off Current		$V_{EB} = 5 \text{ V}, I_{C} = 0$		2	mA
h _{FE}	DC Current Gain ⁽¹⁾		$V_{CE} = 3 \text{ V, } I_{C} = 0.5 \text{ A}$	1000		
			$V_{CE} = 3 \text{ V}, I_{C} = 3 \text{ A}$	1000		
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽¹⁾		$I_C = 3 \text{ A}, I_B = 12 \text{ mA}$		2.0	V
			$I_C = 5 \text{ A}, I_B = 20 \text{ mA}$		4.0	v
V _{BE} (on)	Base-Emitter On Voltage ⁽¹⁾		$V_{CE} = 3 \text{ V}, I_{C} = 3 \text{ A}$		2.5	V
C _{ob}	Output Capacitance		V _{CB} = 10 V, I _E = 0, f = 0.1 MHz		200	pF

Note:

1. Pulse test: $pw \le 300 \mu s$, duty cycle $\le 2\%$.

Typical Performance Characteristics

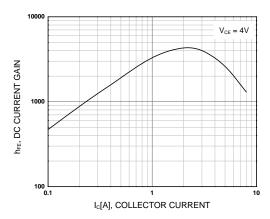
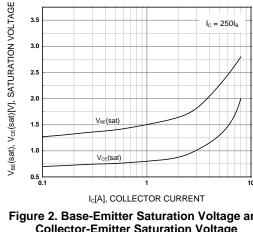


Figure 1. DC Current Gain



I_C = 250I_R

3.5

Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

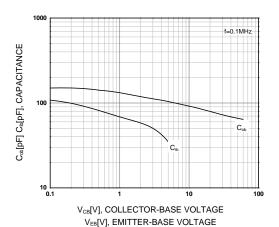


Figure 3. Output and Input Capacitance vs. Reverse Voltage

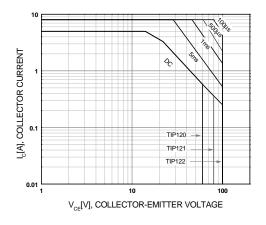


Figure 4. Safe Operating Area

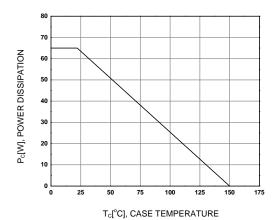
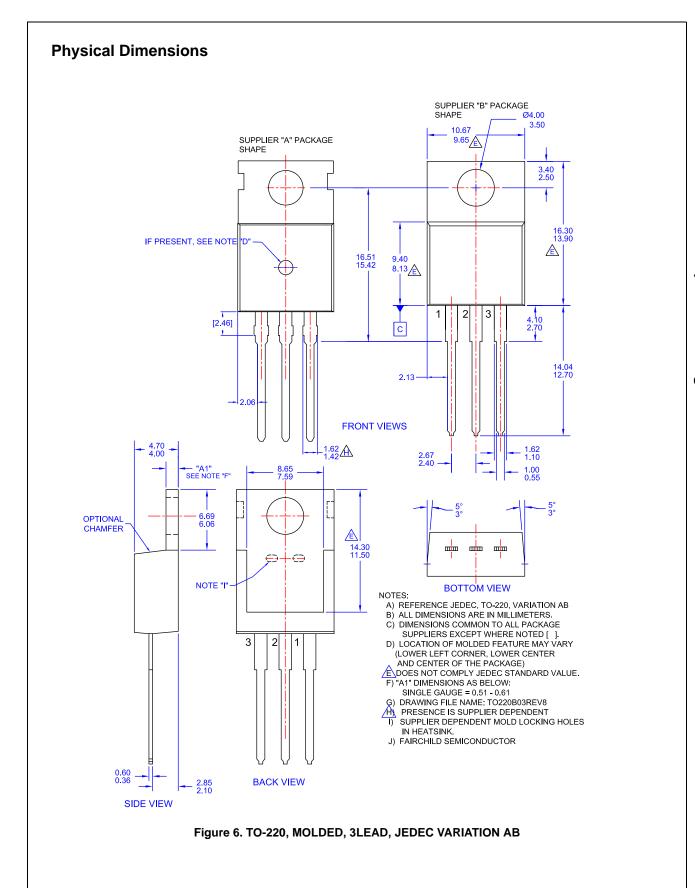


Figure 5. Power Derating



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