

December 2009

# TIP42/TIP42A/TIP42B/TIP42C PNP Epitaxial Silicon Transistor

#### **Features**

- Medium Power Linear Switching Applications
- Complement to TIP41/TIP41A/TIP41B/TIP41C



1.Base 2.Collector 3.Emitter

## **Absolute Maximum Ratings** $T_A=25$ °C unless otherwise noted

Symbol	Parameter	Value Uni	Units	
V <sub>CBO</sub>	Collector-Base Voltage : TIP42 : TIP42A : TIP42B : TIP42C	- 40 V - 60 V - 80 V - 100 V	,	
V <sub>CEO</sub>	Collector-Emitter Voltage : TIP42 : TIP42A : TIP42B : TIP42C	- 40 V - 60 V - 80 V - 100 V	,	
V <sub>EBO</sub>	Emitter-Base Voltage	- 5 V	,	
I <sub>C</sub>	Collector Current (DC)	- 6 A		
I <sub>CP</sub>	Collector Current (Pulse)	-10 A		
Ι <sub>Β</sub>	Base Current	-2 A		
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	65 W	1	
	Collector Dissipation (T <sub>A</sub> =25°C)	2 W	/	
TJ	Junction Temperature	150 °C	;	
T <sub>STG</sub>	Storage Temperature	- 65 to 150 °C	;	

# $\textbf{Electrical Characteristics} \ \, \textbf{T}_{A} \text{=-} 25^{\circ} \text{C unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage : TIP42 : TIP42A : TIP42B : TIP42C	$I_C = -30 \text{mA}, I_B = 0$	-40 -60 -80 -100		V V V
I <sub>CEO</sub>	Collector Cut-off Current : TIP42/42A : TIP42B/42C	$V_{CE} = -30V, I_{B} = 0$ $V_{CE} = -60V, I_{B} = 0$		-0.7 -0.7	mA mA
I <sub>CES</sub>	Collector Cut-off Current : TIP42 : TIP42A : TIP42B : TIP42C	$V_{CE} = -40V, V_{EB} = 0$ $V_{CE} = -60V, V_{EB} = 0$ $V_{CE} = -80V, V_{EB} = 0$ $V_{CE} = -100V, V_{EB} = 0$		-400 -400 -400 -400	μΑ μΑ μΑ μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		-1	mA
h <sub>FE</sub>	* DC Current Gain	$V_{CE} = -4V, I_{C} = -0.3A$ $V_{CE} = -4V, I_{C} = -3A$	30 15	75	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	$I_C = -6A, I_B = -600mA$		-1.5	V
V <sub>BE</sub> (sat)	* Base-Emitter Saturation Voltage	$V_{CE} = -4V, I_{C} = -6A$		-2.0	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -500mA,$ f = 1MHz	3.0		MHz

<sup>\*</sup> Pulse Test: PW≤300μs, Duty Cycle≤2%

## **Typical Performance Characteristics**

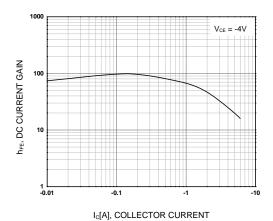


Figure 1. DC current Gain

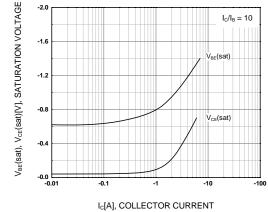


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

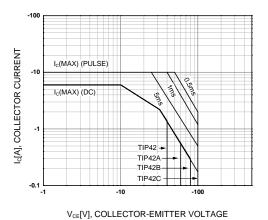


Figure 3. Safe Operating Area

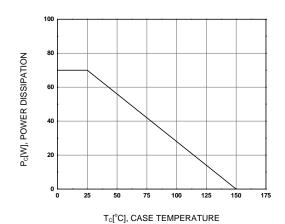
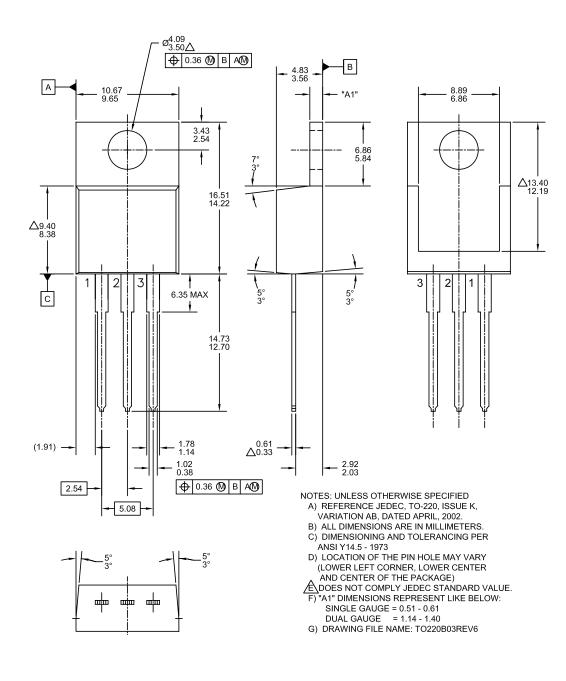


Figure 4. Power derating

### **Mechanical Dimensions**

# TO-220



Dimensions in Millimeters





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