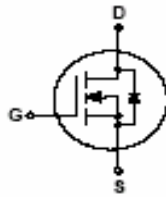
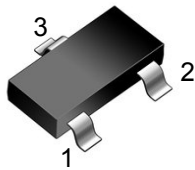


**SOT-23**

**MARKING:** S28

**Features**

Advanced trench process technology  
 High density cell design for Ultra Low On-Resistance  
 Halogen free and RoHS compliant

**Mechanical Data**

SOT-23 Small Outline Plastic Package  
 EpoxyUL:94V-0

**Maximum Ratings & Thermal Characteristics**

(Ratings at 25°C ambient temperature unless otherwise specified.)

Symbol	Parameter	Rating	Unit	
V <sub>DS</sub>	Drain-Source Breakdown Voltage	100	V	
V <sub>GS</sub>	Gate-Source Voltage	±20	V	
T <sub>J</sub>	Maximum Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature Range	-50 to 155	°C	
I <sub>S</sub>	Diode Continuous Forward Current	T <sub>c</sub> =25°C	2	A
I <sub>DM</sub>	Pulse Drain Current Tested	T <sub>c</sub> =25°C	9	A
I <sub>D</sub>	Continuous Drain Current@GS=10V	T <sub>c</sub> =25°C	2	A
P <sub>D</sub>	Maximum Power Dissipation	T <sub>c</sub> =25°C	1.3	W
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient(( <sup>*</sup> 1 in2 Pad of 2-oz Copper), Max.)	96	°C/W	

**Electrical Characteristics**

(Ratings at 25°C ambient temperature unless otherwise specified.)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
BV(BR) <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	--	--	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.8	2.5	V
R <sub>DS(on)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =2A	--	250	280	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =1A	--	280	310	mΩ

**Dynamic Electrical Characteristics**

C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz	--	387	--	pF
C <sub>OSS</sub>	Output Capacitance		--	30	--	pF
C <sub>RSS</sub>	Reverse Transfer Capacitance		--	28	--	pF

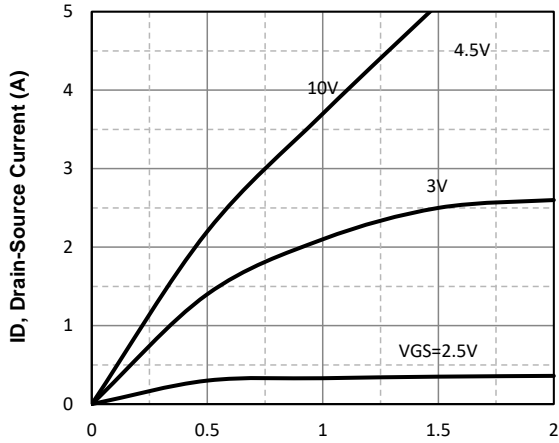
**Switching Characteristics**

Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =50V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V	--	9.5	--	nC
Q <sub>gs</sub>	Gate Source Charge		--	1.8	--	nC
Q <sub>gd</sub>	Gate Drain Charge		--	2	--	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =50V, I <sub>D</sub> =1.3A, V <sub>GS</sub> =10V, R <sub>G</sub> =1Ω	--	4	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	17.5	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	13	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	28	--	nS

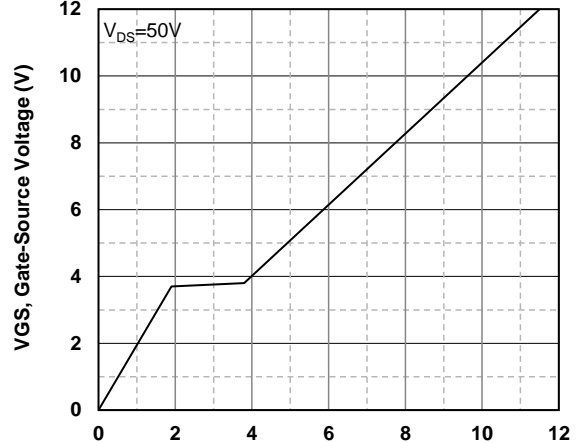
**Source- Drain Diode Characteristics**

V <sub>SD</sub>	Forward on voltage	T <sub>j</sub> =25°C, I <sub>s</sub> =2A,	--	--	1.2	V
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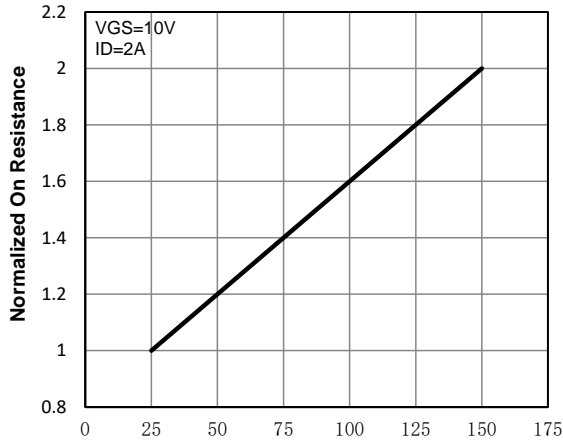
# Ratings and Characteristic Curves



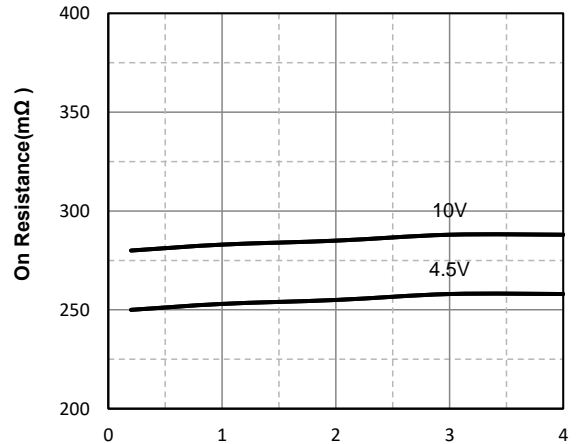
VDS, Drain -Source Voltage (V)  
Fig1. Typical Output Characteristics



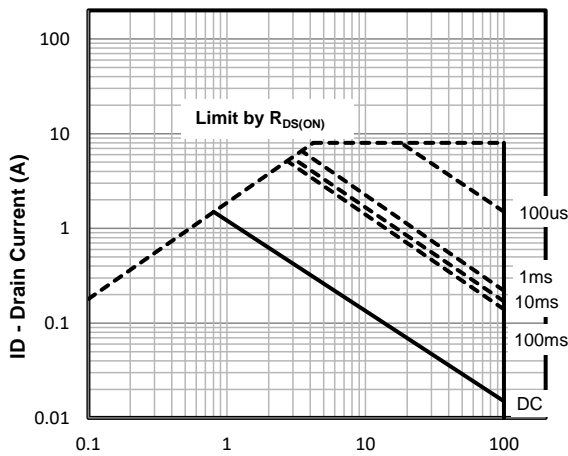
Qg -Total Gate Charge (nC)  
Fig2. Typical Gate Charge Vs. Gate-Source Voltage



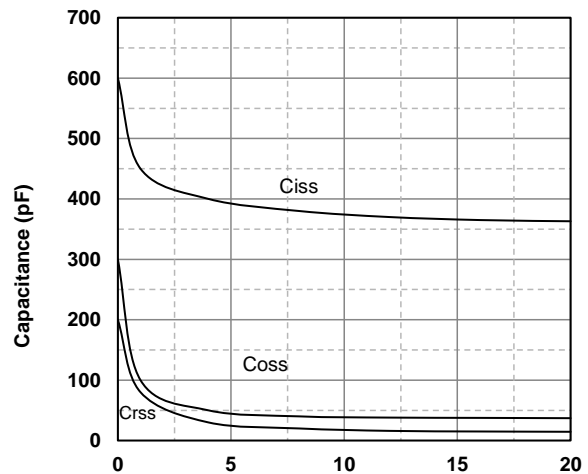
Tj - Junction Temperature (°C)  
Fig3. Normalized On-Resistance Vs. Temperature



ID, Drain-Source Current (A)  
Fig4. On-Resistance Vs. Drain-Source Current



VDS, Drain -Source Voltage (V)  
Fig5. Maximum Safe Operating Area

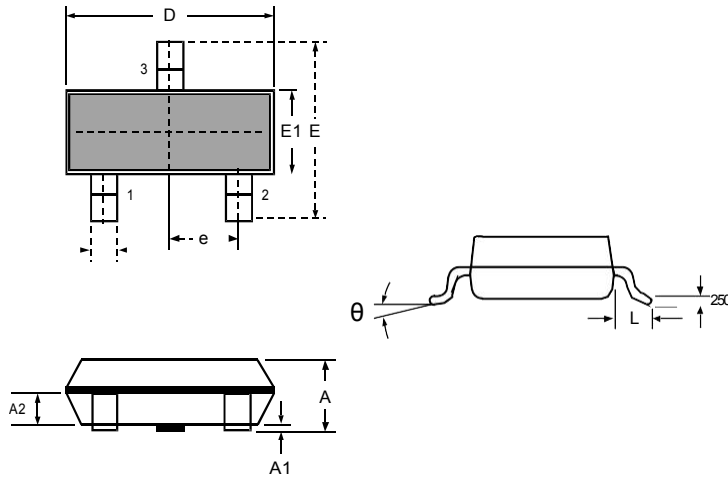


VDS, Drain-Source Voltage (V)  
Fig6 Typical Capacitance Vs. Drain-Source Voltage

### Summary of Packing Options

Package	Packing Description	Packing Quantity	Industry Standard
SOT-23	Tape/Reel, 7" reel	3000	EIA-481-1

### Package Outline Dimensions: SOT-23



DIMENSIONS

SYMBOL	MILLIMETER		INCHES	
	MIN	MAX	MIN	MAX
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
D	2.800	3.000	0.110	0.118
b	0.300	0.500	0.012	0.020
E	2.250	2.550	0.089	0.100
E1	1.200	1.400	0.047	0.055
e	0.950 BSC		0.037 BSC	
L	0.300	0.500	0.012	0.020
θ	0	8°	0	8°

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