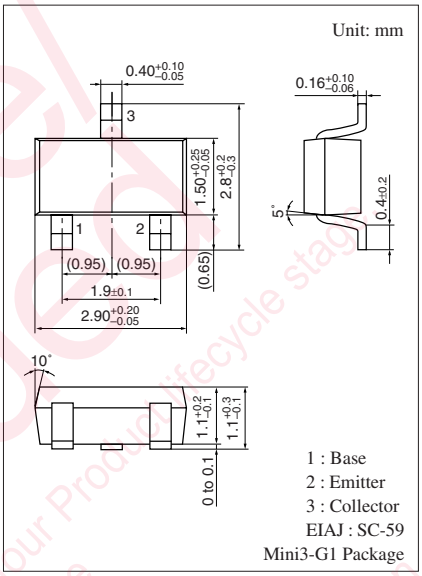
2SA2046

Silicon PNP epitaxial planar type

For DC-DC converter



Marking Symbol: 3Z

■ Features

- Low collector-emitter saturation voltage $V_{CE(sat)}$
- Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing.

■ Absolute Maximum Ratings $T_a = 25°C$

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	−30	V
Collector-emitter voltage (Base open)	V_{CEO}	−20	V
Emitter-base voltage (Collector open)	V_{EBO}	−5	V
Collector current	I_C	−1.5	A
Peak collector current	I_{CP}	−5	A
Collector power dissipation *	P_C	400	mW
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	−55 to +150	°C

Note) ∗: Measure on the ceramic substrate at 15 mm × 15 mm × 0.6 mm

■ Electrical Characteristics $T_a = 25°C \pm 3°C$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = -10$ µA, $I_E = 0$	−30			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = -1$ mA, $I_B = 0$	−20			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = -10$ µA, $I_C = 0$	−5			V
Forward current transfer ratio *	h_{FE}	$V_{CE} = -2$ V, $I_C = -100$ mA	160		560	—
Collector-emitter saturation voltage *	$V_{CE(sat)}$	$I_C = -500$ mA, $I_B = -25$ mA		−50	−150	mV
Transition frequency	f_T	$V_{CB} = -10$ V, $I_E = 20$ mA, f = 200 MHz		170		MHz
Collector output capacitance (Common base, input open circuited)	C_{ob}	$V_{CB} = -10$ V, $I_E = 0$, f = 1 MHz		25	35	pF

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.
2. ∗: Pulse measurement

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