

# Schottky Barrier Diode DB2W40200L

DB2W40200L Silicon epitaxial planar type

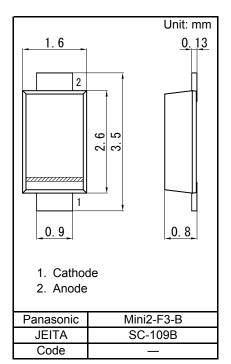
### For rectification

### Features

- Low forward voltage VF
- Forward current (Average) IF(AV) = 2 A rectification is possible
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: 42

### Packaging

Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)



# Unit Internal Connection V A A 2 A 2 C Internal Connection

#### ■ Absolute Maximum Ratings Ta = 25 °C

	-		
Parameter	Symbol	Rating	Unit
Reverse voltage	VR	40	V
Maximum peak reverse voltage	VRM	40	V
Forward current <sup>*1</sup>	IF	2.0	Α
Non-repetitive peak forward surge current *2	IFSM	30	А
Junction temperature <sup>*1</sup>	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	°C

Note: \*1 TI = 80 ° C

\*2 50 Hz sine wave 1 cycle (Non-repetitive peak current)



## **Panasonic**

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#### ■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	VF	IF = 2.0 A			0.47	V
Reverse current	IR	VR = 40 V			250	μA
Terminal capacitance	Ct	VR = 10 V, f = 1 MHz		50		pF
Reverse recovery time *1	trr	IF = IR = 100 mA, Irr = 10 mA		15		ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 Measuring methods for Diodes.2. This product is sensitive to electric shock (static electricity, etc.). Due attention must be paid on

the charge of a human body and the leakage of current from the operating equipment.

3. \*1 trr test circuit

Bias Application Unit (N-50BU)

Input Pulse

 $\frac{t_p}{10\%}$ 

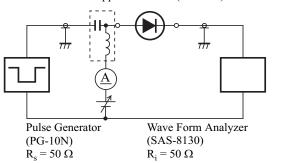
90%

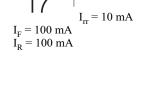
 $\begin{array}{l} t_p = 2 \ \mu s \\ t_r = 0.35 \ ns \\ \delta = 0.05 \end{array}$ 

V<sub>R</sub>

Output Pulse

I<sub>F</sub>

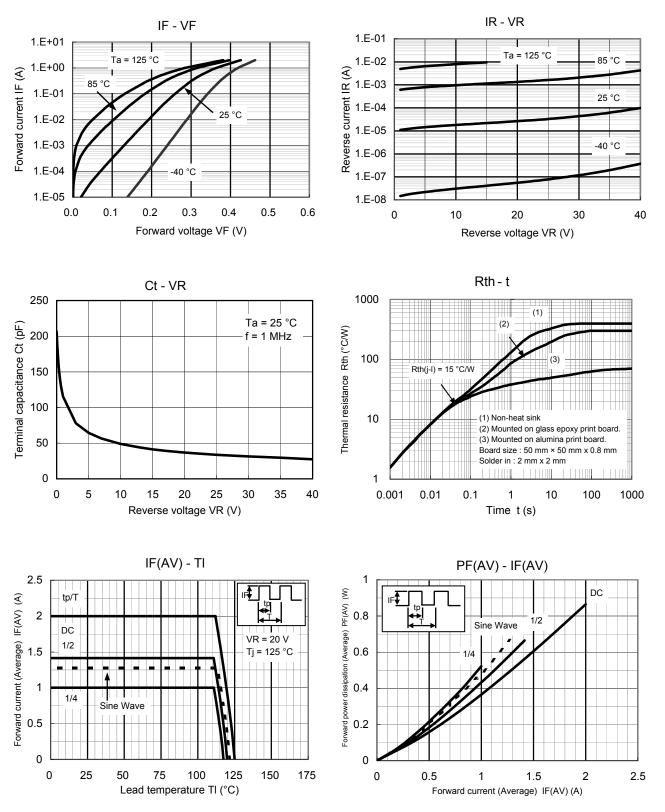






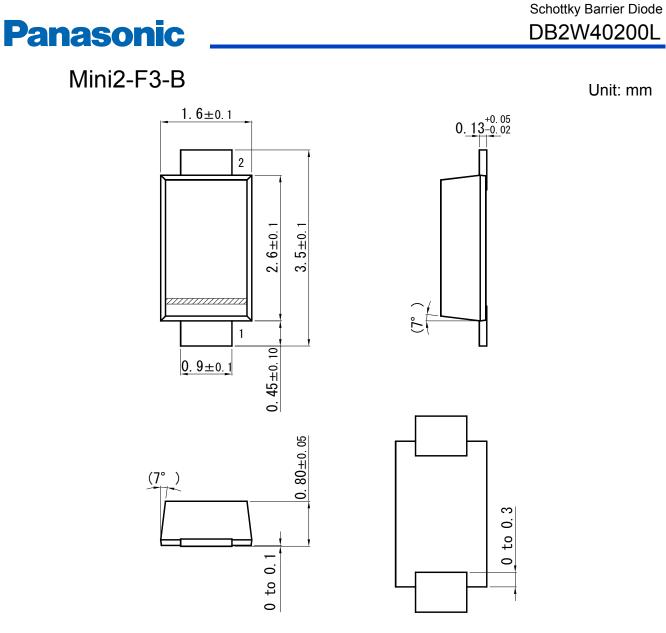
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### Technical Data (reference)

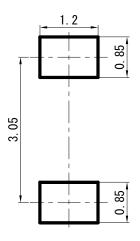


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Established : 2012-03-02 Revised : 2013-04-27







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