Schottky Barrier Diode

DB2X20700L

# **Panasonic**

# DB2X20700L

Silicon epitaxial planar type

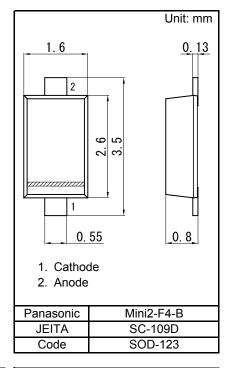
For high frequency rectification DB3X207K in Mini2 type package

#### ■ Features

- · Low forward voltage VF
- Forward current (Average) IF(AV) = 1 A rectification is possible
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: AA

#### ■ Packaging

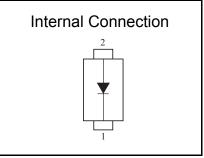
Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)



### ■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Reverse voltage	VR	20	V
Repetitive peak reverse voltage	VRRM	20	V
Forward current (Average) *1	IF(AV)	1	Α
Non-repetitive peak forward surge current *2	IFSM	7	Α
Junction temperature	Tj	125	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +125	°C

Note: \*1 For embedded alumina substrate



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<sup>\*2 50</sup> Hz sine wave 1 cycle (Non-repetitive peak current)

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### DB2X20700L

### ■ Electrical Characteristics Ta = 25 °C ± 3 °C

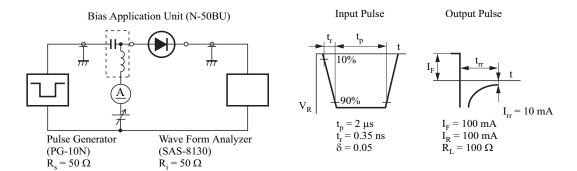
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	VF	IF = 1.0 A			0.4	V
Reverse current	IR	VR = 6 V			1.5	mA
Terminal capacitance	Ct	VR = 10 V, f = 1 MHz		43		pF
Reverse recovery time *1	trr	IF = IR = 100 mA, Irr = 10 mA, RL = 100 $\Omega$		12		ns

- Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 Measuring methods for Diodes.
  - 2. This product is sensitive to electric shock (static electricity, etc.). Due attention must be paid on the charge of a human body and the leakage of current from the operating equipment.
  - 3. \*1 trr test circuit

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Revised

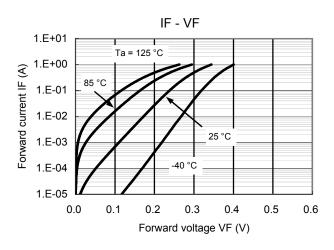


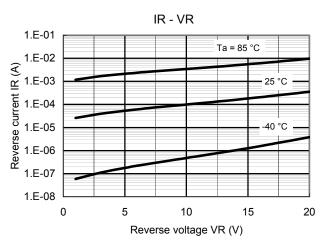
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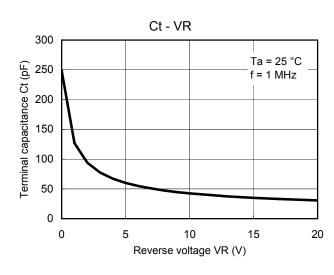
Schottky Barrier Diode

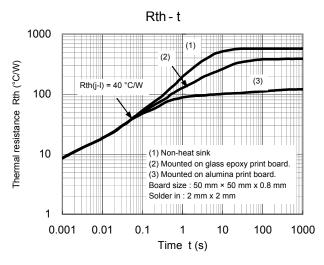
DB2X20700L

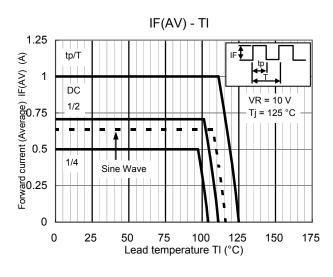
### Technical Data (reference)

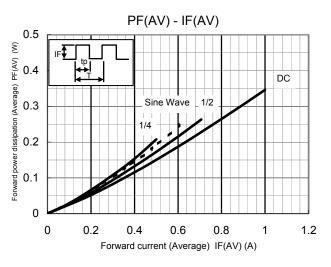








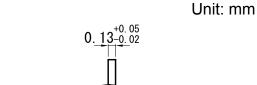


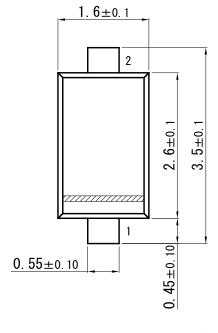


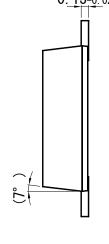
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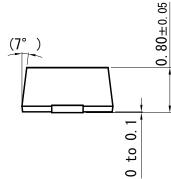
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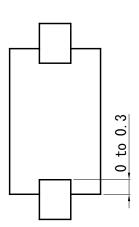
Mini2-F4-B



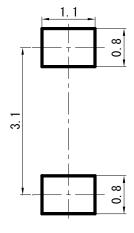








# ■ Land Pattern (Reference) (Unit: mm)



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