## **DMC204C0**

### Silicon NPN epitaxial planar type

For low frequency amplification

#### ■ Features

- $\bullet$  High forward current transfer ratio  $h_{\text{FE}}$  with excellent linearity
- $\bullet$  Low collector-emitter saturation voltage  $V_{\text{CE(sat)}}$
- Halogen-free / RoHS compliant
   (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

#### ■ Marking Symbol: D5

#### ■ Basic Part Number

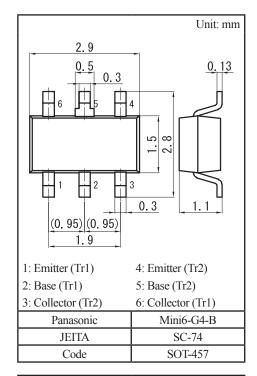
Dual DSC2C01 (Individual)

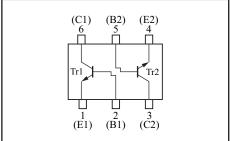
#### Packaging

DMC204C00R Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

	Parameter	Symbol	Rating	Unit
Tr1 Tr2	Collector-base voltage (Emitter open)	V <sub>CBO</sub>	100	V
	Collector-emitter voltage (Base open)	V <sub>CEO</sub>	100	V
	Emitter-base voltage (Collector open)	V <sub>EBO</sub>	15	V
	Collector current	$I_{C}$	20	mA
	Peak collector current	I <sub>CP</sub>	50	mA
Overall	Total power dissipation	$P_{T}$	300	mW
	Junction temperature	T <sub>j</sub>	T <sub>j</sub> 150	
	Operating ambient temperature	T <sub>opr</sub> -40 to +85		°C
	Storage temperature	T <sub>stg</sub>	-55 to +150	°C

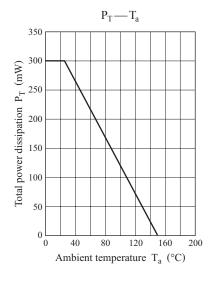


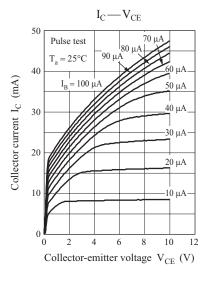


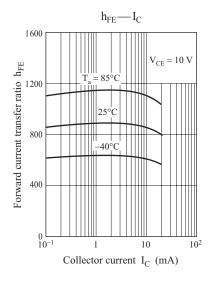
#### ■ Electrical Characteristics $T_a = 25$ °C±3°C

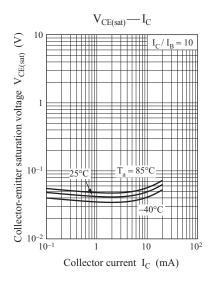
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	$V_{CBO}$	$I_C = 10 \mu A, I_E = 0$	100			V
Collector-emitter voltage (Base open)	$V_{CEO}$	$I_C = 1 \text{ mA}, I_B = 0$	100			V
Emitter-base voltage (Collector open)	$V_{\mathrm{EBO}}$	$I_E = 10 \mu A, I_C = 0$	15			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = 60 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = 60 \text{ V}, I_{B} = 0$			1	μΑ
Forward current transfer ratio	h <sub>FE</sub>	$V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$	400		1200	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 10 \text{ mA}, I_B = 1 \text{ mA}$		0.05	0.20	V
Transition frequency	$f_T$	$V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$		140		MHz

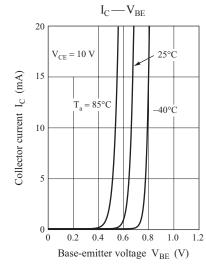
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

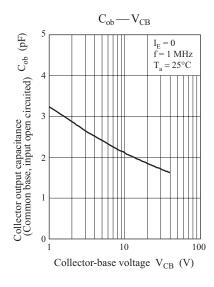


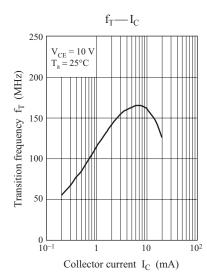








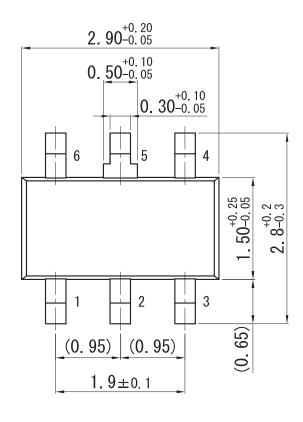


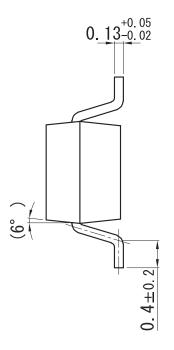


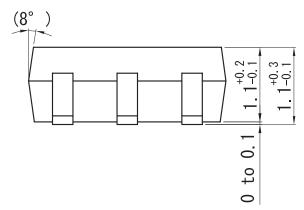
Ver. CED 2

Mini6-G4-B

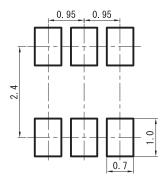
Unit: mm







#### ■ Land Pattern (Reference) (Unit: mm)



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