DMC205C0

Silicon NPN epitaxial planar type

For low frequency amplification

■ Features

- \bullet High forward current transfer ratio h_{FE} with excellent linearity
- \bullet Low collector-emitter saturation voltage $V_{\text{CE(sat)}}$
- Halogen-free / RoHS compliant
 (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

■ Marking Symbol: D6

■ Basic Part Number

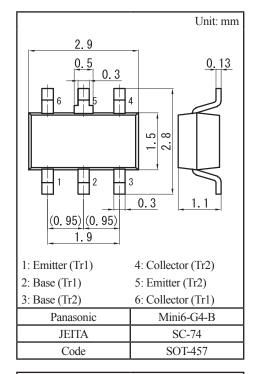
Dual DSC2C01 (Individual)

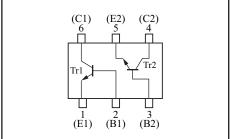
Packaging

DMC205C00R Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

■ Absolute Maximum Ratings $T_a = 25$ °C

| | Parameter | Symbol | Rating | Unit |
|------------|---------------------------------------|-----------------------------|-------------|------|
| Tr1 Tr2 | Collector-base voltage (Emitter open) | V _{CBO} | 100 | V |
| | Collector-emitter voltage (Base open) | V _{CEO} | 100 | V |
| | Emitter-base voltage (Collector open) | V _{EBO} | 15 | V |
| | Collector current | I_{C} | 20 | mA |
| | Peak collector current | I_{CP} | 50 | mA |
| Overall | Total power dissipation | P _T | 300 | mW |
| | Junction temperature | T _j | 150 | °C |
| | Operating ambient temperature | T _{opr} -40 to +85 | | °C |
| | Storage temperature | T _{stg} | -55 to +150 | °C |

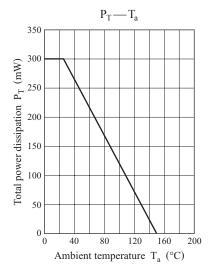


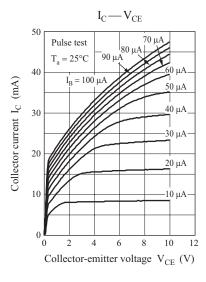


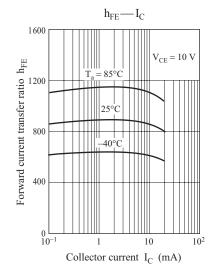
■ Electrical Characteristics $T_a = 25$ °C±3°C

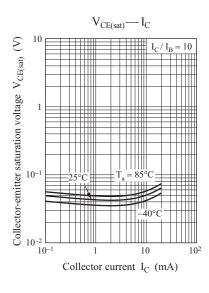
| a | | | | | | | | |
|--|----------------------|---|-----|------|------|------|--|--|
| Parameter | Symbol | Conditions | Min | Тур | Max | Unit | | |
| Collector-base voltage (Emitter open) | V_{CBO} | $I_C = 10 \mu A, I_E = 0$ | 100 | | | V | | |
| Collector-emitter voltage (Base open) | V _{CEO} | $I_C = 1 \text{ mA}, I_B = 0$ | 100 | | | V | | |
| Emitter-base voltage (Collector open) | V_{EBO} | $I_E = 10 \mu A, I_C = 0$ | 15 | | | V | | |
| Collector-base cutoff current (Emitter open) | I _{CBO} | $V_{CB} = 60 \text{ V}, I_{E} = 0$ | | | 0.1 | μΑ | | |
| Collector-emitter cutoff current (Base open) | I _{CEO} | $V_{CE} = 60 \text{ V}, I_{B} = 0$ | | | 1 | μΑ | | |
| Forward current transfer ratio | h _{FE} | $V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$ | 400 | | 1200 | _ | | |
| Collector-emitter saturation voltage | V _{CE(sat)} | $I_C = 10 \text{ mA}, I_B = 1 \text{ mA}$ | | 0.05 | 0.20 | V | | |
| Transition frequency | f_T | $V_{CE} = 10 \text{ V}, I_{C} = 2 \text{ mA}$ | | 140 | | MHz | | |

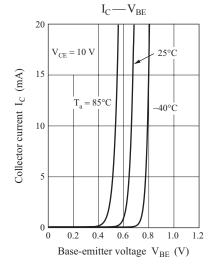
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

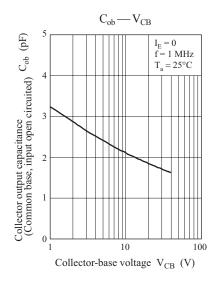


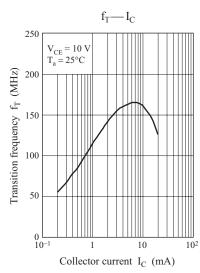








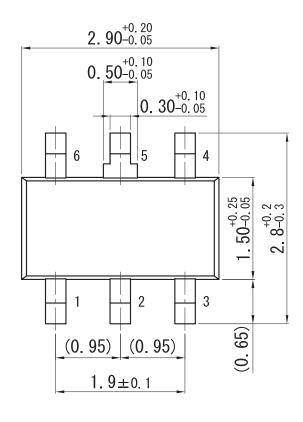


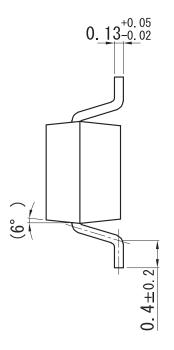


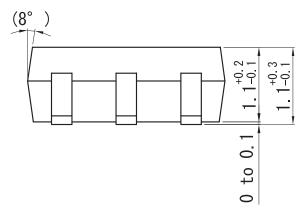
Ver. CED 2

Mini6-G4-B

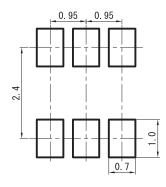
Unit: mm







■ Land Pattern (Reference) (Unit: mm)



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